



Product/Process Change Notice - PCN 11_0177 Rev. -

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This notice is to inform you of a change that will be made to certain ADI products (see Material Report). Any issues with this PCN or requirements to qualify the change (additional data or samples) must be sent to ADI within 30 days of publication date. ADI contact information is listed below.

PCN Title: Datasheet Timing Characteristic Updates for AD5722/32/52 , AD5724/34/54, AD5722R,32R,52R, AD5724R/34R/54R

Publication Date: 17-Aug-2011

Effectivity Date: 17-Aug-2011 *(the earliest date that a customer could expect to receive changed material)*

Revision Description:

Datasheet Revision

Description Of Change

Timing Characteristics Table (Reference Table 3 in datasheets):

Change t7 from 5nS to 7nS
Change t8 from 0nS to 2nS
Change t10 from 20nS to 130nS

Reason For Change

Datasheet update to accurately reflect device capability.

Impact of the change (positive or negative) on fit, form, function & reliability

Main impact is extending the delay between /SYNC rising edge edge to /LDAC falling edge (t10) from 20nS to 130nS.
Small increases for data hold time t8 and data setup time t7.

Product Identification *(this section will describe how to identify the changed material)*

Product is not changing - datasheet update only.

Summary of Supporting Information

The above specification changes will be reflected in datasheets:
AD5722/32/52 Rev D
AD5722R/32R/52R Rev D
AD5724/34/54 Rev D
AD5724R/34R/54R Rev E

Comments

Main change is extending the delay between /SYNC rising edge edge to /LDAC falling edge (t10) from 20nS to 130nS.
Small increases for data hold time t8 and data setup time t7.

Supporting Documents None

For questions on this PCN, send email to the regional contacts below or contact your local ADI sales representative

Americas: PCN_Americas@analog.com

Europe: PCN_Europe@analog.com

Japan: PCN_Japan@analog.com

Rest of Asia: PCN_ROA@analog.com

Appendix A - Affected ADI Models**Added Parts On This Revision - Product Family / Model Number (19)**

AD5734R / AD5734RBREZ-U2	AD5752 / AD5752AREZ	AD5752 / AD5752AREZ-REEL7	AD5752 / AD5752AREZ-U4
AD5752 / AD5752BREZ-U4	AD5752R / AD5752RAREZ-U3	AD5752R / AD5752RBREZ	AD5752R / AD5752RBREZ-REEL7
AD5752R / AD5752RBREZ-U2	AD5752R / AD5752RBREZ-U3	AD5752R / AD5752RBREZ-U4	AD5754 / AD5754AREZ
AD5754 / AD5754AREZ-REEL7	AD5754 / AD5754AREZ-U4	AD5754 / AD5754BREZ	AD5754 / AD5754BREZ-REEL7
AD5754R / AD5754RAREZ-U2	AD5754R / AD5754RBREZ	AD5754R / AD5754RBREZ-REEL7	

Appendix B - Revision History

Rev	Publish Date	Rev Description
Rev. -	17-Aug-2011	Datasheet Revision

Analog Devices, Inc.

DocId:1621 Parent DocId:None Layout Rev.5