



Product/Process Change Notice - PCN 11_0088 Rev. -

Analog Devices, Inc. Three Technology Way Norwood, Massachusetts 02062-9106

This notice is to inform you of a change that will be made to certain ADI products (see Material Report). Any issues with this PCN or requirements to qualify the change (additional data or samples) must be sent to ADI within 30 days of publication date. ADI contact information is listed below.

PCN Title: Top and Bottom Mark Standardization for Certain LCC (Leadless Chip Carrier) and Metal Can Packages

Publication Date: 01-Jun-2011

Effectivity Date: 30-Aug-2011 *(the earliest date that a customer could expect to receive changed material)*

Revision Description:

Initial Release

Description Of Change

Change for LCC and Metal Can top & bottom marking orientation with respect to Pin 1. In the new format, the marking is rotated 90 degrees relative to Pin 1 as compared to the existing format. See attachment.

Marking (top/bottom) will be marked per the attached illustration on this PCN.

Reason For Change

To standardize top and bottom marking for LCC and Metal Can packages with all other ADI other military grade models.

Impact of the change (positive or negative) on fit, form, function & reliability

This change affects the form (marking orientation).

Product Identification *(this section will describe how to identify the changed material)*

The earliest date code of the changed product is Date Code 1131.

Summary of Supporting Information

No re-qual necessary as the marking process has not changed, only the orientation.

Supporting Documents

Attachment 1: Type: Marking Comparison

ADI_PCN_11_0088_Rev_-_LCC_MC_TOP_BOTTOM BRAND STANDARDIZATION.pdf

For questions on this PCN, send email to the regional contacts below or contact your local ADI sales representative

Americas:	PCN_Americas@analog.com	Europe:	PCN_Europe@analog.com	Japan:	PCN_Japan@analog.com
				Rest of Asia:	PCN_ROA@analog.com

Appendix A - Affected ADI Models

Added Parts On This Revision - Product Family / Model Number (84)

AD524S / 5962-8853901V2A	AD524S / 5962R8853901V2A	AD549 / AD5490001LH	AD704 / AD704SE/883B	AD8184S / AD81840002RC	AD8351S / 5962R0821801V2A
AD8351S / AD8351ARC-EMX	AD9283S / AD9283-703RC	AD9283S / AD92830099RC	AMP01S / 5962-8863001V3A	AMP01S / 5962-8863003V3A	AMP03 / AMP03BJZ
AMP03 / AMP03FJZ	DAC08 / 5962-89932012A	DAC08 / DAC08RC/883C	DAC08S / 5962R8993201V2A	DAC08S / 5962R8993202V2A	DAC8412 / 5962-9176402M3A
DAC8412 / DAC8412BTC/883C	DAC8413 / 5962-9176404M3A	DAC8413 / DAC8413BTC/883C	MAT01 / MAT01AH	MAT01 / MAT01AHZ	MAT01 / MAT01GH
MAT01 / MAT01GHZ	MAT02S / MAT020903H	MAT03 / MAT03EH	MAT03 / MAT03EHZ	MAT03 / MAT03FH	MAT03 / MAT03FHZ
MAT12 / MAT12AHZ	MUX08 / 5962-87716022A	OP07S / JM38510/13501BGA	OP07S / JM38510/13501SGA	OP07S / JM38510/13502BGA	OP08S / OP080051J
OP11 / 5962-89801012A	OP200 / 5962-8859301M2A	OP200S / 5962-8859301V2A	OP215S / 5962R8853804V2A	OP249 / 5962-9151901M2A	OP27 / OP27AJ/883C
OP27 / OP27GJZ	OP270S / 5962-8872101V2A	OP270S / 5962R8872101V2A	OP27S / 5962R9468002V2A	OP27S / JM38510/13503BGA	OP27S / JM38510/13503SGA
OP400 / 5962-8777101M3A	OP400S / 5962-8777101V3A	OP42 / 5962-88513012A	OP42 / 5962-8851301GA	OP42 / OP42AJ	OP42 / OP42ARC/883C
OP42 / OP42EJ	OP42 / OP42FJ	OP467 / OP467ARC/883C	OP467S / 5962-9325801V2A	OP470 / 5962-88565012A	OP470S / 5962R8856501V2A
OP471 / 5962-88565022A	OP471 / 5962-88565023A	OP497 / 5962-9452101M2A	OP77 / 5962-87738012A	OP77 / 5962-8773802GA	OP77 / OP77FJZ
OP77S / 5962-8773802VGA	PM139S / 5962-8773901V2A	PM139S / 5962R8773901V2A	PM155S / JM38510/11404SGA	PM156S / JM38510/11405BGA	PM156S / JM38510/11405SGA
REF01 / REF01AJ/883C	REF01 / REF01CJ	REF01S / 5962-8958101V2A	REF02 / 85514012A	REF02 / REF02AJ/883C	REF02 / REF02RC/883C
REF05S / 5962R0051601VGA	REF43S / REF430077RC	REF43S / REF430803J	REF43S / REF430803RC	SW06 / 5962-89669012A	TMP01 / TMP01FJ

Appendix B - Revision History

Rev	Publish Date	Rev Description
Rev. -	01-Jun-2011	Initial Release

Analog Devices, Inc.

DocId:1517 Parent DocId:None Layout Rev:4