

PCN 11_0034 Specification Changes

Parameter	FROM	TO	Unit	Description
t ₅	7	5	ns min	\overline{CS} high to SCLK high setup
t ₆	15	5	ns min	SCLK high to \overline{CS} low hold time
t ₇	10	5	ns min	SCLK high to \overline{CS} high hold time
t ₈	7	10	ns min	Data setup time
t ₉	5	4	ns min	Data hold time ($V_{INH} = 90\%$ of V_{DD} , $V_{INL} = 10\%$ of V_{DD})
t ₁₀	15	20	ns min	\overline{LDAC} pulsewidth
t ₁₁	15	10	ns min	\overline{CS} high to \overline{LDAC} low setup

