



Product/Process Change Notice - PCN 10_0289 Rev. A

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This notice is to inform you of a change that will be made to certain ADI products (see Material Report). Any issues with this PCN or requirements to qualify the change (additional data or samples) must be sent to ADI within 30 days of publication date. ADI contact information is listed below.

Note: Revised fields are indicated by a red field name. See Appendix B for revision history.

PCN Title: Datasheet changes for ADSP-BF51x/ BF51xF Products

Publication Date: 18-Mar-2011

Effectivity Date: 18-Mar-2011 *(the earliest date that a customer could expect to receive changed material)*

Revision Description:

Correcting information contained in original PCN.

The following changes were incorrectly listed on the original (Rev-) PCN, but are not required and are not included in the Rev B datasheet.

6) Revised parameter "VddMEM" to "VddEXT" in Asynchronous Read Cycle Timing (Pg#29).

7) Revised parameter "VddMEM" to "VddEXT" in SDRAM Interface Timing (Pg#31).

Description Of Change

The datasheet is being revised as follows:

1) VIH specification revised from 1.1V to 1.2V for VDDEXT=1.9V (Pg#20)

2) tMDCIH min specification revised from 11.5ns to 0ns (Pg#48)

3) tMDCOH min specification revised from -1.0ns to -1.25ns (Pg#48)

4) ppi-tHFSPE specification revised from 1.5ns to 1.75ns (Pg#33)

5) gpf-tGPOD specification for VddEXT=2.5V/3.3V, revised from 8.2ns to 8.5ns (Pg#43)

8) Revised parameter "VddMEM" to "VddEXT" in Parallel Peripheral Interface Timing (Pg#33)

9) Revised parameter "VddMEM" to "VddEXT" in Serial Ports Timing (Pg#37)

10) Corrected multiple package dimensions incorrectly represented in earlier datasheet revision (Pg#64)

11) Revised tWH, tWL, tOH specifications for RSI Controller Timing Standard and High-speed modes (Table 31, Pg#35 & Table 32, Pg#36)

12) Revised fVCO specification in Phase-Locked Loop Operating Conditions; from 70 to 72MHz (Pg#21)

13) Added the tPSUD specification in Parallel Peripheral Interface Timing (Pg#33)

Reason For Change

Revising datasheet to more accurately reflect device capabilities. There is no change to the BF51x/ BF51xF product design, electrical testing or physical package.

Impact of the change (positive or negative) on fit, form, function & reliability

No change to fit, form, function or reliability.

Summary of Supporting Information

Changes are reflected in ADSP-BF51x/ BF51xF, Revision B Product Datasheet (attached).

An electrical characteristic comparison has been performed to ensure the validity of revised specifications. Supporting data will be made available upon request.

Supporting Documents

Attachment 1: Type: Revised Datasheet Specification

ADI_PCN_10_0289_Rev_A_ADSP-BF51x_BF51xF datasheet Rev.B.pdf

For questions on this PCN, send email to the regional contacts below or contact your local ADI sales representative

Appendix A - Affected ADI Models

Existing Parts - Product Family / Model Number (41)

ADSP-BF512 / ADSP-BF512BBCZ-3	ADSP-BF512 / ADSP-BF512BBCZ-4	ADSP-BF512 / ADSP-BF512BSWZ-3	ADSP-BF512 / ADSP-BF512BSWZ-4	ADSP-BF512 / ADSP-BF512KBCZ-3	ADSP-BF512 / ADSP-BF512KBCZ-4
ADSP-BF512 / ADSP-BF512KSWZ-3	ADSP-BF512 / ADSP-BF512KSWZ-4	ADSP-BF512F / ADSP-BF512BBCZ-4F4	ADSP-BF512F / ADSP-BF512BSWZ-4F4	ADSP-BF512F / ADSP-BF512KBCZ-4F4	ADSP-BF512F / ADSP-BF512KSWZ-4F4
ADSP-BF514 / AD91169Z	ADSP-BF514 / ADSP-BF514BBCZ-3	ADSP-BF514 / ADSP-BF514BBCZ-4	ADSP-BF514 / ADSP-BF514BSWZ-3	ADSP-BF514 / ADSP-BF514BSWZ-4	ADSP-BF514 / ADSP-BF514KBCZ-3
ADSP-BF514 / ADSP-BF514KBCZ-4	ADSP-BF514 / ADSP-BF514KSWZ-3	ADSP-BF514 / ADSP-BF514KSWZ-4	ADSP-BF514F / ADSP-BF514BBCZ-4F4	ADSP-BF514F / ADSP-BF514BSWZ-4F4	ADSP-BF514F / ADSP-BF514KBCZ-4F4
ADSP-BF514F / ADSP-BF514KSWZ-4F4	ADSP-BF516 / ADSP-BF516BBCZ-3	ADSP-BF516 / ADSP-BF516BBCZ-4	ADSP-BF516 / ADSP-BF516BSWZ-3	ADSP-BF516 / ADSP-BF516BSWZ-4	ADSP-BF516 / ADSP-BF516KBCZ-3
ADSP-BF516 / ADSP-BF516KBCZ-4	ADSP-BF516 / ADSP-BF516KSWZ-3	ADSP-BF516 / ADSP-BF516KSWZ-4	ADSP-BF516F / ADSP-BF516BBCZ-4F4	ADSP-BF516F / ADSP-BF516BSWZ-4F4	ADSP-BF516F / ADSP-BF516KBCZ-4F4
ADSP-BF516F / ADSP-BF516KSWZ-4F4	ADSP-BF518 / ADSP-BF518BBCZ-4	ADSP-BF518 / ADSP-BF518BSWZ-4	ADSP-BF518F / ADSP-BF518BBCZ-4F4	ADSP-BF518F / ADSP-BF518BSWZ-4F4	

Appendix B - Revision History

Rev	Publish Date	Rev Description
Rev. -	24-Jan-2011	Initial Release
Rev. A	18-Mar-2011	Correcting information contained in original PCN. The following changes were incorrectly listed on the original (Rev-) PCN, but are not required and are not included in the Rev B datasheet. 6) Revised parameter "VddMEM" to "VddEXT" in Asynchronous Read Cycle Timing (Pg#29). 7) Revised parameter "VddMEM" to "VddEXT" in SDRAM Interface Timing (Pg#31).