

**QUALIFICATION PLAN / STATUS AD5541**

TEST	CONDITIONS	SAMPLE SIZE	RESULTS
High Temperature Operating Life (HTOL)*	<b>JEDEC JESD22-A108</b>	<b>77</b>	<b>In-process Completed 500hrs Expected completion of 1000hrs 10<sup>th</sup> Jan 2010</b>
Solder Heat Resistance (SHR)*	<b>ADI-0049</b>	<b>3 x 77</b>	<b>Pass</b>
Latch-Up	<b>JEDEC Standard 78</b>	<b>6</b>	<b>Pass</b>
Electrostatic Discharge <i>Human Body Model</i>	<b>ESD Association STM5.1- 2007</b>	<b>27</b>	<b>Pass 4000V</b>
Electrostatic Discharge <i>Field-Induced Charged Device Model</i>	<b>ESD Association STM5.3.1- 1999</b>	<b>18</b>	<b>Pass 1500V</b>