

QUALIFICATION PLAN / STATUS			
TEST	CONDITIONS	SAMPLE SIZE	RESULTS
Latch-Up	JEDEC Standard 78	6	Scheduled to complete Aug. 2009
Electrostatic Discharge <i>Human Body Model</i>	ESD Association STM5.1-2007	N/A	Scheduled to complete Aug. 2009
Electrostatic Discharge <i>Field-Induced Charged Device Model</i>	ESD Association STM5.3.1-1999	N/A	Scheduled to complete Aug. 2009

QUALIFICATION RESULTS			
Fab Site	Process Technology	HTOL	ELFR
TSMC Fab 3	0.25 μ m CMOS	Pass	Pass
TOTAL:		0/1389	<100 ppm