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<td>DOCUMENT TITLE:</td>
<td>PACKAGE OUTLINE, 8L, 14L, 16L SOIC .150</td>
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<td>CHANGE NUMBER:</td>
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<td>ORIGINATOR:</td>
<td>Bernard Go</td>
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**REASON FOR CHANGE:**
Add S8MS+24A
NOTES:

1. ALL DIMENSIONS ARE IN MILLIMETERS UNLESS OTHERWISE SPECIFIED.
2. MATERIAL MUST COMPLY WITH BANNED AND RESTRICTED SUBSTANCES SPEC # 10-0131.
3. DIMENSIONS D AND E DO NOT INCLUDE MOLD PROTRUSION.
   ALLOWABLE MOLD PROTRUSION IS 0.15 MM (0.006") PER SIDE.
4. LEADS TO BE COPLANAR WITHIN 0.10MM (0.004").
5. MEETS JEDEC MS012
6. ALL DIMENSIONS APPLY TO BOTH LEADED (−) AND PbFREE (+) PKG. CODES.
### REVISION HISTORY

<table>
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<tr>
<th>REV</th>
<th>CHANGES MADE</th>
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<tr>
<td>A</td>
<td>ECN #C4214. INITIAL RELEASE INTO DOCUMENT CONTROL</td>
<td>8/10/93</td>
<td>BY</td>
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<td>B</td>
<td>ECN #HQ-02-5840. DRAWING UPDATED WITH DALLAS SEMICONDUCTOR LOGO AND INFORMATION.</td>
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<td>ECN #HQ-10-0025. CHANGED TO THE NEW BORDER/TEMPLATE. ADDED CORRESPONDING PACKAGE CODES. REARRANGED DIMENSIONS</td>
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<td>CN 1025295 Pin 1 ID is Optional</td>
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<td>CN 1031351. ADD PKGCODE S8MS-23</td>
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