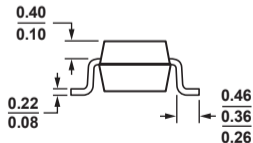
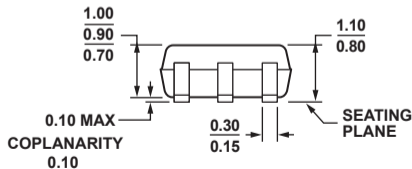
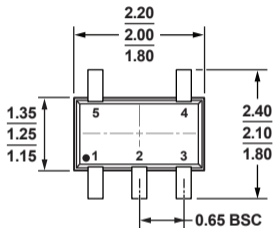


**5-Lead Thin Shrink Small Outline Transistor Package [SC70]
(KS-5)**
Dimensions shown in millimeters



COMPLIANT TO JEDEC STANDARDS MO-203-AA