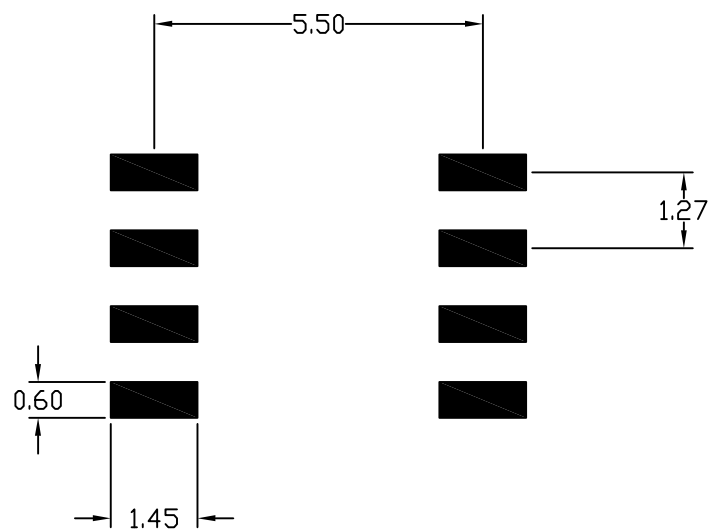




NOTES:

1. REFERENCE PKG. OUTLINE: 21-0702
2. LAND PATTERN COMPLIES TO: IPC7351A.
3. TOLERANCE:  $\pm 0.02$  MM.
4. ALL DIMENSIONS APPLY TO BOTH LEADED (-) AND PbFREE (+) PKG. CODES.
5. ALL DIMENSIONS IN MM.

—DRAWING NOT TO SCALE—



This document (including dimensions, notes & specs) is a recommendation based on typical circuit board manufacturing parameters. Since land pattern design depend on many factors unknown to Maxim (eg. user's board manufacturing specs), user must determine suitability for use. This document is subject to change without notice. Contact technical support at <http://www.maxim-ic.com/support> for further questions.

|                                                             |                                 |           |     |
|-------------------------------------------------------------|---------------------------------|-----------|-----|
| TITLE:<br>PACKAGE LAND PATTERN,<br>[S8-17 / S8+17] (N) SOIC |                                 |           |     |
| APPROVAL                                                    | DOCUMENT CONTROL NO.<br>90-0437 | REV.<br>A | 1/1 |