

PKG. CODES

[S8+2]
 [S8+2C]
 [S8+4]
 [S8+4C]
 [S8+5]
 [S8M+5]
 [S8+6F]
 [S8+7F]
 [S8+8F]
 [S8+10F]
 [S8+11F]
 [S8+16F]
 [S8MK+1]
 [S8+19F]
 [S8+20]
 [S8+21]
 [S8MS+22]
 [S8+22]
 [S8+23]
 [S8MS+24]
 [S8MS+24A]

NOTES:

1. REFERENCE PKG. OUTLINE: 21-0041.
2. LAND PATTERN COMPLIES TO: IPC7351A.
3. TOLERANCE: +/- 0.02 MM.
4. ALL DIMENSIONS APPLY TO BOTH LEADED (-) AND PbFREE (+) PKG. CODES.
5. ALL DIMENSIONS ARE IN MM.

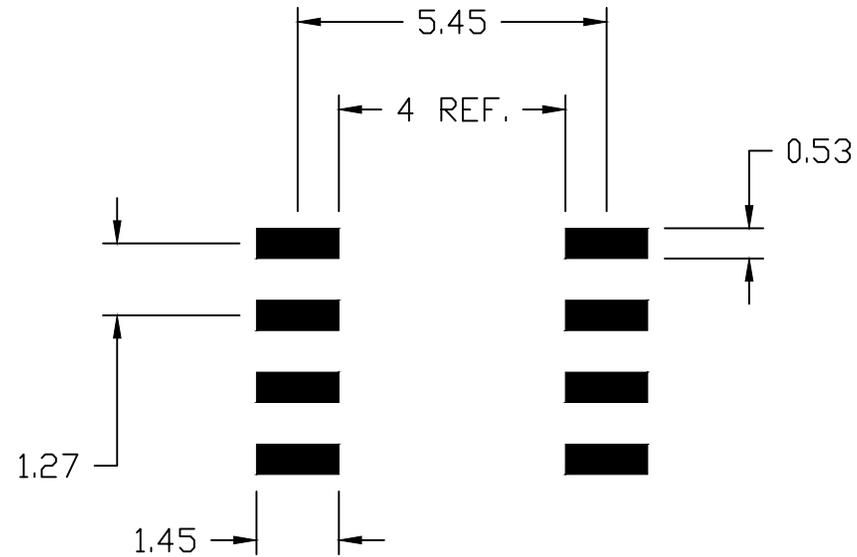
-DRAWING NOT TO SCALE-



This document (including dimensions, notes & specs) is a recommendation based on typical circuit board manufacturing parameters. Since land pattern design depends on many factors unknown to Analog Devices Inc. (eg. user's board manufacturing specs), user must determine suitability for use. This document is subject to change without notice. Contact technical support at <http://www.maxim-ic.com/support> for further questions.

TITLE: PACKAGE LAND PATTERN, [S8] 0.150" SOIC, 8 LEADS			
APPROVAL ALIREZA REZVANI	DOCUMENT CONTROL NO. 90-0096	REV. M	1/2

OPTION FOR IMPROVED HV ISOLATION
 (CLEARANCE/CREEPAGE OF 4MM)



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APPROVAL ALIREZA REZVANI	DOCUMENT CONTROL NO. 90-0096	REV. M	2/2

REVISION HISTORY

REV	CHANGES MADE	DATE	INIT.
A	ECN #DS-09-0118 Initial release	04/08/09	GP
B	ECN #DS-09-0563 S8-13/S8+13 PKG. CODE REMOVED FROM TABLE. PKGCODE HAS BEEN OBSOLETEED.	09/30/09	GP
C	ECN #DS-11-2284 S8MK-1 / +1 PKG. CODE ADDED	07/25/11	GP
D	ECN# DS-13-0970 - Add new package code S8+19F.	10/15/13	AG
E	ECN #DS-14-0949 ADDED PKG CODES S8+20 & S8+21	10/03/14	MM
F	CN# 1001171 - PKGCODES LIST UPDATED.	07/24/15	GP
G	CN# 1015365 REV. G ADDED S8+22 TO PKG CODE TABLE	05/05/17	BG
H	CN # 1019496 REV. H ADDED S8+2C TO PKG CODE TABLE	08/16/17	BG
I	CN 107044. Add pkg code S8-23, remove "+" pkg codes	10/17/19	BG
J	CN 1104528. Add S8MS+24	10/27/20	BG
K	CN 1112420. Add S8+4C	01/13/21	BG
L	CN 1115313. Added Page 2 for Improved HV Isolation Option.	02/08/21	BG
M	CN 1189265. Added S8MS+24A	06/20/23	BG

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