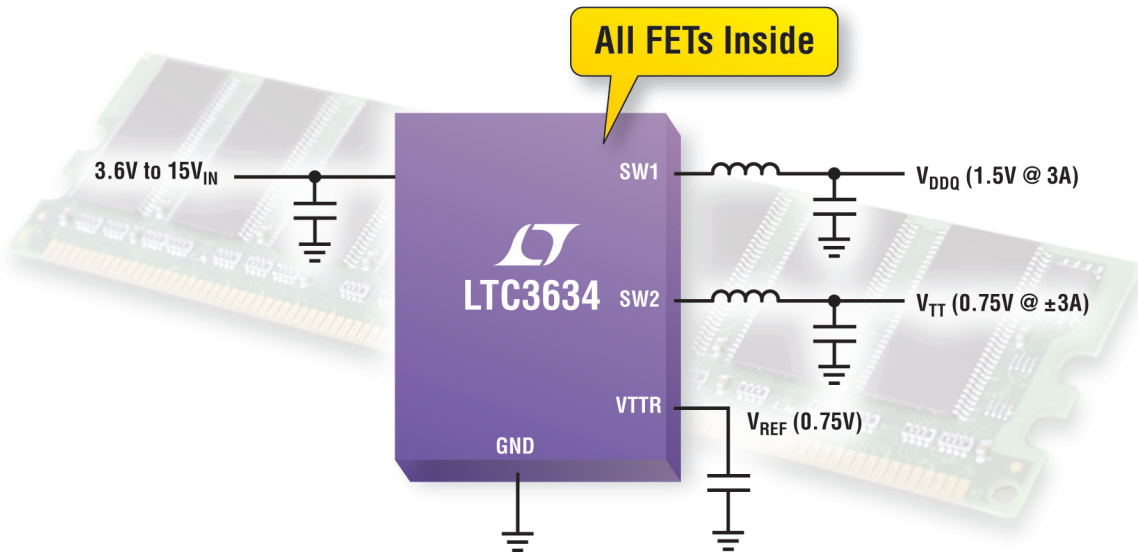


DDRx-We Have You Powered



Monolithic & Controller Solutions Provide Minimal Power Loss & Compact Footprints

The LTC[®]3634 is just one of our growing family of synchronous step-down regulators providing power supply and bus termination rails for double data rate (DDR 1, 2, 3 & 4) SDRAM controllers. The family features 90%+ power conversion efficiencies, minimizing power loss and simplifying thermal design. Operating input voltages can be as high as 38V, enabling operation from 5V, 12V and 24V system rails and various battery-powered systems. The V_{TT} regulated output voltage is equal to $\frac{1}{2} V_{DDQIN}$. An on-chip buffer is capable of driving up to 10mA, providing a low noise reference output $V_{TTR} = \frac{1}{2} V_{DDQIN}$.

▼ DDR Power Solutions

Part Number	V_{IN} Range (V)	I_{OUT}	V_{TTR} Current (mA)	Min V_{TT} (V)
LTC3413	2.25 to 5.5	±3A	-	1.1
LTC3617	2.25 to 5.5	±6A	±10	0.5
LTC3618	2.25 to 5.5	$V_{TT} \pm 3A$ & $V_{DD} + 3A$ or $V_{TT} \pm 6A$	±10	0.5
LTC3634	3.6 to 15	$V_{TT} \pm 3A$ & $V_{DD} + 3A$ or $V_{TT} \pm 6A$	±10	0.5
LTC3876	4.5 to 38	$2 \times \pm 20A^*$	±50	0.5

*Depends on external MOSFETs.

▼ Info & Free Samples

www.linear.com/products/regulators
1-800-4-LINEAR



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