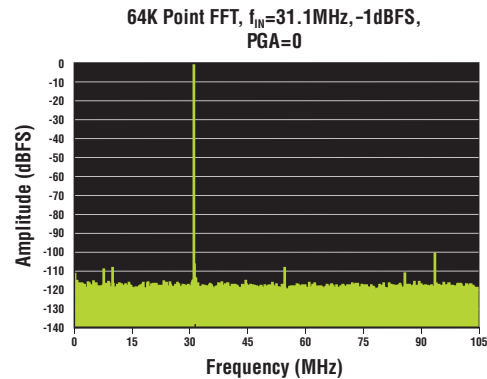
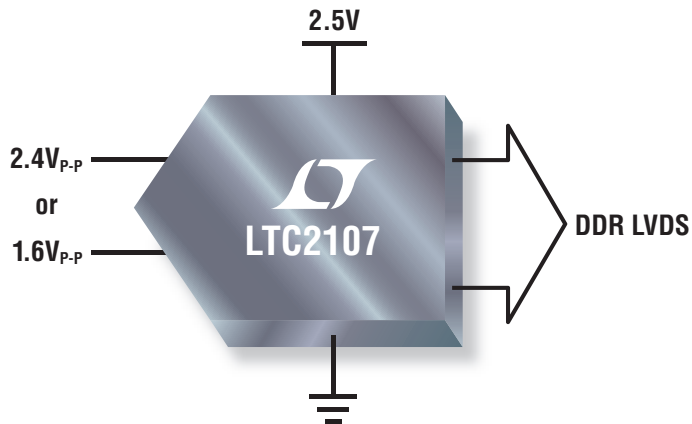


16-Bit, 210Msps ADC

100dB SFDR



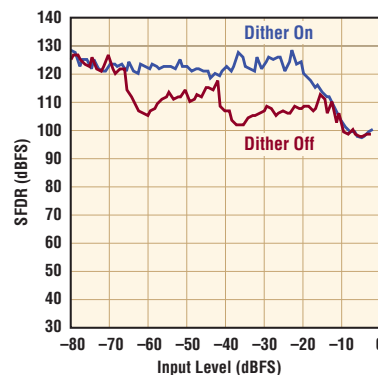
LTC[®]2107 Achieves 80dB SNR from Single 2.5V Supply

Linear Technology now offers the highest performance 16-bit 210Msps ADC, delivering exceptional dynamic range performance for the most demanding wideband, low noise, signal acquisition applications. The internal transparent dither circuit dramatically improves low level input SFDR, while the digital output randomizer eliminates digital coupling between the ADC outputs and analog input.

▼ Features

- 16 Bits, 210Msps, 1280mW
- 80dBFS SNR
- 100dBFS SFDR
- Internal Transparent Dither
- Digital Output Randomizer
- PGA Front End, 2.4V_{p-p} or 1.6V_{p-p} Input Ranges
- CMOS or DDR/LVDS Outputs
- Single 2.5V Supply
- Clock Duty Cycle Stabilizer
- 7mm x 7mm QFN Package

SFDR vs Input Level, f_{IN}=30.6MHz, PGA=0



▼ Info & Free Samples

www.linear.com/product/LTC2107

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