



Summer 2013

## New Evaluation Boards

### Standard Half-Duplex RS-485 Evaluation Board

EVAL-RS485HDEBZ Features:

- Easy evaluation of half-duplex RS-485 transceivers in 8-lead/14-lead SOIC packages
- Screw terminal blocks for power/ground, logic I/O and RS-485 signals
- Jumper selectable enable/disable for RE and DE
- Test points for measuring all signals
- Resistors and footprints for termination and biasing networks

>>Visit the EVAL-RS485HDEBZ product page [now](#).

### Standard Full-Duplex (8-Lead SOIC) RS-485 Evaluation Board

EVAL-RS485FD8EBZ Features:

- Easy evaluation of full-duplex RS-485 transceivers with 8-lead SOIC footprint
- Screw terminal blocks for power/ground, logic I/O and RS-485 signals
- Test points for measuring all signals
- Resistors and footprints for termination and biasing networks

>>Visit the EVAL-RS485FD8EBZ product page [now](#).

### Standard Full-Duplex (14-Lead SOIC) RS-485 Evaluation Board

EVAL-RS485FDEBZ Features:

- Easy evaluation of full-duplex RS-485 transceivers with 14-lead SOIC footprint
- Screw terminal blocks for power/ground, logic I/O, and RS-485 signals
- Jumper selectable enable/disable for RE and DE
- Test points for measuring all signals
- Resistors and footprints for termination and biasing networks

>>Visit the EVAL-RS485FDEBZ product page [now](#).

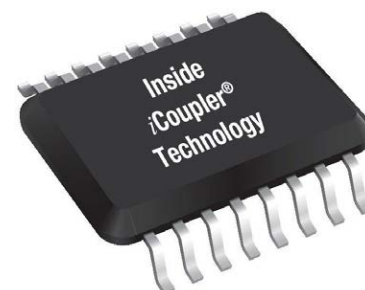
A promotional graphic for a free webcast. It features a blue background with a white circuit board pattern. The text "Free Webcast" is at the top in white. Below it, the question "Got my data over the isolation barrier! Now how do I get power to run it?" is written in white. A red "Register Now" button is centered. At the bottom, the Analog Devices logo and a stylized waveform logo are visible.

## NAppkin Note



**NAppkin Notes:** Written expressly for the Interface and Isolation newsletter. This quarter we bring you " **Using Digital Isolators as Level Shifters in Non-Isolated Applications.**"

[Read Now!](#)



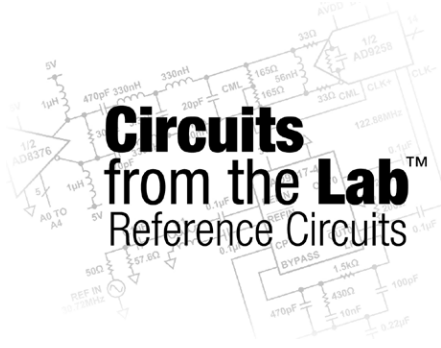
Digital Isolators Deliver Automotive Grade Quality & Reliability.

[Read technical article now!](#)

An advertisement for the EngineerZone support community. It features a man in an orange shirt and glasses looking thoughtful. The text reads: "Engineers are Asking 'Why does my I<sup>2</sup>C output look strange?'" and "Visit the Interface and Isolation EngineerZone Support Community for the Answer". The EngineerZone logo is in the top right.

## Technical Items of Interest

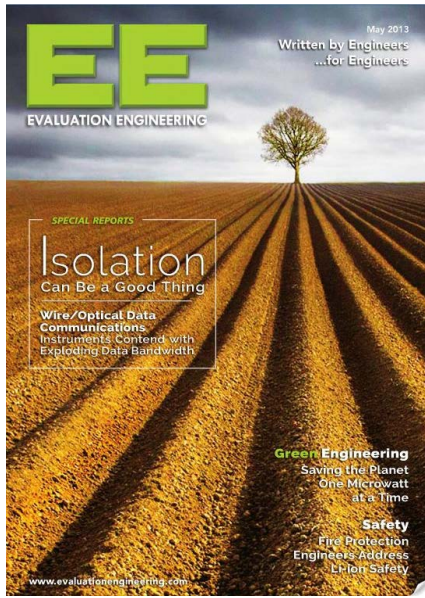
- [Application Note: LVDS and M-LVDS Circuit Implementation Guide](#)



- [Circuits from the Lab: Fully Isolated, Single Channel Voltage and 4mA to 20mA Output with HART](#)
- [Circuits from the Lab: Complete 4mA to 20mA Loop Powered Field Instrument with HART Interface](#)
  - Check out the video too:



**Evaluation Engineering  
Highlight:  
Isolation Can Be a Good Thing.**



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