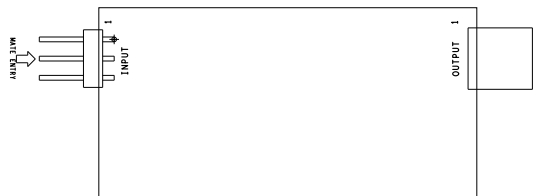


REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	18DEC18	J.VIERNES
B	AS PER ECR-086427	03MAY19	J.VIERNES

ASSEMBLY NOTES:

1.
- BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).
2.
- REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.
3.
- RoHS COMPLIANCE: ASSEMBLY VENDOR SHOULD ASSURE COMPLIANCE WITH LEAD-FREE AND RoHS PCB ASSEMBLY STANDARDS (EU RoHS DIRECTIVE 2002/95/EC).



VIEW FROM PRIMARY SIDE

EVALUATION BOARD

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES DECIMALS   FRACTIONS   ANGLES .XX   -.010   --1/32   -- 2 .XXX   -.005 .XXXX   -.0050		APPROVAL	DATE	<div><div></div><div>ANALOG DEVICES</div></div> <div>WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887</div>			
		TEMPLATE ENGINEER N/A					
		HARDWARE SERVICES E.CONDA	18DEC18				
		HARDWARE SYSTEMS N/A		TITLE ASSEMBLY SCP THRU BOARD EVALZ			
MATERIAL	TEST ENGINEER N/A			SIZE C			
	COMPONENT ENGINEER A.CAUILAN-AGENA	18DEC18					
	TEST PROCESS N/A						
	HARDWARE RELEASE K.JABATAN						
FINISH	DESIGNER J.PINEDA	181EC18		FSCM NO 24355	DRAWING NUMBER 01-051912	REV B	SHEET 1 OF 1
	PTD ENGINEER J.VIERNES	18DEC18					
	CHECKER N/A						
DO NOT SCALE DWG				SCALE	1 / 1		