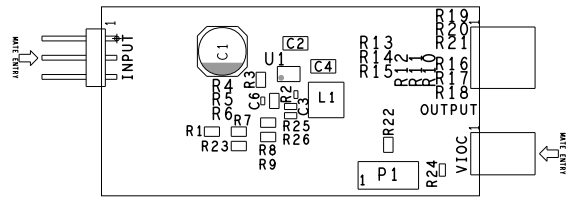


REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	26NOV18	J VIERNES
B	AS PER ECR-086388	07MAY19	J VIERNES
C	AS PER ECR-090283	31OCT19	J VIERNES

ASSEMBLY NOTES:


- 1. BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).
- 2. REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.
- 3. REPAIRS ARE NOT ALLOWED IN SOLDERMASK FREE AREAS ON EITHER SIDE OF THE BOARD.
- 4. RoHS COMPLIANCE: ASSEMBLY VENDOR SHOULD ASSURE COMPLIANCE WITH LEAD-FREE AND RoHS PCB ASSEMBLY STANDARDS (EU RoHS DIRECTIVE 2002/95/EC).
- 5. INSTALL SOLDER BLOB USING THE TABLE BELOW (SEE DETAILS A) AND PLACE MARK ON TICK BOX IN THE PRIMARY SIDE



BOM	PARTS	TICK BOX
05-051162-01-b	R4, R5, R6, R10, R11, R12, R19, R20 AND R21	BUCK
05-051162-02-b	R13, R14, R15, R16, R17, R18	INVERTER

DETAIL A
SCALE FACTOR: 1:4

PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES			APPROVAL		DATE		<div><div></div><div>ANALOG DEVICES</div></div> <div>WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887</div>			
TOLERANCES			TEMPLATE ENGINEER		N/A					
DECIMALS FRACTIONS ANGLES			HARDWARE SERVICES		N/A					
.XX -.010 --1/32 -- 2			N/A		N/A		TITLE ASSEMBLY SCP-LT3470A EVALZ			
.XXX -.005			HARDWARE SYSTEMS		N/A					
.XXXX -.0050			N/A		N/A					
MATERIAL			TEST ENGINEER J VIERNES		24NOV18					
			COMPONENT ENGINEER N/A		N/A					
			TEST PROCESS N/A		N/A					
			HARDWARE RELEASE N/A		N/A					
			N/A		N/A					
FINISH			DESIGNER L EVANGELIO		07NOV18		SIZE	FSCM NO	DRAWING NUMBER	REV
			PTD ENGINEER J VIERNES		26NOV18		C	24355	01-051162	C
			CHECKER N/A		N/A					
DO NOT SCALE DWG							SCALE 1/1		SHEET 1 OF 1	