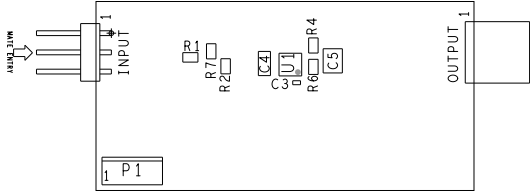


REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	06DEC18	J.VIERNES
B	AS PER ECR-086420	07MAY19	J.VIERNES

ASSEMBLY NOTES:

1.
- BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).
2.
- REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.
3.
- REPAIRS ARE NOT ALLOWED IN SOLDERMASK FREE AREAS ON EITHER SIDE OF THE BOARD.
4.
- RoHS COMPLIANCE: ASSEMBLY VENDOR SHOULD ASSURE COMPLIANCE WITH LEAD-FREE AND RoHS PCB ASSEMBLY STANDARDS (EU RoHS DIRECTIVE 2002/95/EC).



PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES			APPROVAL		DATE		<div><div><div></div><div>ANALOG DEVICES</div></div><div>WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887</div></div>								
TOLERANCES			TEMPLATE ENGINEER		N/A										
DECIMALS FRACTIONS ANGLES			HARDWARE SERVICES		N/A										
XX .-.010 --1/32 -- 2			HARDWARE SERVICES		N/A		TITLE  ASSEMBLY  SCP-LT1964 EVALZ								
XXX .-.005			HARDWARE SYSTEMS		N/A										
.XXXX .-.0050			HARDWARE SYSTEMS		N/A										
MATERIAL			TEST ENGINEER		N/A										
			N/A		N/A										
			COMPONENT ENGINEER		10OCT18										
			A. CAUILAN		10OCT18										
FINISH			TEST PROCESS		N/A		SIZE C								
			N/A		N/A					FSCM NO 24355					
			HARDWARE RELEASE		10OCT18								DRAWING NUMBER 01-051166		
			K. JABATAN		10OCT18										
DO NOT SCALE DWG			DESIGNER		10OCT18										
			J.M. PLAZA		10OCT18										
			PTD ENGINEER		10OCT18										
			J. VIERNES		10OCT18		SCALE 1 / 1								
CHECKER		10OCT18		SHEET 1 OF 2											
J. VIERNES		10OCT18													

EVALUATION BOARD