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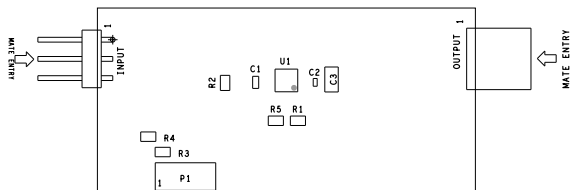
A

EVALUATION BOARD


REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	23OCT18	J.VIERNES
B	AS PER ECR-086390	07MAY19	J.VIERNES

ASSEMBLY NOTES:

- 1. BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).
- 2. REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.
- 3. RoHS COMPLIANCE: ASSEMBLY VENDOR SHOULD ASSURE COMPLIANCE WITH LEAD-FREE AND RoHS PCB ASSEMBLY STANDARDS (EU RoHS DIRECTIVE 2002/95/EC).



PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES XX -.010 +-1/32 +- 2 XXX -.005 XXXX -.0050			APPROVAL		DATE		<div><div></div><div>ANALOG DEVICES</div></div> <div>WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887</div>							
			TEMPLATE ENGINEER N/A											
			HARDWARE SERVICES E. CONDA		23OCT18									
			HARDWARE SYSTEMS N/A											
MATERIAL			TEST ENGINEER N/A				TITLE ASSEMBLY SCP-LT1962 EVALZ							
			COMPONENT ENGINEER A. CAUILAN-AGENA		23OCT18									
			TEST PROCESS N/A											
			HARDWARE RELEASE K. JABATAN		23OCT18									
FINISH			DESIGNER J. PACAMARRA		23OCT18		SIZE C							
			PTD ENGINEER J. VIERNES		23OCT18						FSCM NO 24355			
			CHECKER N/A											
											REV B			
DO NOT SCALE DWG							SCALE 1 / 1		SHEET 1 OF 1					