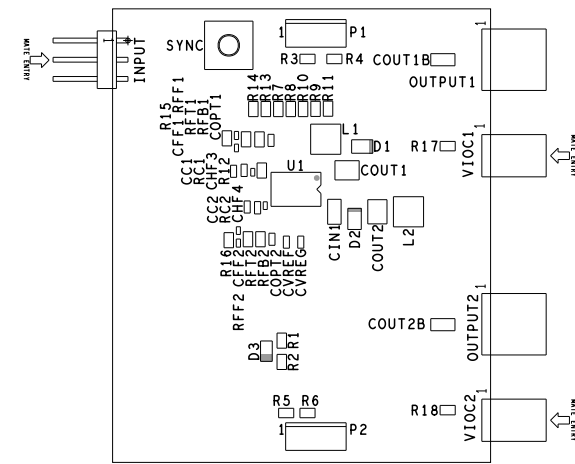


REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	03JAN19	A. SERQUINA
B	AS PER ECR-086560	07MAY19	J. VIERNES

ASSEMBLY NOTES:

1. BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).
2. REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.
3. REPAIRS ARE NOT ALLOWED IN SOLDERMASK FREE AREAS ON EITHER SIDE OF THE BOARD.
4. RoHS COMPLIANCE: ASSEMBLY VENDOR SHOULD ASSURE COMPLIANCE WITH LEAD-FREE AND RoHS PCB ASSEMBLY STANDARDS (EU RoHS DIRECTIVE 2002/95/EC).



PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES	APPROVAL		DATE		<div><div></div><div>ANALOG DEVICES</div></div> <div>WWM DIVISION 704 WOBURN STREET WILMINGTON, MA 01887</div>				
	TEMPLATE ENGINEER		-						
	TOLERANCES		-		TITLE ASSEMBLY SCP-ADP5070 EVALZ				
	DECIMALS	FRACTIONS	ANGLES	-					
	.XX --.010	--1/32	-- 2	-					
.XXX --.005			-		TITLE ASSEMBLY SCP-ADP5070 EVALZ				
.XXXX --.0050			-						
MATERIAL	TEST ENGINEER		-						
	COMPONENT ENGINEER		-						
	TEST PROCESS		-						
	HARDWARE RELEASE		-		TITLE ASSEMBLY SCP-ADP5070 EVALZ				
FINISH	DESIGNER C.BAUTISTA	03JAN19	SIZE	FSCM NO	DRAWING NUMBER			REV	
	PTD ENGINEER A.SERQUINA	03JAN19	C	24355	01-051364			B	
	CHECKER A.SERQUINA	03JAN19							
DO NOT SCALE DWG	SCALE			1/1		SHEET 1 of 1			