

SCP-ADP150 Ultralow Noise, 150 mA CMOS Linear Regulator

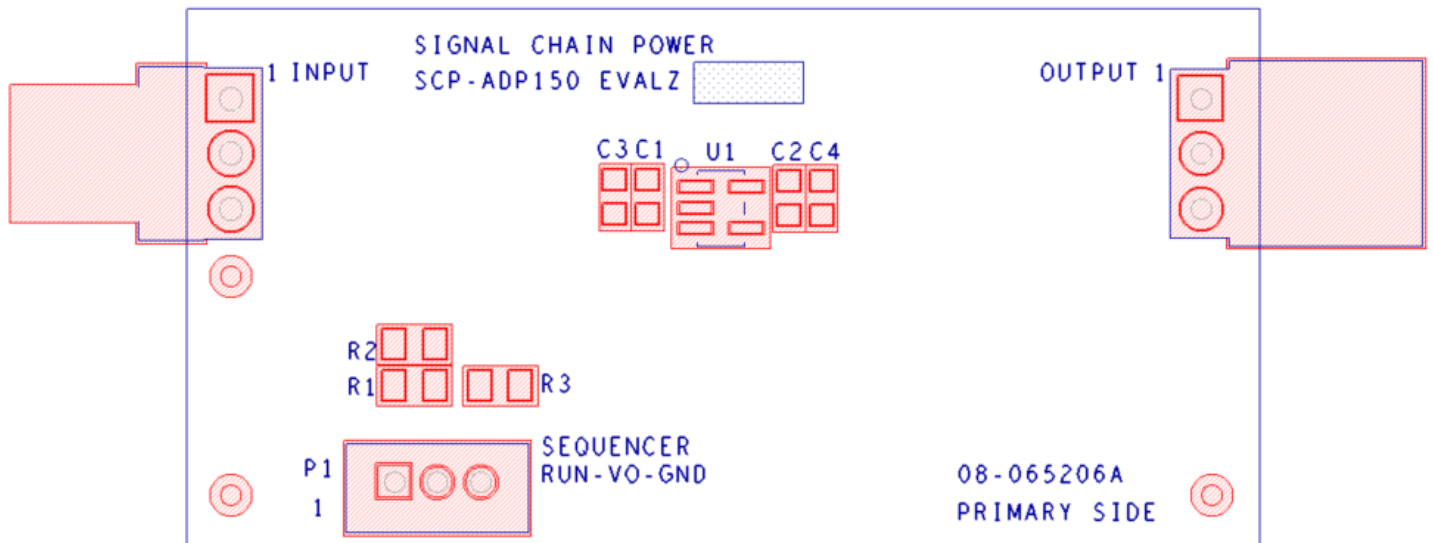


Figure 1. SCP-ADP150 Board Silkscreen, Top Layer

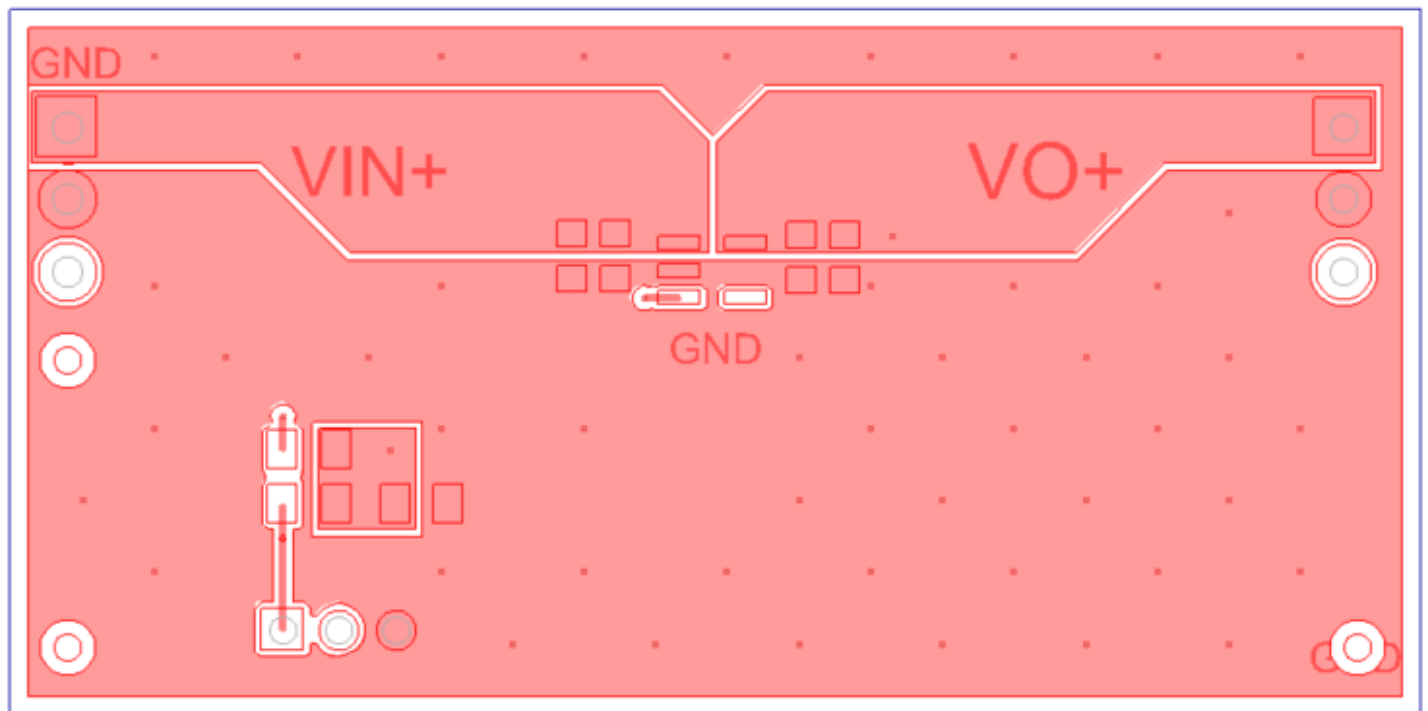


Figure 2. SCP-ADP150 Board, Top Layer

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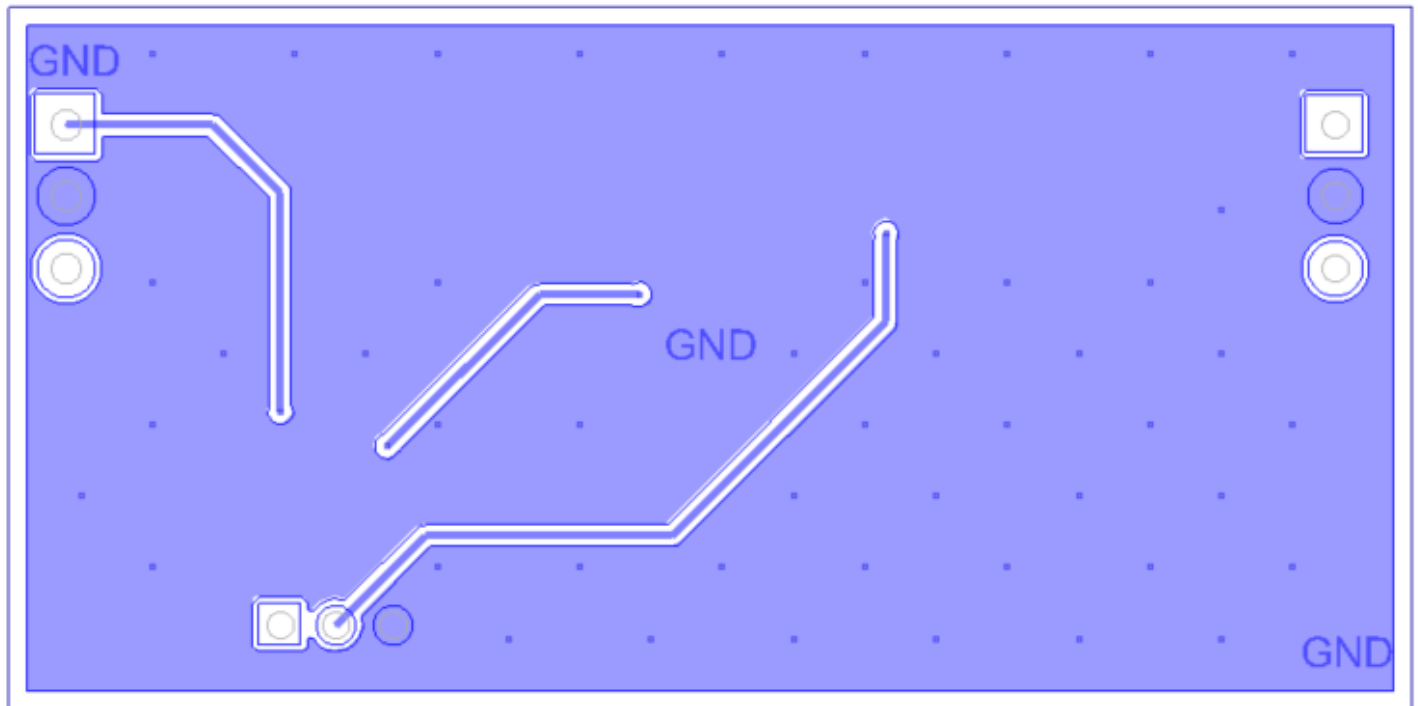


Figure 3. SCP-ADP150 Board, Bottom Layer