

8

7

6

5

4

3

2

1

THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.

RELAY CONTROL CHART

CONTROL	CODE	DEVICE	FUNCTION	CONNECTOR

JUMPER TABLE		
JP#	ON	OFF
1		
2		
3		
4		
5		

\* SEE ASSEMBLY INSTRUCTIONS

REVISIONS

REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	02DEC18	J VIERNES
B	AS PER ECR-086422	26APR19	J VIERNES

SCHEMATIC

TEMPLATE ENGINEER  
-

HARDWARE SERVICES  
-

HARDWARE SYSTEMS  
-

TEST ENGINEER  
-

COMPONENT ENGINEER  
-

TEST PROCESS  
-

HARDWARE RELEASE  
-

DESIGNER  
J PINEDA

PTD ENGINEER  
J VIERNES

CHECKER  
-

DATE

02DEC18

02DEC18

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES

TOLERANCES

DECIMALS  
X.XX +0.010  
X.XXX +0.005

FRACTIONS  
+1/32

ANGLES  
+2

MASTER PROJECT TEMPLATE  
TBD

TESTER TEMPLATE  
no\_template

DRAWING NO.  
02\_051168

REV.  
B

SIZE  
D

SCALE  
1:1

CODE ID NO.  
CodeID

SHEET 1 OF 2

8

7

6

5

4

3

2

1

SCHEMATIC

HW TYPE : SCP EVAL-Z

Product(s): 1X5 BREAK OUT BOARD

:

PACKAGE :

:

<User Define>

<User Define>

<User Define>

DESIGNER  
J PINEDA

PTD ENGINEER  
J VIERNES

CHECKER  
-

DATE

02DEC18

02DEC18

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES

TOLERANCES

DECIMALS  
X.XX +0.010  
X.XXX +0.005

FRACTIONS  
+1/32

ANGLES  
+2

MASTER PROJECT TEMPLATE  
TBD

TESTER TEMPLATE  
no\_template

DRAWING NO.  
02\_051168

REV.  
B

SIZE  
D

SCALE  
1:1

CODE ID NO.  
CodeID

SHEET 1 OF 2

8

7

6

5

4

3

2

1

SCHEMATIC

HW TYPE : SCP EVAL-Z

Product(s): 1X5 BREAK OUT BOARD

:

PACKAGE :

:

<User Define>

<User Define>

<User Define>

DESIGNER  
J PINEDA

PTD ENGINEER  
J VIERNES

CHECKER  
-

DATE

02DEC18

02DEC18

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES

TOLERANCES

DECIMALS  
X.XX +0.010  
X.XXX +0.005

FRACTIONS  
+1/32

ANGLES  
+2

MASTER PROJECT TEMPLATE  
TBD

TESTER TEMPLATE  
no\_template

DRAWING NO.  
02\_051168

REV.  
B

SIZE  
D

SCALE  
1:1

CODE ID NO.  
CodeID

SHEET 1 OF 2

8

7

6

5

4

3

2

1

SCHEMATIC

HW TYPE : SCP EVAL-Z

Product(s): 1X5 BREAK OUT BOARD

:

PACKAGE :

:

<User Define>

<User Define>

<User Define>

DESIGNER  
J PINEDA

PTD ENGINEER  
J VIERNES

CHECKER  
-

DATE

02DEC18

02DEC18

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES

TOLERANCES

DECIMALS  
X.XX +0.010  
X.XXX +0.005

FRACTIONS  
+1/32

ANGLES  
+2

MASTER PROJECT TEMPLATE  
TBD

TESTER TEMPLATE  
no\_template

DRAWING NO.  
02\_051168

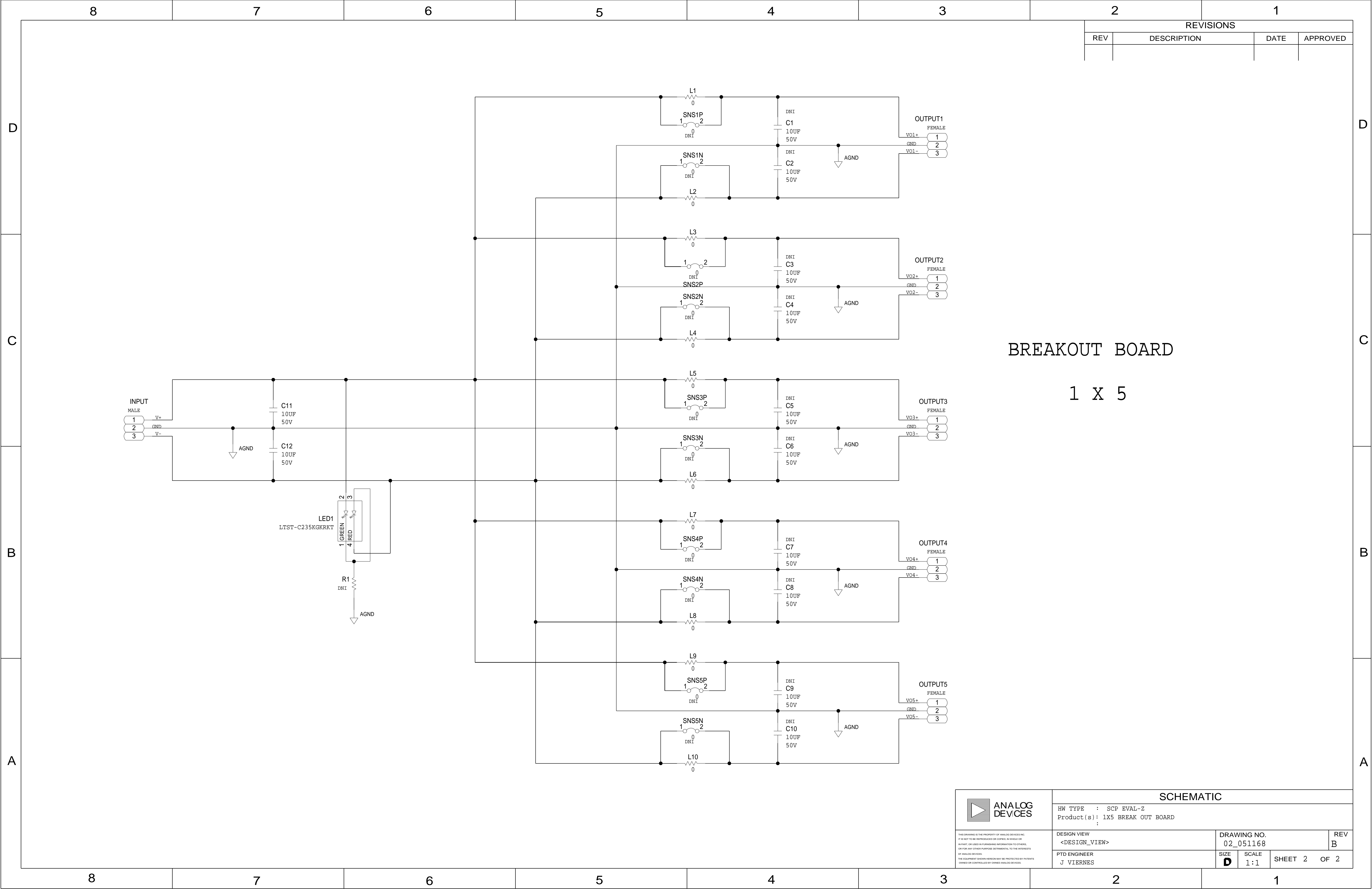
REV.  
B

SIZE  
D

SCALE  
1:1

CODE ID NO.  
CodeID

SHEET 1 OF 2



L7  
0

SNS4P  
1 2

DNI  
0

C7  
10UF  
50V

DNI  
AGND

OUTPUT4  
FEMALE

VO4+  
GND  
VO4-

1  
2  
3

L8  
0

SNS4N  
1 2

DNI  
0

C8  
10UF  
50V

DNI  
AGND

L9  
0

SNS5P  
1 2

DNI  
0

C9  
10UF  
50V

DNI  
AGND

OUTPUT5  
FEMALE

VO5+  
GND  
VO5-

1  
2  
3

L10  
0

SNS5N  
1 2

DNI  
0

C10  
10UF  
50V

DNI  
AGND

87654321

SIZE  
D

SCALE  
1:1

SHEET 2 OF 2

ANALOG  
DEVICES

THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC.  
IT IS NOT TO BE REPRODUCED OR COPIED IN WHOLE OR  
IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS,  
OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS  
OF ANALOG DEVICES.  
THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS  
OWNED OR CONTROLLED BY OWNED ANALOG DEVICES.

SCHEMATIC

HW TYPE : SCP EVAL-Z  
Product(s): 1X5 BREAK OUT BOARD

DESIGN VIEW  
<DESIGN\_VIEW>

PTD ENGINEER  
J VIERNES

DRAWING NO.  
02\_051168

REV  
B

1 X 5

BREAKOUT BOARD