

# IMPORTANT NOTES:

DO NOT CONNECT PINS 61, 62.  
R12 IS CRITICAL COMPONENT AND MUST BE 12.4K OHM, 1%, 100PPM/c.  
PIN 1 SEPARATELY FILTERED FROM OTHER VDD18 PINS.

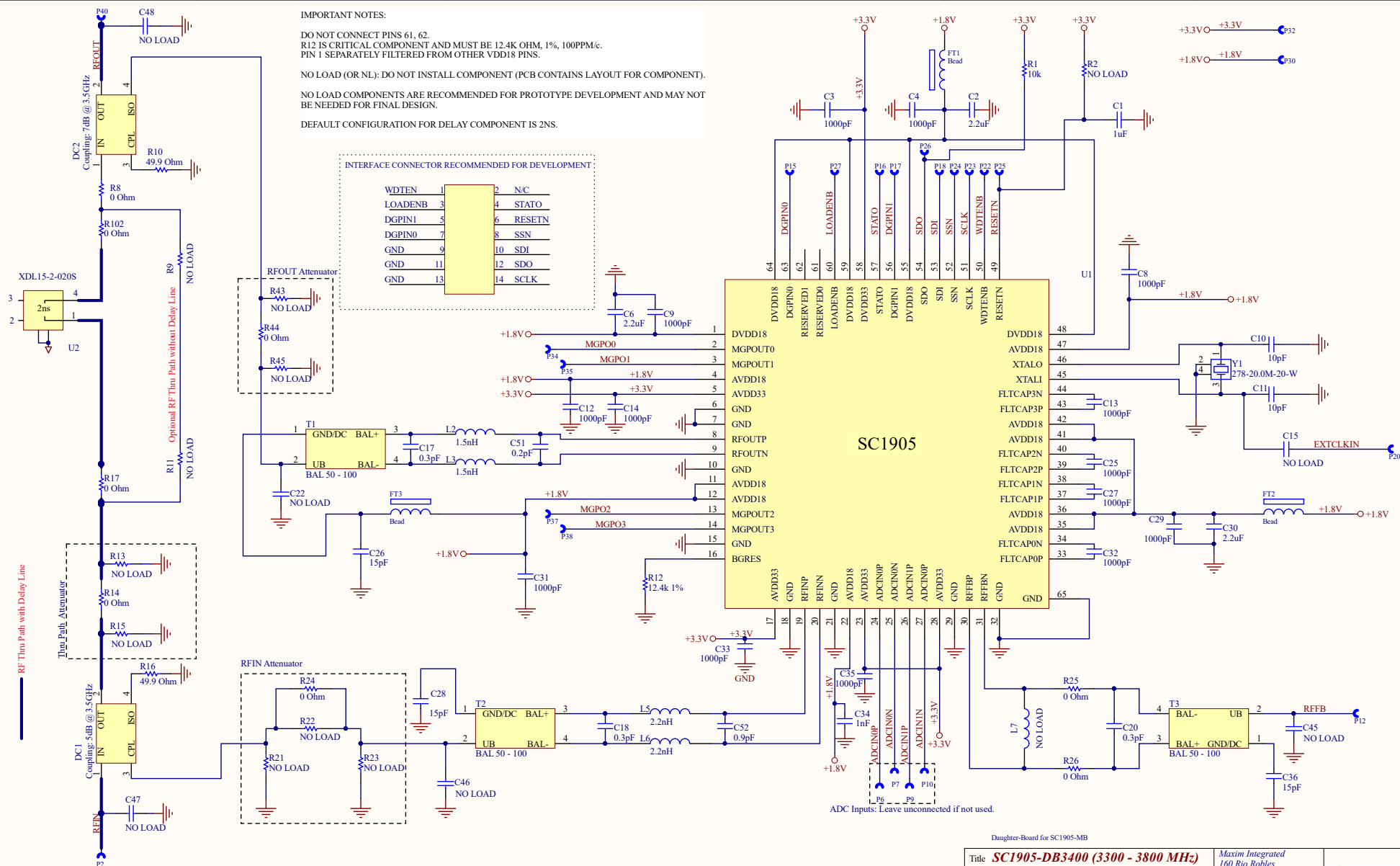
NO LOAD (OR NL): DO NOT INSTALL COMPONENT (PCB CONTAINS LAYOUT FOR COMPONENT).

NO LOAD COMPONENTS ARE RECOMMENDED FOR PROTOTYPE DEVELOPMENT AND MAY NOT BE NEEDED FOR FINAL DESIGN.

DEFAULT CONFIGURATION FOR DELAY COMPONENT IS 2NS.

## INTERFACE CONNECTOR RECOMMENDED FOR DEVELOPMENT

WDTEN	1	2	NC
LOADENB	3	4	STATO
DGPIN1	5	6	RESETN
DGPIN0	7	8	SSN
GND	9	10	SDI
GND	11	12	SDO
GND	13	14	SCLK



Daughter-Board for SC1905-MB

Title	<b>SC1905-DB3400 (3300 - 3800 MHz)</b>		Maxim Integrated 160 Rio Robles San Jose, CA. 95134 U.S.A. 408-601-1000
Size:	B	Number:*	
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