



IMPORTANT NOTES:

DO NOT CONNECT PINS 61, 62.  
R12 IS CRITICAL COMPONENT AND MUST BE 12.4K OHM, 1%, 100PPM/C.  
PIN 1 SEPARATELY FILTERED FROM OTHER VDD18 PINS.

NO LOAD (OR NL): DO NOT INSTALL COMPONENT (PCB CONTAINS LAYOUT FOR COMPONENT).  
NO LOAD COMPONENTS ARE FOR SCINTERA VALIDATION PURPOSES, NOT NEEDED FOR FINAL DESIGN.

DEFAULT CONFIGURATION FOR DELAY COMPONENT IS 2NS.

INTERFACE CONNECTOR RECOMMENDED FOR DEVELOPMENT

|         |    |    |        |
|---------|----|----|--------|
| WDTEN   | 1  | 2  | N/C    |
| LOADENB | 3  | 4  | STATO  |
| DGPIN1  | 5  | 6  | RESETN |
| DGPIN0  | 7  | 8  | SSN    |
| GND     | 9  | 10 | SDI    |
| GND     | 11 | 12 | SDO    |
| GND     | 13 | 14 | SCLK   |