

IMPORTANT NOTES:

DO NOT CONNECT PINS 61, 62.  
R12 IS CRITICAL COMPONENT AND MUST BE 12.4K OHM, 1%, 100PPM/c.  
PIN 1 SEPARATELY FILTERED FROM OTHER VDD18 PINS.

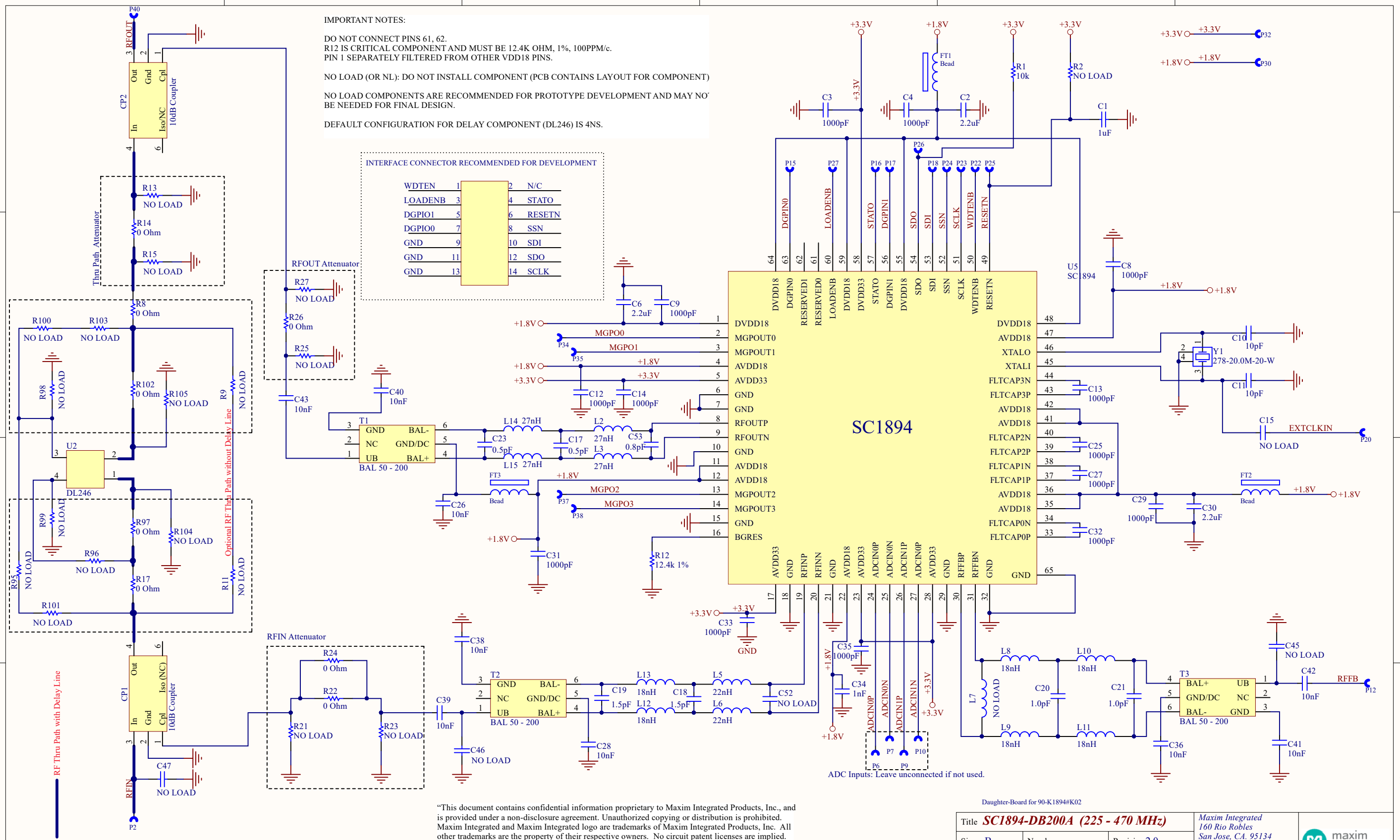
NO LOAD (OR NL): DO NOT INSTALL COMPONENT (PCB CONTAINS LAYOUT FOR COMPONENT)

NO LOAD COMPONENTS ARE RECOMMENDED FOR PROTOTYPE DEVELOPMENT AND MAY NO BE NEEDED FOR FINAL DESIGN.

DEFAULT CONFIGURATION FOR DELAY COMPONENT (DL246) IS 4NS.


INTERFACE CONNECTOR RECOMMENDED FOR DEVELOPMENT

WDTEN	1	2	N/C
LOADENB	3	4	STATO
DGPIO1	5	6	RESETN
DGPIO0	7	8	SSN
GND	9	10	SDI
GND	11	12	SDO
GND	13	14	SCLK



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Daughter-Board for 90-K1894#K02

Title <b>SC1894-DB200A (225 - 470 MHz)</b>			<i>Maxim Integrated</i> <i>160 Rio Robles</i> <i>San Jose, CA. 95134</i> <i>USA</i> <i>408-601-1000</i>	
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