

D

C

B

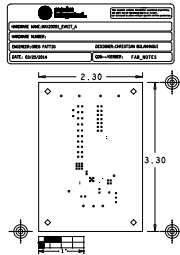
A

D

C

B

A




DRILL CHART: TOP to BOTTOM				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
+	15.0	+3.0/-3.0	PLATED	6
□	25.0	+3.0/-3.0	PLATED	60
○	28.53	+3.0/-3.0	PLATED	3
○	35.0	+0.0/-0.0	PLATED	1
○	39.37	+3.0/-3.0	PLATED	13
◇	125.0	+3.0/-3.0	PLATED	4

LAMINATION DIAGRAM				
LAYER NUMBER	LAYER NAME	FINISH WEIGHT (oz)	DIELECTRIC MATERIAL	DIELECTRIC THICKNESS (in.)
1	TOP	2	FOIL	
2	INNER2_PAD	1	TBD	B-STAGE
3	INNER3_PRR	1	TBD	CORE
4	BOTTOM	2	FOIL	B-STAGE

THE FINISHED PCB THICKNESS TO BE: 0.0625" +/- 0.010"

- NOTES:
- UNLESS OTHERWISE SPECIFIED
 - DIMENSIONS ARE IN INCHES (EXCEPT WHERE NOTED).
 - MATERIAL: (USE CHECKED ITEMS FOR MATERIAL)
 - BOARD MATERIAL:
 - () FR-4 (RHS COMPLIANT) OR EQUIVALENT
 - () ISOLA-FR400HR
 - () NELCO-4000-13 OR EQUIVALENT
 - () 370HR (RHS COMPLIANT) OR EQUIVALENT
 - () ROGERS 4350B/FR400HR
 - () ROGERS 4002C/FR400HR
 - () OTHER
 - THE PCB SHALL BE FABRICATED TO IPC-6012, TYPE X, CLASS 2. WORKMANSHIP SHALL CONFORM TO IPC-A-600, CLASS 2, CURRENT REVISIONS.
 - BOARD MATERIAL & CONSTRUCTION SHALL MEET THE REQUIREMENTS OF UL796 WITH FLAMMABILITY RATING OF 94V-0.
 - OVERALL BOARD THICKNESS REFER TO LAMINATION DIAGRAM. TOLERANCE APPLIES AFTER ALL LAMINATION AND PLATING PROCESSES. IT IS TO BE MEASURED FROM TOP PCB METAL TO BOTTOM PCB METAL UNLESS OTHERWISE SPECIFIED.
 - BOW & TWIST NOT TO EXCEED 0.0075 IN. (0.75%) PER LINEAR INCH. BOW & TWIST SHOULD BE MEASURED PER IPC-TM-650, METHOD 2.4.22.
- TOOLING: (USE CHECKED ITEMS FOR TOOLING)
- PHOTO ETCH CIRCUITRY PER ENCLOSED GERBER RST4X OR ODB++ FORMAT FILE. DRILL LOCATION AND SIZE CONTROLLED BY EXCELLON CNC DRILL FILE.
 - IF STATED IN THE LAMINATION DIAGRAM, THE DIELECTRIC THICKNESS OF ANY CONTROLLED IMPEDANCE LAYER IS FOR REFERENCE ONLY. FINAL ACCEPTANCE SHALL BE DETERMINED BY THESE LAYERS HAVING A CHARACTERISTIC IMPEDANCE OF +/-10% OHMS AS STATED IN THE LAMINATION DIAGRAM. THE VENDOR CAN MAKE ADJUSTMENTS AS LONG AS THE STATED IMPEDANCE AND OVERALL BOARD THICKNESS IS MAINTAINED. ANY ADJUSTMENT MADE TO TRACE WIDTH OR SPACING MUST HAVE PRIOR WRITTEN APPROVAL FROM MAXIM.
 - ALL TRACES FILLETED OPTION TO ENHANCE RELIABILITY AT PAD JUNCTIONS WHERE SPACING PERMITS, UNLESS OTHERWISE SPECIFIED:
 - () FILLETED
 - (X) NOT FILLETED
 - LAYER TO LAYER REGISTRATIONS SHALL BE WITHIN .003 INCHES. LEGEND TO LEGEND +/- 0.007 INCHES.
- FINISH: (USE CHECKED ITEMS FOR FINISHES)
- PLATING SPECIFICATION:
 - () STARTING AND FINISH COPPER WEIGHT FOR OUTER LAYERS TO BE (1 OZ). FOR OUTER LAYERS WHERE SPACING PREVENTS THE USE OF (1 OZ) AS A STARTING WEIGHT, THE STARTING WEIGHT CAN BE (0.5 OZ) AS LONG AS THE FINISH COPPER WEIGHT IS (1 OZ) UNLESS OTHERWISE SPECIFIED.
 - (X) STARTING COPPER WEIGHT FOR OUTER LAYERS TO BE (1 OZ), THE FINISH COPPER WEIGHT IS (2 OZ). FOR OUTER LAYERS WHERE SPACING PREVENTS THE USE OF (1 OZ) AS A STARTING WEIGHT, THE STARTING WEIGHT CAN BE (0.5 OZ) AS LONG AS THE FINISH COPPER WEIGHT IS (2 OZ). UNLESS OTHERWISE SPECIFIED.
 - () STARTING COPPER WEIGHT FOR OUTER LAYERS TO BE (2 OZ), THE FINISH COPPER WEIGHT IS (2 OZ) MINIMUM. FOR OUTER LAYERS WHERE SPACING PREVENTS THE USE OF (1 OZ) AS A STARTING WEIGHT, THE STARTING WEIGHT CAN BE (<2 OZ) AS LONG AS THE FINISH COPPER WEIGHT IS (2 OZ). UNLESS OTHERWISE SPECIFIED.
 - (X) OTHER: INNER LAYER COPPER WEIGHT TO BE 1oz.
 - CHECK ALL THAT APPLY:
 - () FINISH CONDUCTOR SURFACES: IMMERSION GOLD, 3-8 MICRO INCHES OVER 100 MICRO INCHES MINIMUM OF ELECTROLESS NICKEL.
 - (X) LEAD FREE AND RHS COMPLIANT OR EQUIVALENT LEAD FREE PLATING
 - () ELECTRODEPOSITED HARD GOLD PLATE, TYPE 1 (99.7% MIN GOLD), GRADE C (ENDOF HARDNESS 130-200), CLASS 1 (50-100 MICRO INCHES THICK) IN ACCORDANCE WITH MIL-G-45204C. GENERAL SURFACING REQUIREMENTS MUST MEET ANSI/IPC-A-600 (CURRENT REV) SECTION 4.0, CLASS 1 (50-100 MICROINCHES THICK) OVER ELECTRODEPOSITED NICKEL PLATE IN ACCORDANCE WITH ANSI/IPC-A-600, SECTION 4.0, CLASS 3 (200-600 MICROINCHES THICK).
 - () FINISH CONDUCTOR SURFACES: IMMERSION GOLD, 2-5 MICRO INCHES OVER 150-200 MICRO INCHES MINIMUM OF ELECTROLESS NICKEL.
 - () FINGERS TO BE GOLD PLATED.
 - () OTHER
 - DRILL SIZES ARE FINISHED HOLE SIZES. ALL HOLES SHALL BE LOCATED WITHIN .005 DTP. MINIMUM BARREL PLATING OF .001 IN. PLATED HOLES SHALL NOT BE ROUGH OR IRREGULAR SO AS TO HINDER PROPER SOLDER WICKING.
 - CHECK ALL THAT APPLY:
 - (X) GREEN SOLDERMASK OVER BARE COPPER/BARE GOLD (BOTH SIDES) WITH LIQUID PHOTO IMAGEABLE INK (LPI) PER ARTWORK.
 - () GREEN TAIYO PSR-4000
 - CHECK ALL THAT APPLY:
 - () APPLY SILKSCREEN TO BOTH SIDES USING A NON-CONDUCTIVE, WHITE EPOXY BASED INK PER ARTWORK.
 - (X) APPLY SILKSCREEN TO TOP SIDE USING A NON-CONDUCTIVE, WHITE EPOXY BASED INK PER ARTWORK.
 - () OTHER
 - VENDOR LOGO & DATE CODE REQUIRED IN INK ON BOTTOM SIDE ONLY. DATE CODE FORMAT MUST BE YYYY MM DD.
 - TESTING:
 - FINAL ELECTRICAL TEST TO BE PERFORMED USING PROVIDED IPC-D-356A NETLIST OR ODB++ FORMAT FILE. (REQUIRED UNLESS OTHERWISE SPECIFIED IN QUOTE)
 - THE PCB SHALL HAVE A VERIFICATION STAMP.
 - A TIME DOMAIN REFLECTOMETER REPORT FOR EACH IMPEDANCE CONTROLLED LAYER AND A CERTIFICATE OF COMPLIANCE SHALL BE PROVIDED BY VENDOR AT TIME OF SHIPMENT.
 - MISCELLANEOUS: (USE CHECKED ITEMS)
 - FOR BLIND AND BURIED VIA INFORMATION REFER TO DRILL CHART.
 - () NON-CONDUCTIVE EPOXY: FILL AND CAP ALL 0.00XX INCH DRILLED VIAS.
 - () SILVER, FILL AND CAP ALL 0.00XX INCH DRILLED VIAS.
 - FOR VIA SIZES XX.XX INCH USE NON CONDUCTIVE VIA FILL AND CAP
 - FINISHED SURFACE CONTACTS AND FILLED VIAS TO BE FREE OF ANY PITS, SCRATCHES, PROBE MARKS OR OTHER DEFORMITIES THAT COULD EFFECT THE APPEARANCE AND PERFORMANCE OF THE CONTACT SURFACE. CONTACTS ARE TO BE AS FLAT AS POSSIBLE. NOT TO EXCEED +/- 0.001" OF FLATNESS.
 - THIEVING:
 - () SUPPLIER MAY ADD THIEVING TO COMPENSATE FOR LOW COPPER DENSITY AREAS ON THIS DESIGN.
 - (X) SUPPLIER MAY ADD THIEVING TO COMPENSATE FOR LOW COPPER DENSITY AREAS ON THIS DESIGN.

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FRACTIONS XX --			DECIMALS .XX +/- .01 .XX +/- .005		TITLE: FABRICATION DWG. MAX20051 EVKIT	
MATERIAL:			DRAWN BY: CPB		DATE: 3/14	
SEE NOTES			CHECKED BY:		DATE:	
FINISH:			APPR. BY:		DATE:	
SEE NOTES			APPR. BY:		DATE:	
			NOT TO SCALE		TEMPLATE REV C	
			SHEET 1 OF 1		REV A	