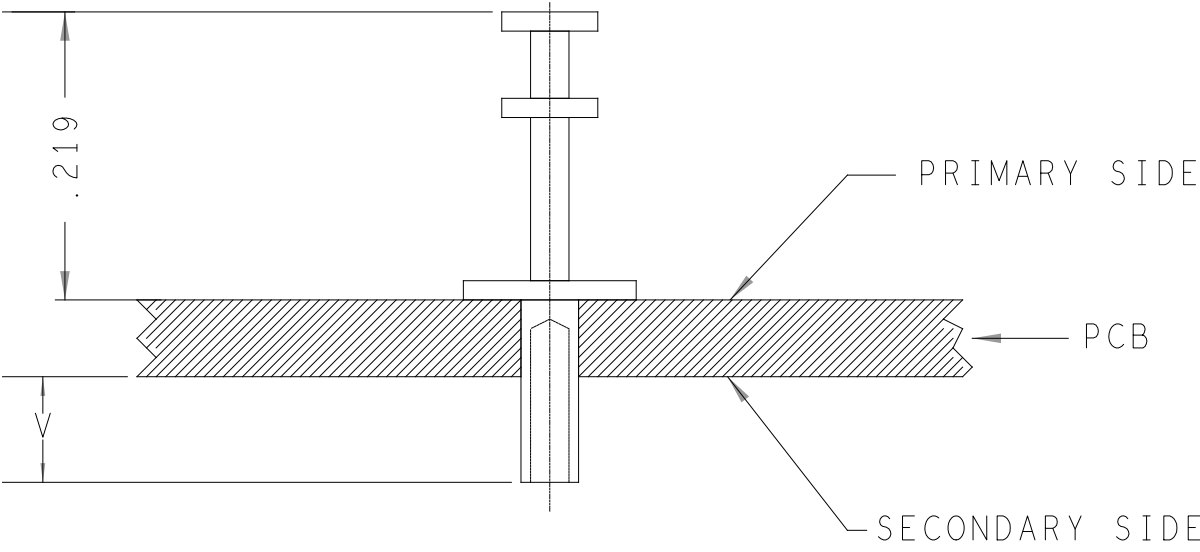
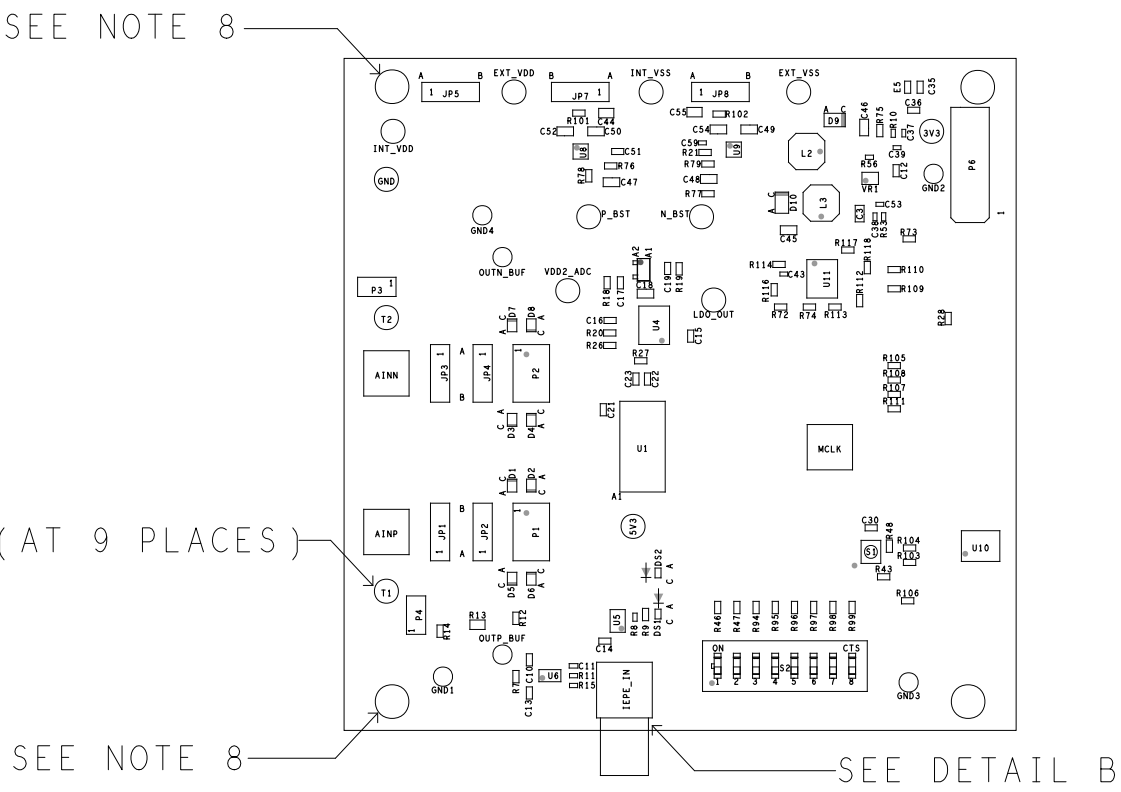
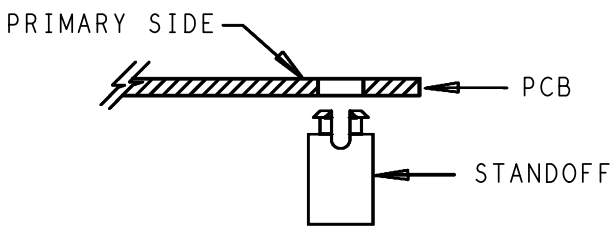
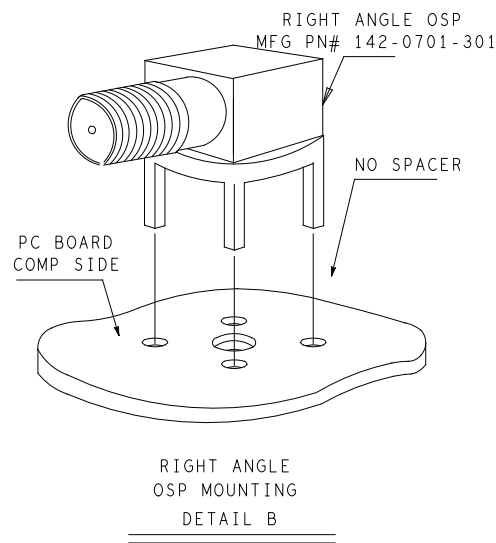


ASSEMBLY NOTES:

1. BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).
2. REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.
3. REPAIRS ARE NOT ALLOWED IN SOLDERMASK FREE AREAS ON EITHER SIDE OF THE BOARD.
4. RoHS COMPLIANCE: ASSEMBLY VENDOR SHOULD ASSURE COMPLIANCE WITH LEAD-FREE AND RoHS PCB ASSEMBLY STANDARDS (EU RoHS DIRECTIVE 2002/95/EC).
5. INSTALL JUMPER SHUNTS(M031256) ON PIN 1 & 2 FOR: JP1, JP2, JP3, JP4, JP5, JP7, JP8
INSTALL JUMPER SHUNTS(M031256) ON PIN 3 & 4 FOR: P1, P2
EXCESS JUMPERS TO BE SHIPPED WITH THE ASSEMBLED BOARD.
6. SINGULATE BOARDS AFTER ASSEMBLY PROCESS.
7. SMOOTHEN EDGES AND FREE FROM BURRS AFTER DEPANELIZATION PROCESS.
8. INSTALL STANDOFFS AS SHOWN BELOW:

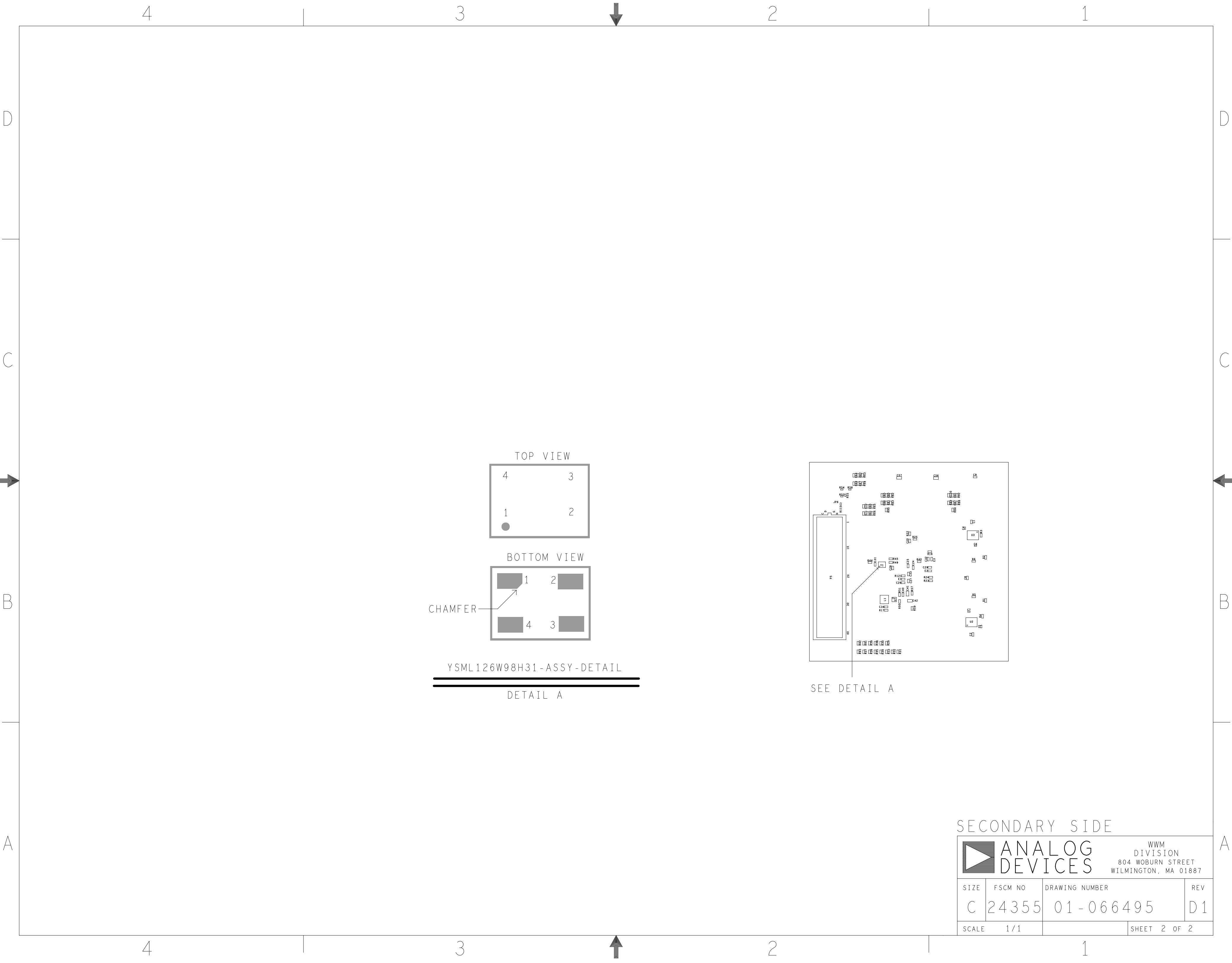


DETAIL A
NO TO SCALE



PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES .XX -.010 --1/32 -- 2 .XXX -.005 .XXXX -.0050	APPROVAL	DATE	<div><div><div></div><div>ANALOG DEVICES</div></div><div>WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887</div></div> <div>TITLE ASSEMBLY EV-ADAQ7767-1FMC1Z CUSTOMER EVAL Z</div>			
	TEMPLATE ENGINEER					
	HARDWARE SERVICES M. VALE	31MAY21				
	HARDWARE SYSTEMS					
MATERIAL	TEST ENGINEER		SIZE FSCM NO DRAWING NUMBER REV C 24355 01-066495 D1			
	COMPONENT ENGINEER N. CATALAN	12MAY23				
	TEST PROCESS					
FINISH	HARDWARE RELEASE K. JABATAN	31MAY21	SCALE 1/1 SHEET 1 OF 2			
	DESIGNER E. DELOS REYES	31MAY21				
	PTD ENGINEER M. KWOK CHECKER	31MAY21				
DO NOT SCALE DWG						



<div><div><div></div></div><div><div>ANALOG</div><div>DEVICES</div></div><div><div>WWM</div><div>DIVISION</div><div>804 WOBURN STREET</div><div>WILMINGTON, MA 01887</div></div></div>			
SIZE	FSCM NO	DRAWING NUMBER	REV
C	24355	01-066495	D1
SCALE	1 / 1	SHEET 2 OF 2	