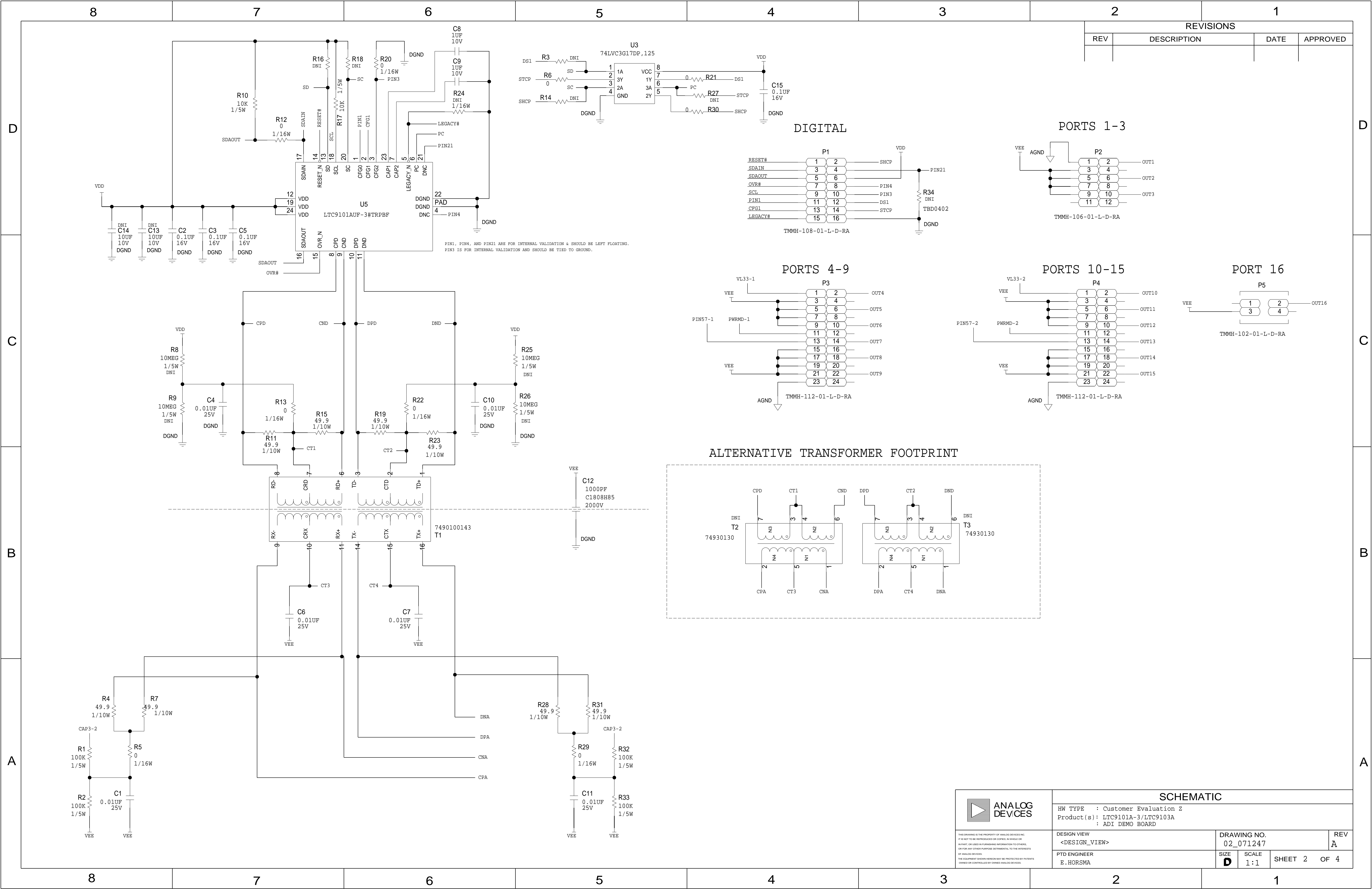
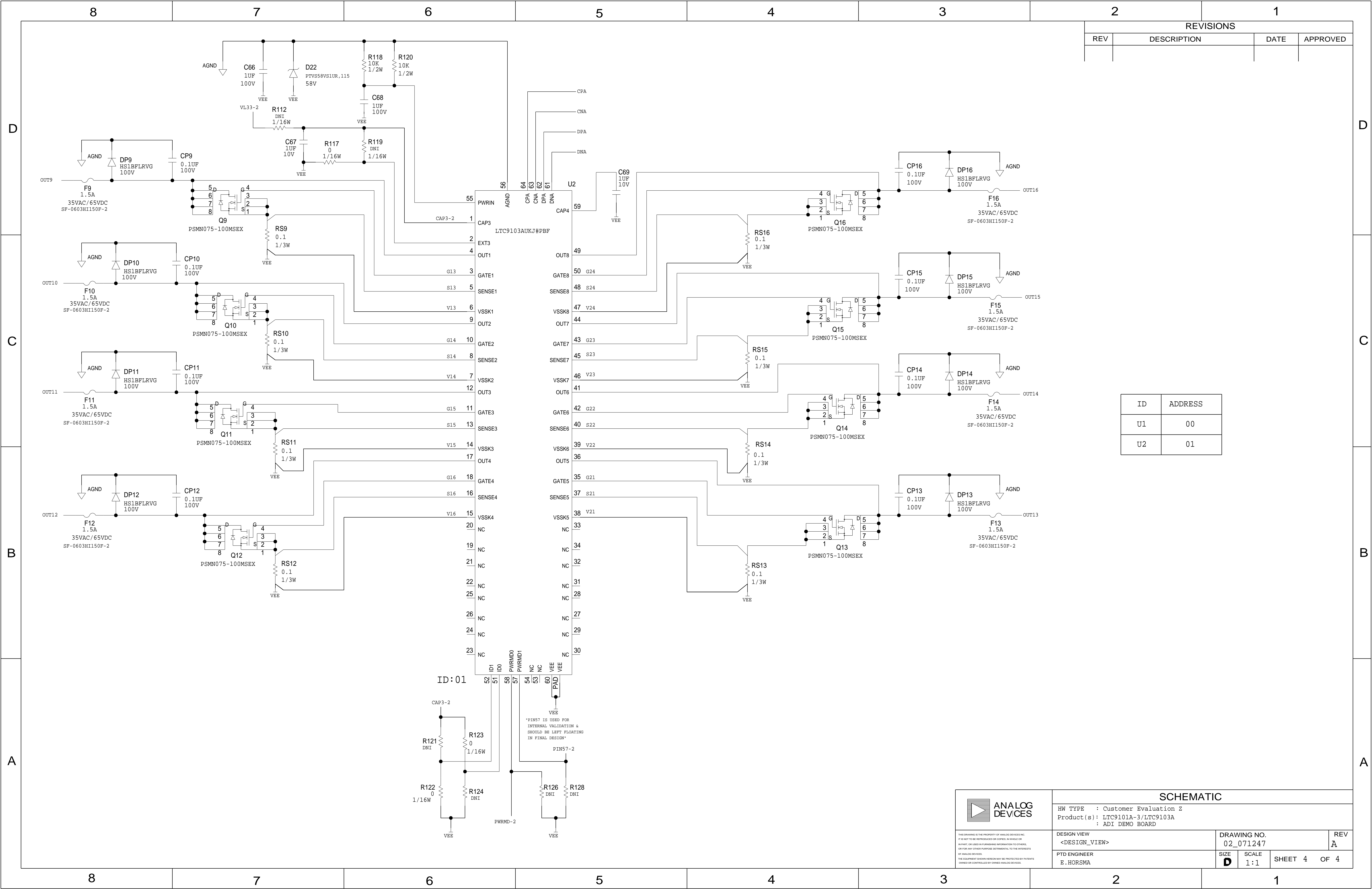


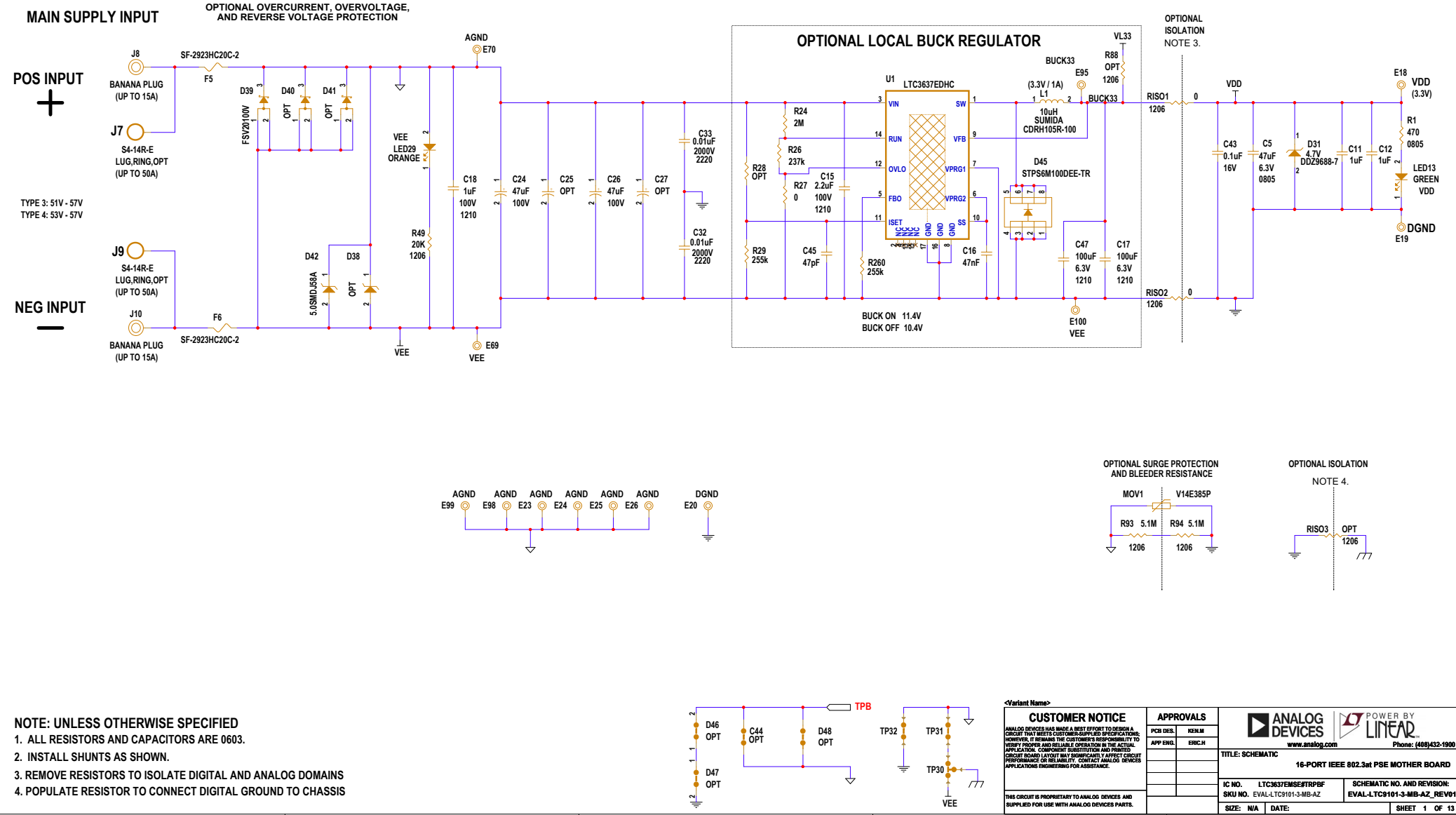
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D	THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.					JUMPER TABLE			REVISIONS												
																REV	DESCRIPTION			DATE	APPROVED
																A	INITIAL RELEASE			27APR22	E.HORSMA
	RELAY CONTROL CHART					MODEL NAME: EVAL-LTC9101-3-AZ															
	CONTROL	CODE	DEVICE	FUNCTION	CONNECTOR																
C																					
B																					
A																					
8		7		6		5		4		3		2		1							
P.O SPEC.		BK/BD SPEC.		SOCKET OEM		OEM PART#		HANDLER		SCHEMATIC						ANALOG DEVICES					
										HW TYPE : Customer Evaluation Z											
										Product(s): LTC9101A-3/LTC9103A											
										: ADI DEMO BOARD											
										PACKAGE : LeadCount-lead BodySize PackageType-family											
										: Pitch-pitch StyleVendor Style											
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										TBD		no_template		02_071247		A					
										UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES											
										DECIMALS		FRACTIONS		ANGLES							
										X.XX +0.010		+1/32		+2							
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														CodeID		OF 4					





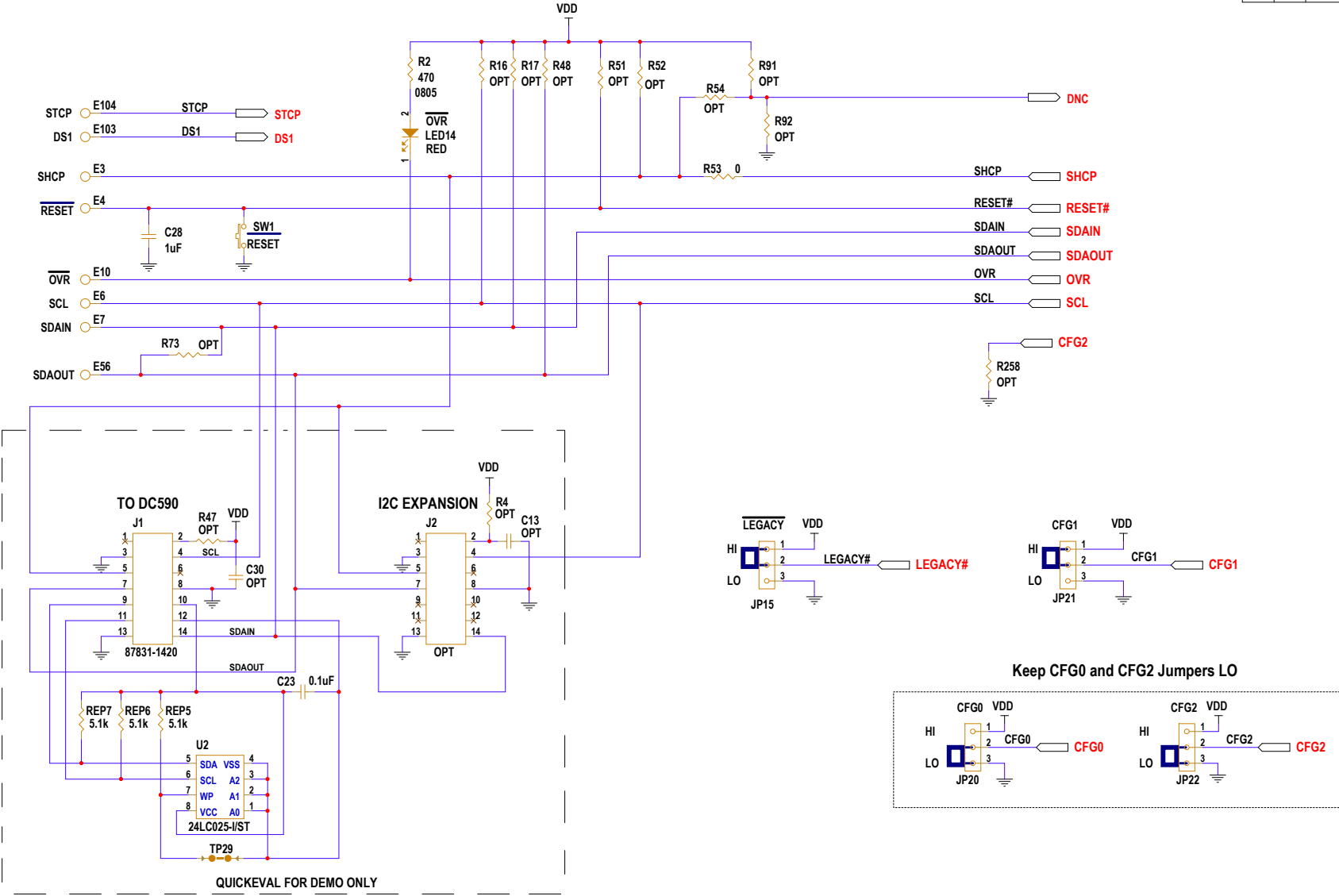
POWER SUPPLY

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
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CONFIGURATIONS

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	1			



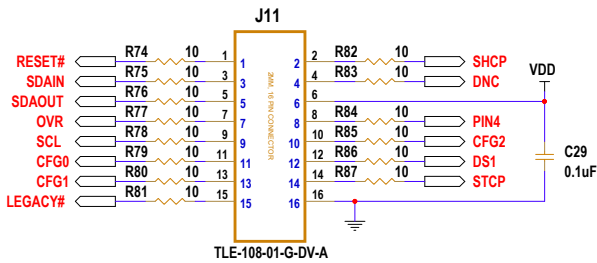
NOTE: UNLESS OTHERWISE SPECIFIED
1. ALL RESISTORS AND CAPACITORS ARE 0603.
2. INSTALL SHUNTS AS SHOWN.

CUSTOMER NOTICE		APPROVALS		 ANALOG DEVICES www.analog.com		 POWER BY LINEAR Phone: (408)432-1900	
				PCB DES.	KELJM	TITLE: SCHEMATIC 16-PORT IEEE 802.3at PSE MOTHER BOARD	
				APP ENG.	ERIC.H		
ANALOG DEVICES HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT ANALOG DEVICES APPLICATIONS ENGINEERING FOR ASSISTANCE.							
THIS CIRCUIT IS PROPRIETARY TO ANALOG DEVICES AND SUPPLIED FOR USE WITH ANALOG DEVICES PARTS.				IC NO. LTC3637EMSE#TRPBF		SCHEMATIC NO. AND REVISION:	
				SKU NO. EVAL-LTC9101-3-MB-AZ		EVAL-LTC9101-3-MB-AZ_REV01	
				SIZE: N/A		DATE:	
						SHEET 2 OF 13	

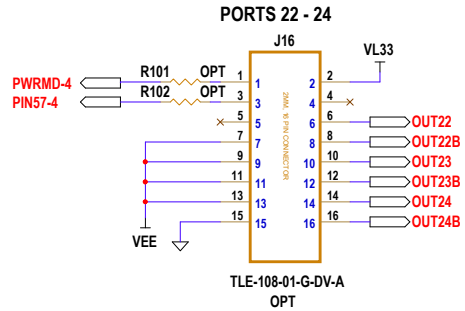
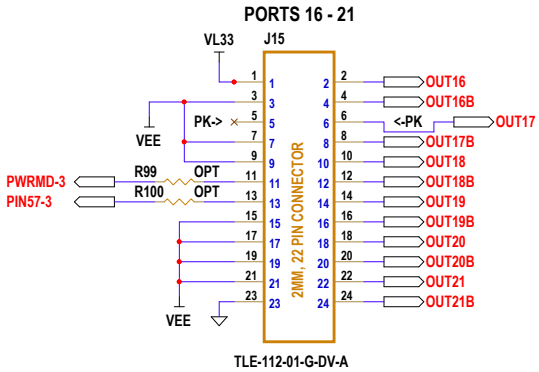
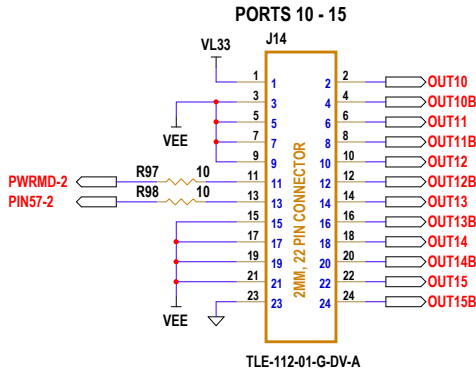
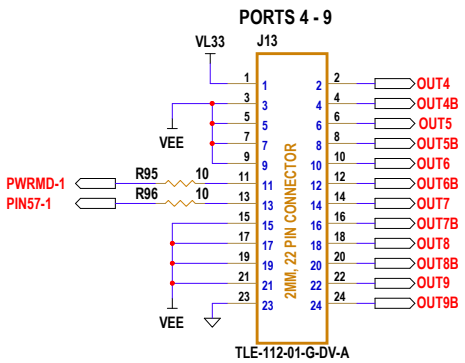
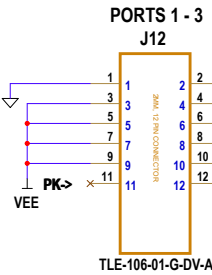
DAUGHTER CARD CONNECTIONS

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	1			



DIGITAL CONNECTIONS



ANALOG CONNECTIONS

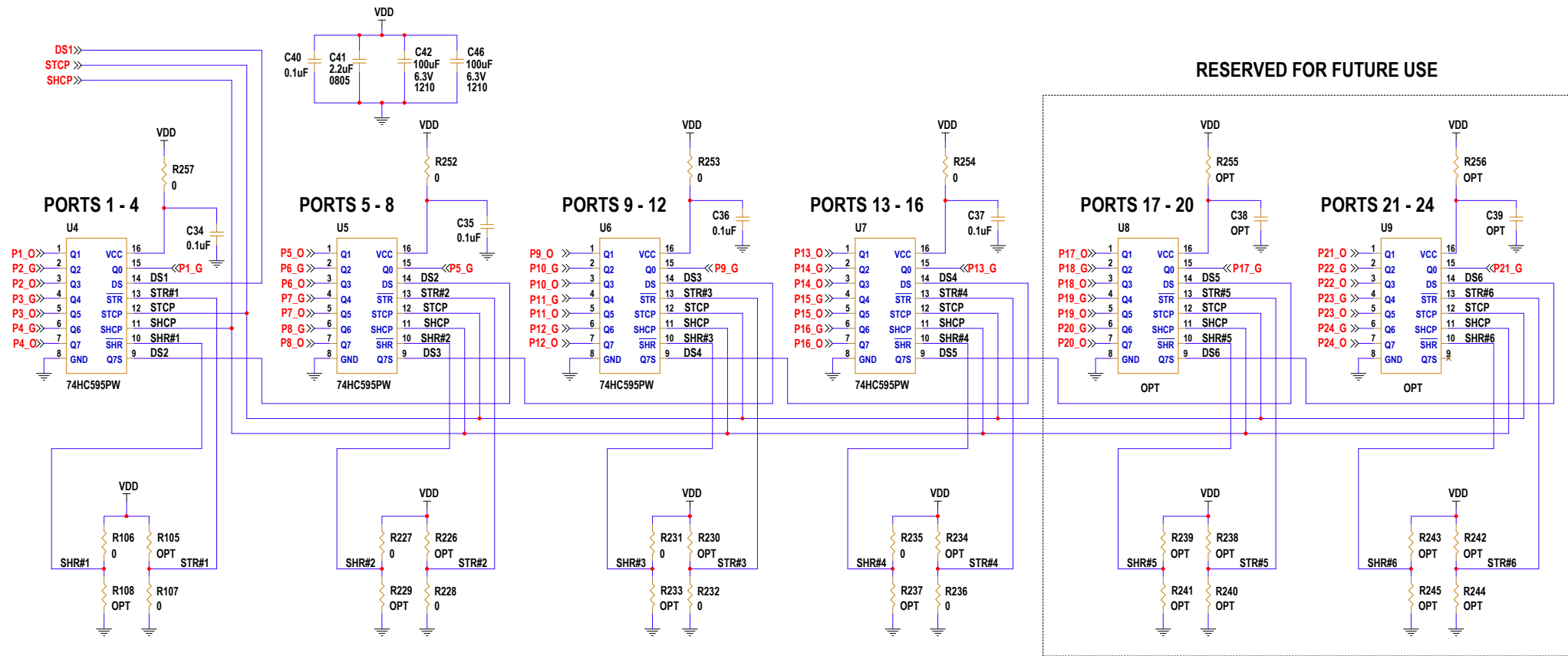


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		APP ENG.	ERIC.H		
TITLE: SCHEMATIC				16-PORT IEEE 802.3at PSE MOTHER BOARD	
IC NO. LTC3637EMSE#TRPBF				SCHEMATIC NO. AND REVISION:	
SKU NO. EVAL-LTC9101-3-MB-AZ				EVAL-LTC9101-3-MB-AZ_REV01	
SIZE: N/A				DATE:	
				SHEET 3 OF 11	

LED SHIFT REGISTERS

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	1			



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<Core Design>

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APPROVALS

PCB DES.	KENLM
APP ENG.	ERIC.H



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TITLE: SCHEMATIC

16-PORT IEEE 802.3at PSE MOTHER BOARD

IC NO. LTC3637EMSE#TRPBF

SKU NO. EVAL-LTC9101-3-MB-AZ

SIZE: N/A

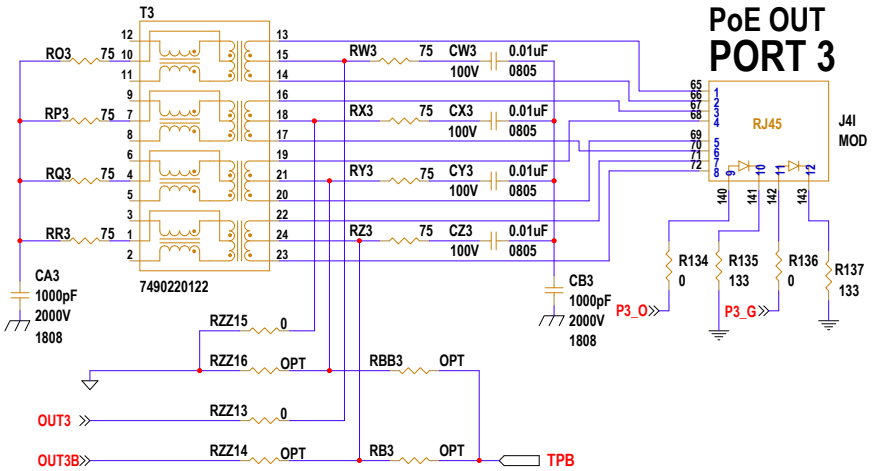
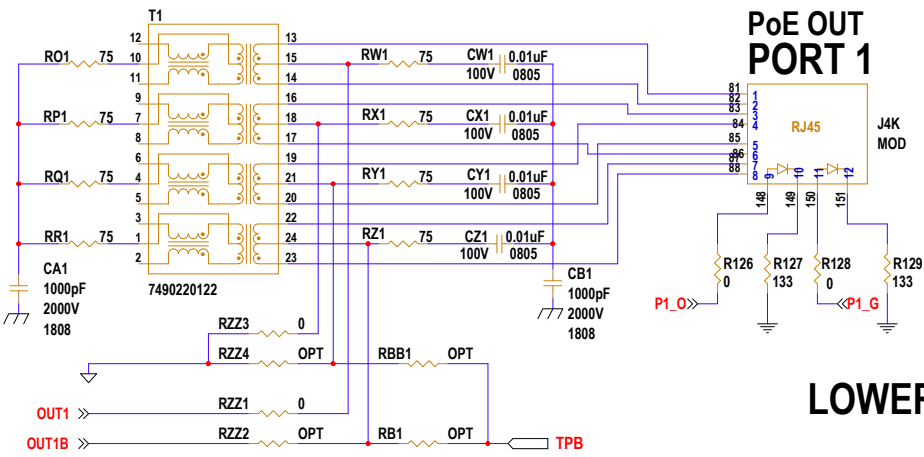
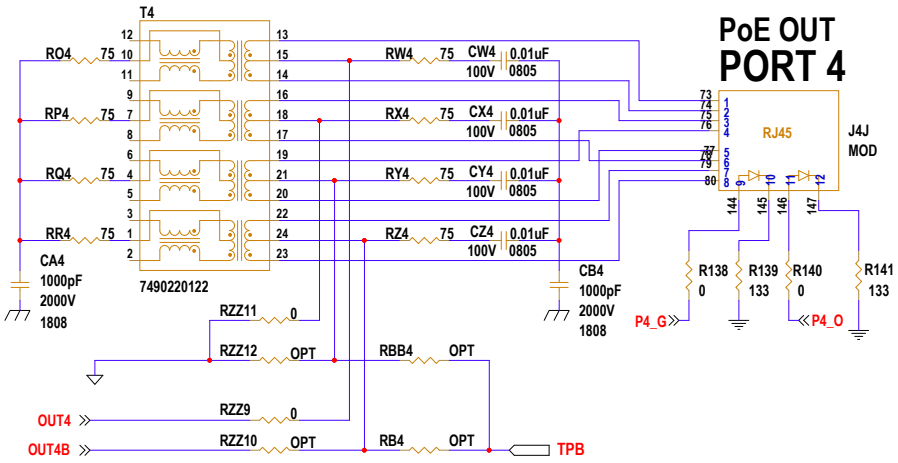
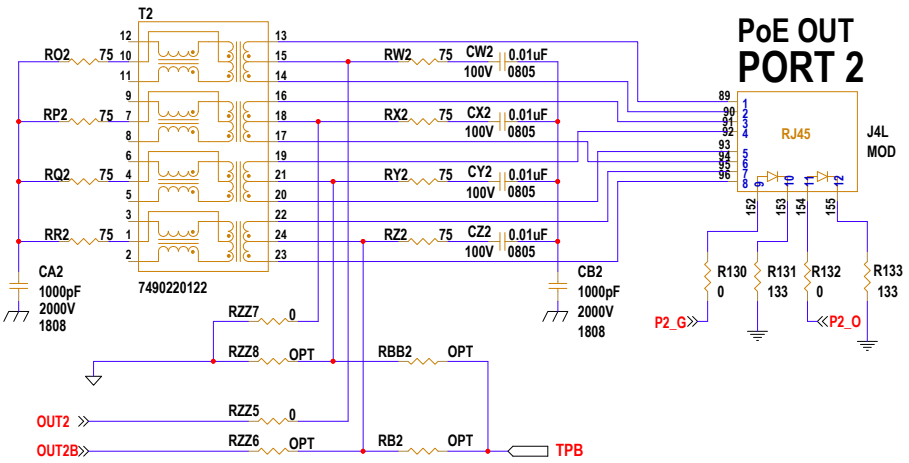
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EVAL-LTC9101-3-MB-AZ_REV01

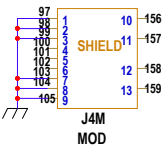
SHEET 4 OF 13

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	1			



UPPER ROW



LOWER ROW

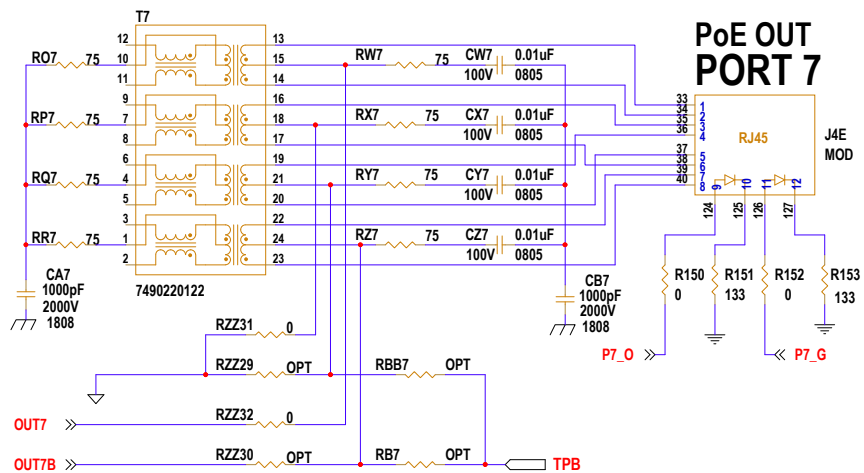
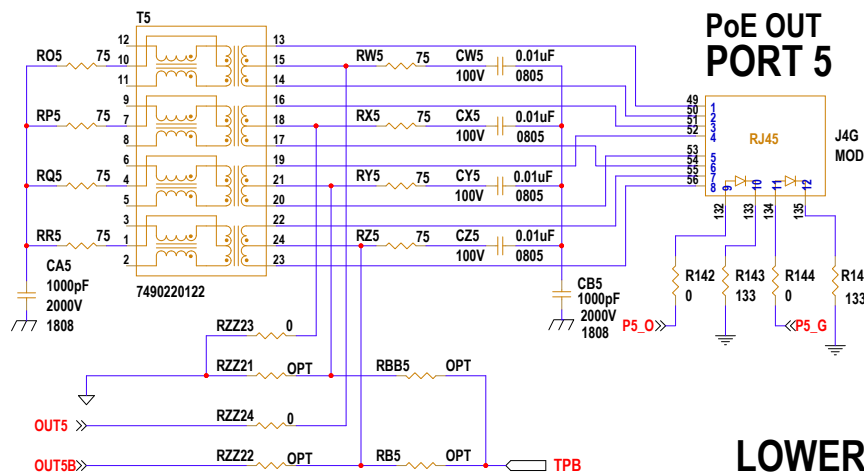
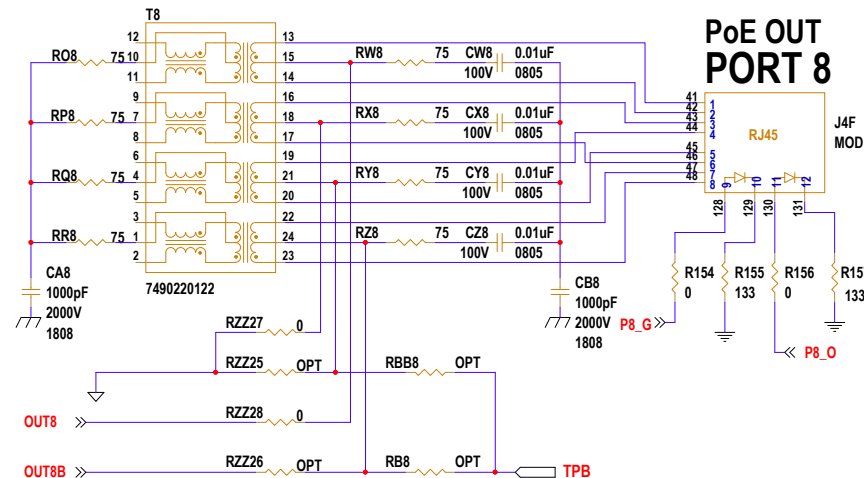
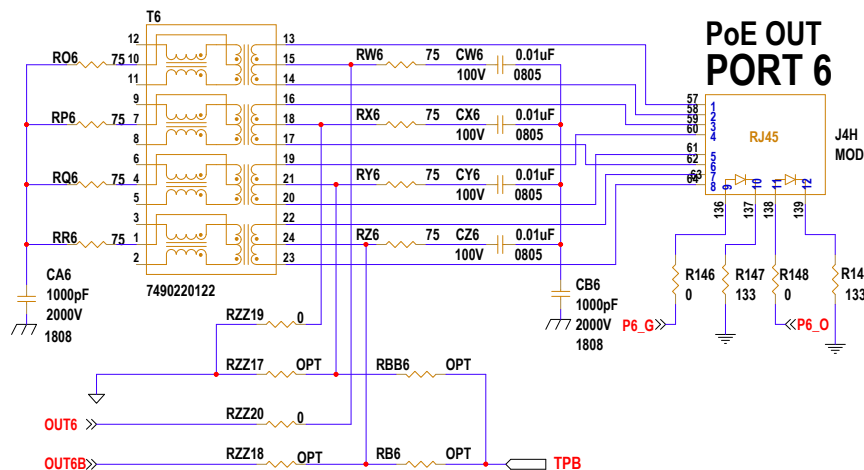


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				SKU NO. EVAL-LTC9101-3-MB-AZ		EVAL-LTC9101-3-MB-AZ_REV01	
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						SHEET 5 OF 13	

UPPER ROW


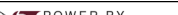
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ECO	REV	DESCRIPTION	APPROVED	DATE
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LOWER ROW

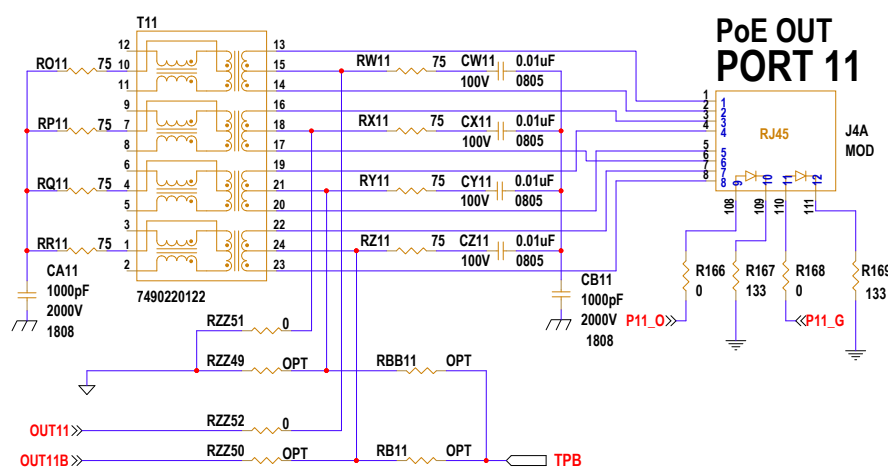
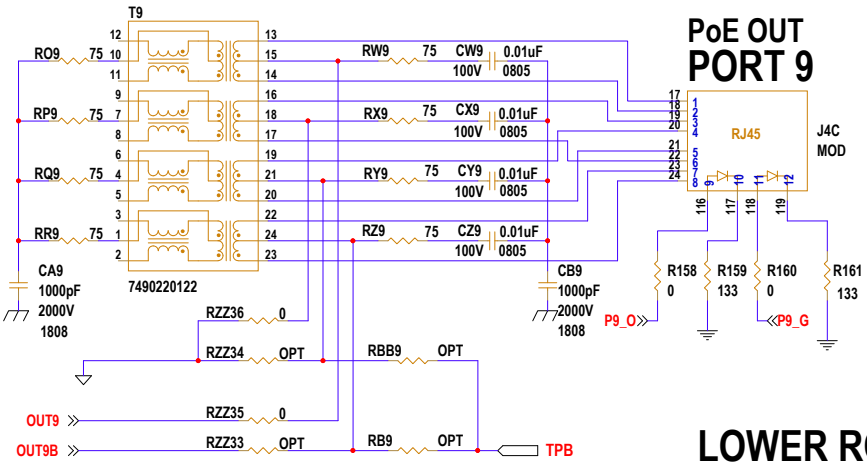
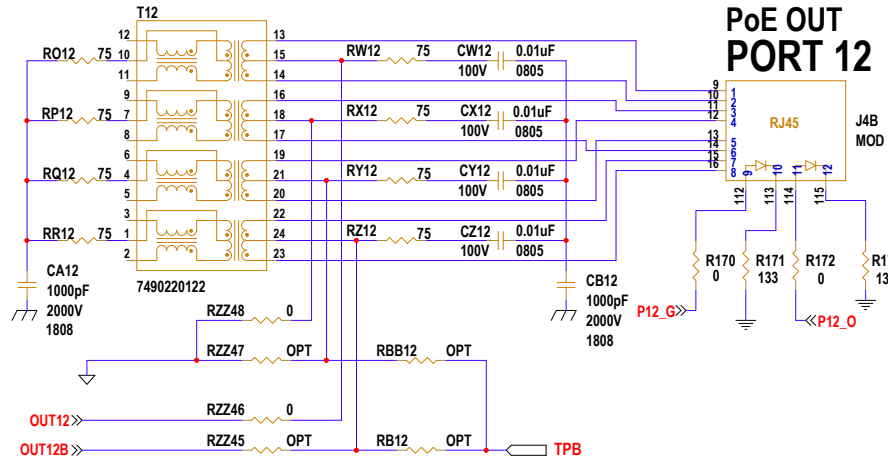
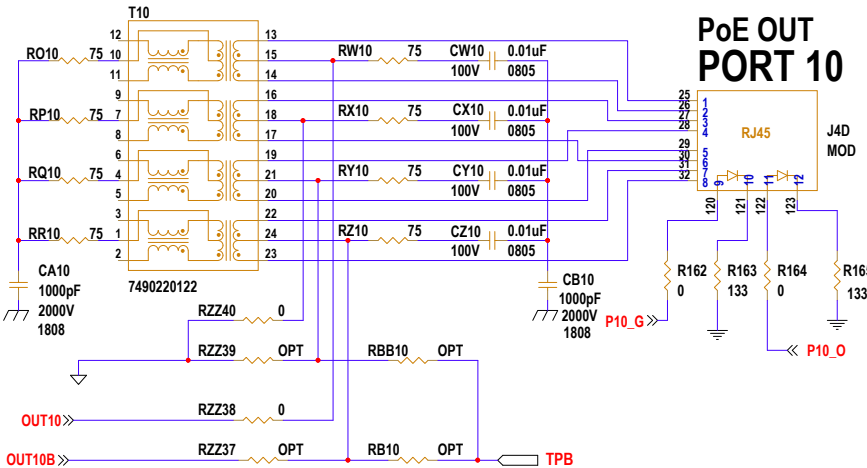
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		APP ENG.	ERIC.H				
<p>THIS CIRCUIT IS PROPRIETARY TO ANALOG DEVICES AND SUPPLIED FOR USE WITH ANALOG DEVICES PARTS.</p>				<p>SCHEMATIC NO. AND REVISION:</p> <p>EVAL-LTC9101-3-MB-AZ_REV01</p>		<p>SHEET 6 OF 13</p>	

UPPER ROW



REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	1			



LOWER ROW

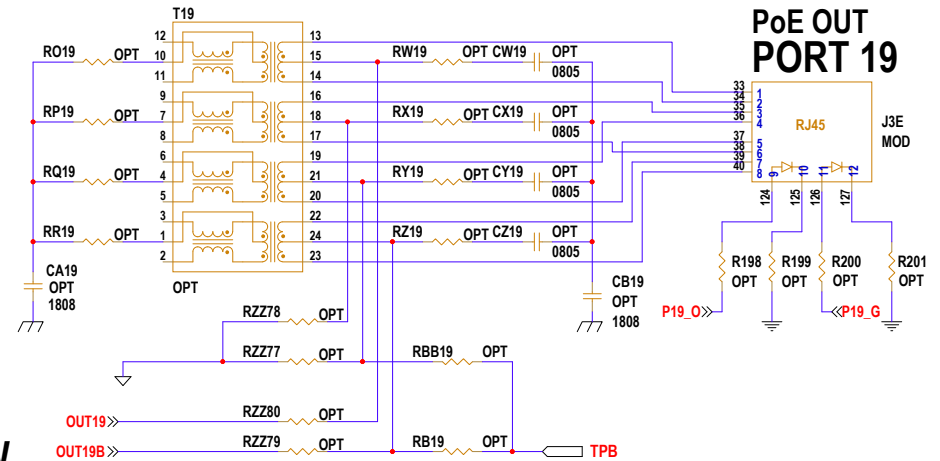
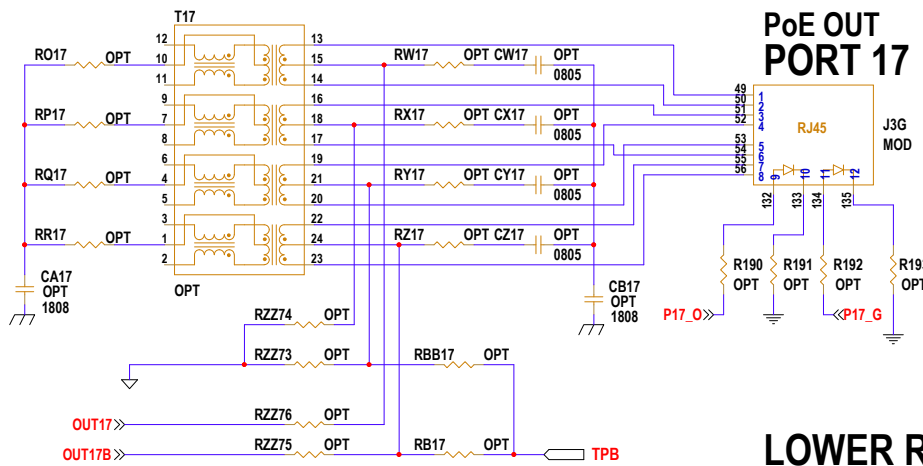
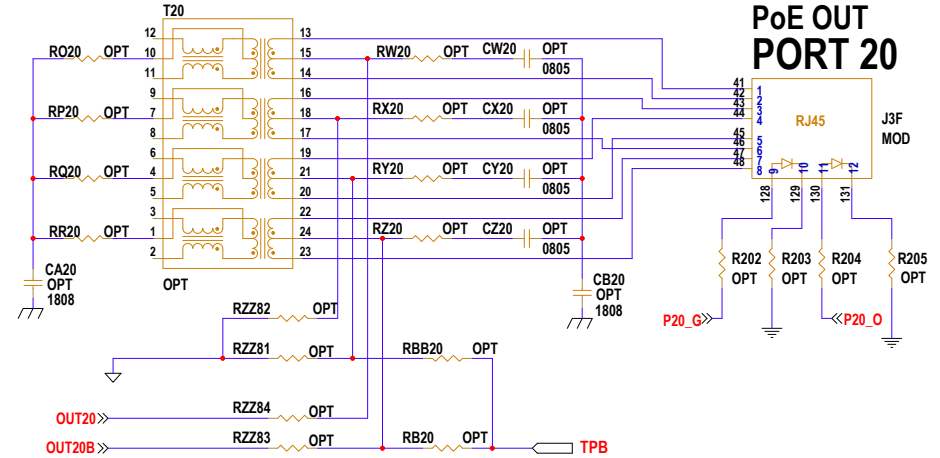
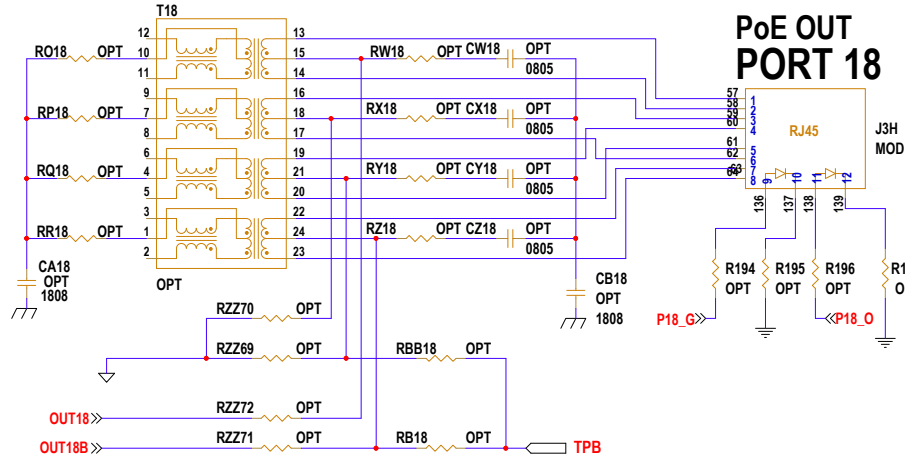
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	PCB DES.	KELM														
	APP ENG.	ERIC.H														
TITLE: SCHEMATIC		16-PORT IEEE 802.3at PSE MOTHER BOARD														
IC NO. LTC3637EMSE#TRPBF		SCHEMATIC NO. AND REVISION:														
SKU NO. EVAL-LTC9101-3-MB-AZ		EVAL-LTC9101-3-MB-AZ_REV01														
SIZE: N/A		DATE:														
		SHEET 7 OF 11														

UPPER ROW



REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
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LOWER ROW

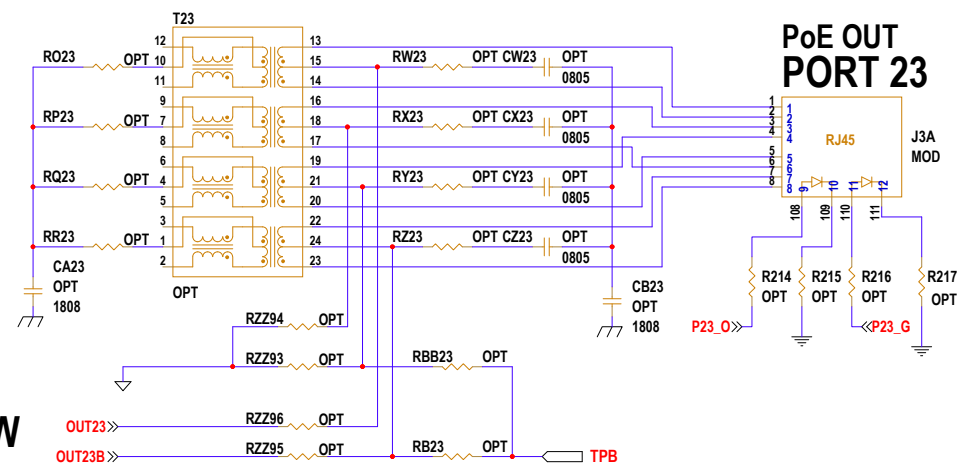
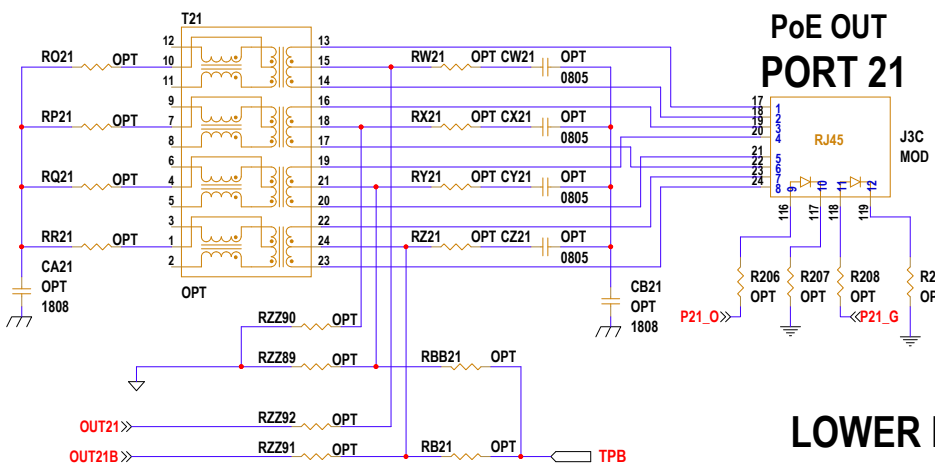
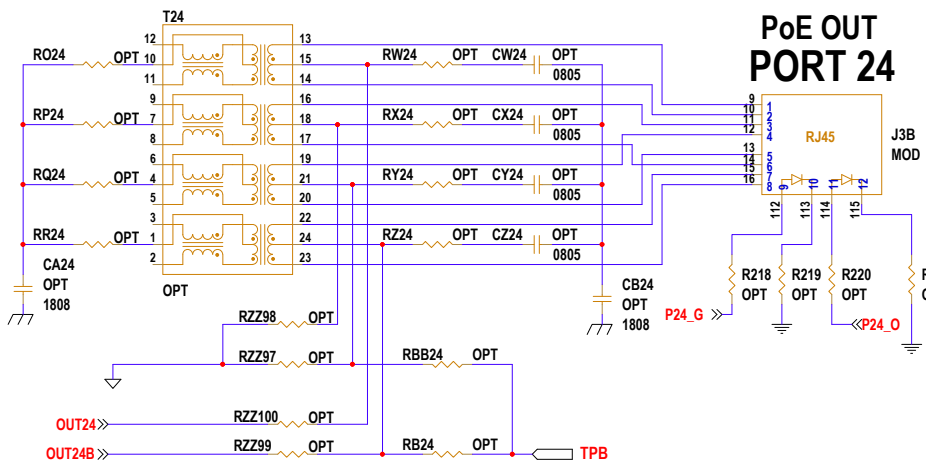
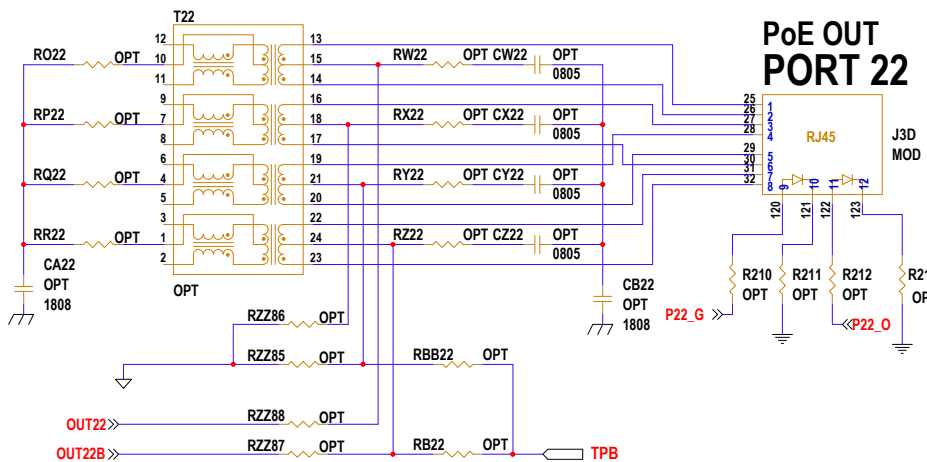
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CUSTOMER NOTICE		APPROVALS		 ANALOG DEVICES www.analog.com	 POWER BY LINEAR Phone: (408) 432-1900
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				IC NO. LTC3637EMSE#TRPBF	SCHEMATIC NO. AND REVISION:
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				SIZE: N/A	SHEET 9 OF 13
				DATE:	

UPPER ROW

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	1			



LOWER ROW

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APPROVALS

PCB DES.	KEN.M
APP ENG.	ERIC.H

**Phone: (408)432-1900****TITLE: SCHEMATIC**

16-PORT IEEE 802.3at PSE MOTHER BOARD

IC NO.	LTC3637EMSE#TRPBF
SKU NO.	EVAL LTC9101 2 MR A7

SCHEMATIC NO. AND REVISION:
EVAL-1 TC9101-3-MB-A7 REV01

SIZE: N/A	DATE:
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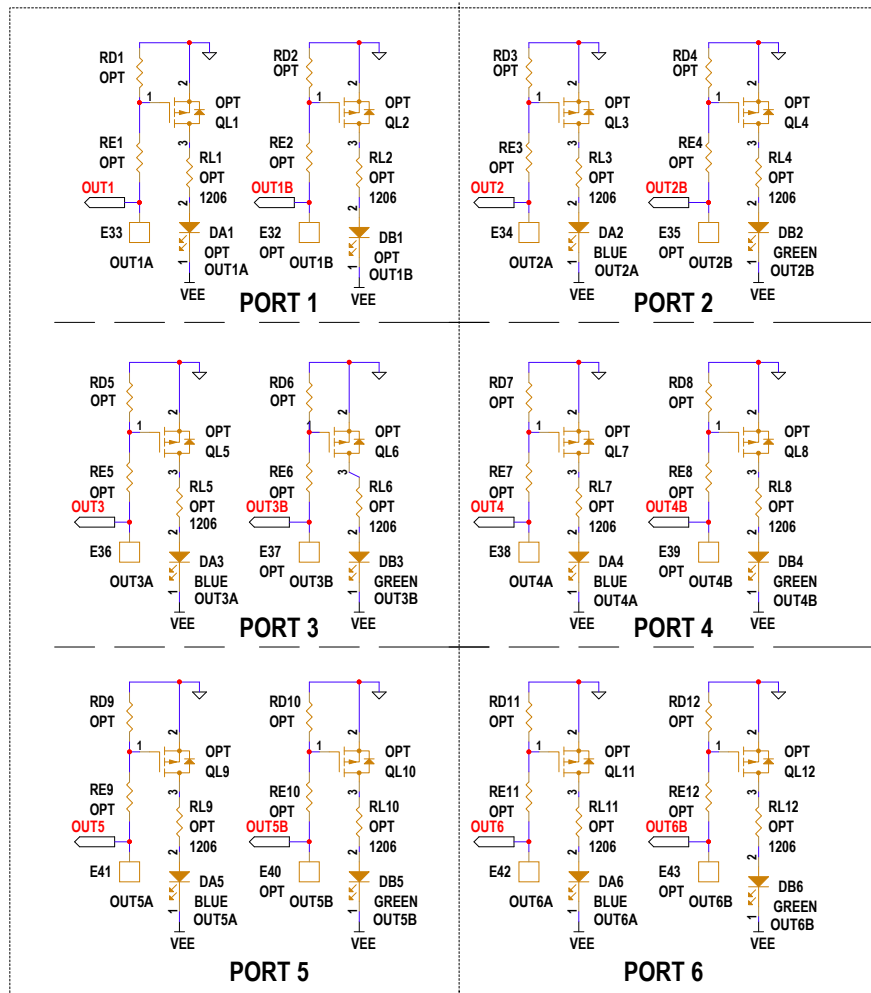
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SHEET 10 OF 13

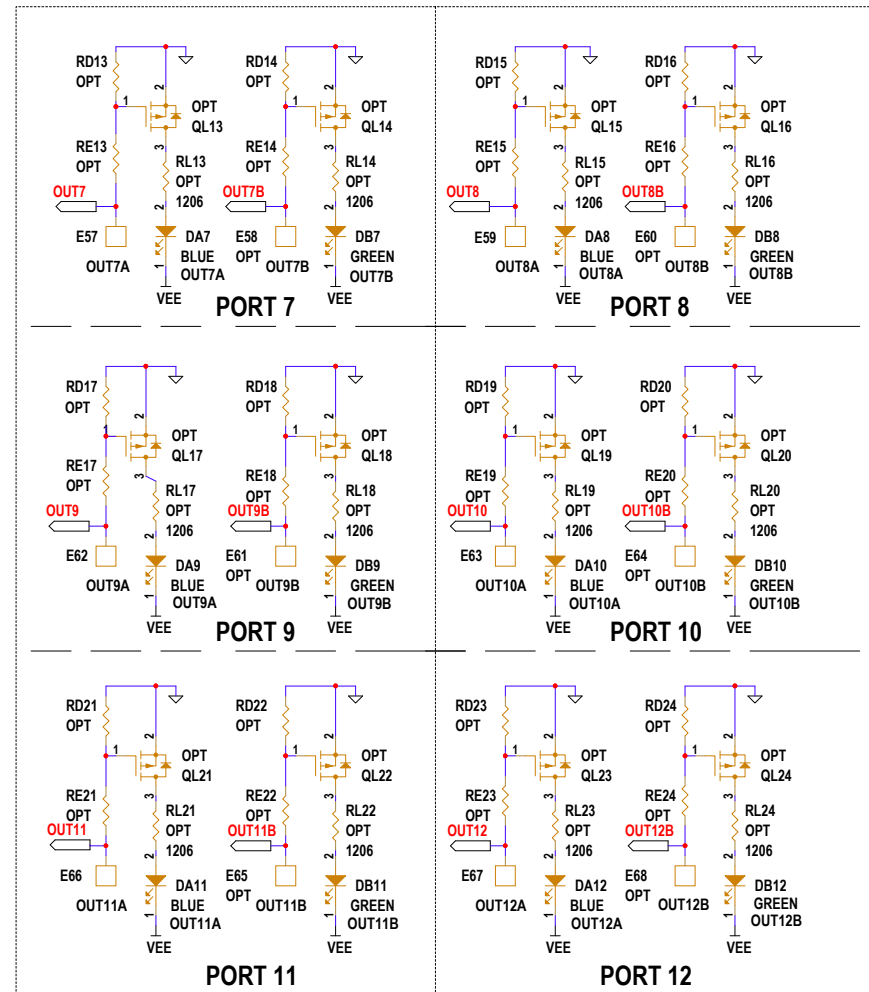
OF 13

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	1			

OPTIONAL PORT LED DRIVERS - PORTS 1 TO 6



OPTIONAL PORT LED DRIVERS - PORTS 7 - 12



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<Core Design>

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APP ENG. ERIC.H



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TITLE: SCHEMATIC

16-PORT IEEE 802.3at PSE MOTHER BOARD

IC NO. LTC3637EMSE#TRPBF

SKU NO. EVAL-LTC9101-3-MB-AZ

SIZE: N/A

DATE:

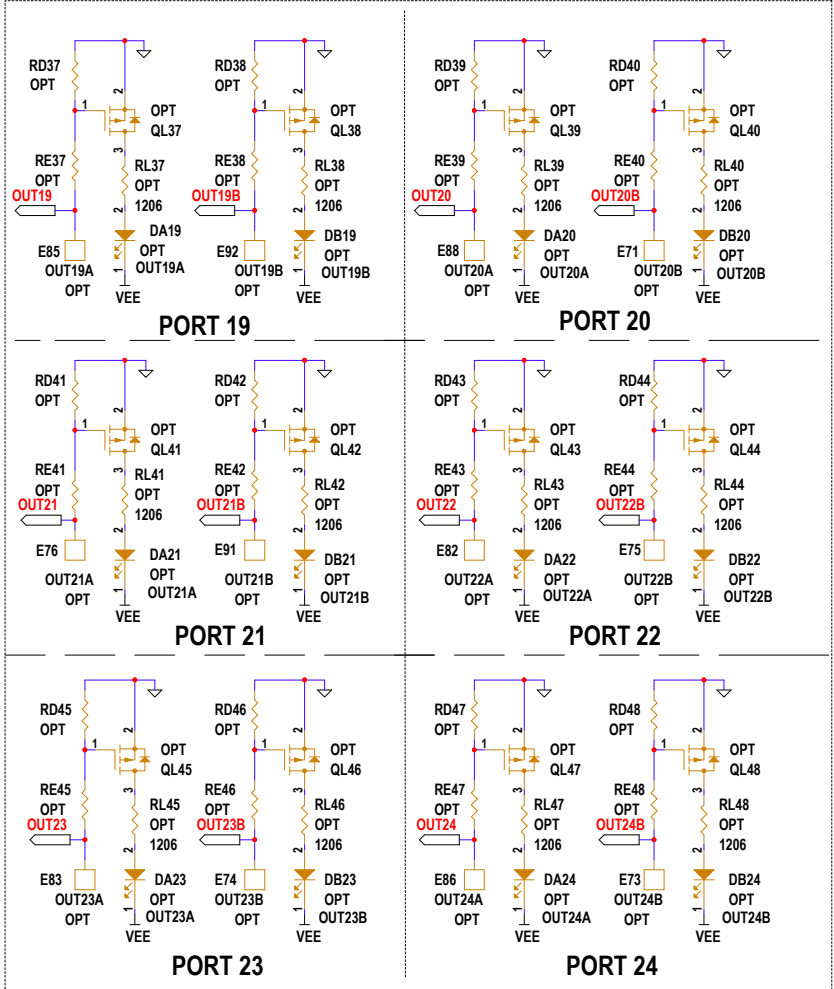
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EVAL-LTC9101-3-MB-AZ_REV01



SHEET 11 OF 13

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
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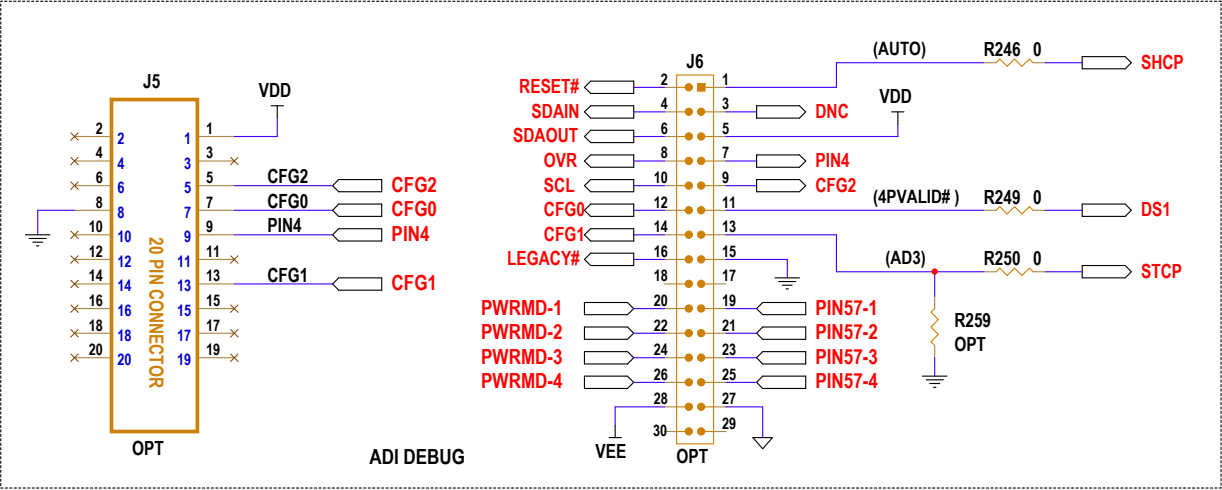
OPTIONAL PORT LED DRIVERS AND TEST POINTS - PORTS 19 -24



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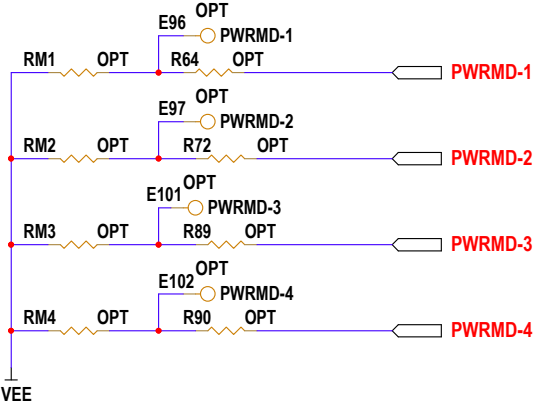
<p><Core Design></p> <p>CUSTOMER NOTICE</p> <p>ANALOG DEVICES HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO TEST PROPERLY AND RELIABLE OPERATOR REQUIREMENTS FOR APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT ANALOG DEVICES APPLICATIONS ENGINEERING FOR ASSISTANCE.</p>		<p>APPROVALS</p> <table border="1"> <tr> <td>PCB DES.</td> <td>KELM</td> </tr> <tr> <td>APP ENG.</td> <td>ERIC.H</td> </tr> <tr> <td> </td> <td> </td> </tr> <tr> <td> </td> <td> </td> </tr> <tr> <td> </td> <td> </td> </tr> <tr> <td> </td> <td> </td> </tr> <tr> <td> </td> <td> </td> </tr> </table>		PCB DES.	KELM	APP ENG.	ERIC.H											<div>  <div> <p>ANALOG DEVICES</p> <p>www.analog.com</p> </div> </div> <div>  <div> <p>POWER BY LINEAR</p> <p>Phone: (408)432-1900</p> </div> </div>	
PCB DES.	KELM																		
APP ENG.	ERIC.H																		
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<p>SIZE: N/A</p>		<p>DATE:</p>		<p>SHEET 12 OF 13</p>															

HARDWARE AND OPTIONAL COMPONENTS



PCA ADDITIONAL PARTS

MP1	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
MP2	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
MP3	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
MP4	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
MP5	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
MP6	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
MP7	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
MP8	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
MP9	STANDOFF,NYLON,SNAP-ON,0.75" (19.1mm)
LB1	BOARD S/N LABEL 895-0154
PCB1	PCB, DC3017A REV03 (MAO BANG)



NOTE: UNLESS OTHERWISE SPECIFIED
1. INSTALL SHUNTS AS SHOWN.

CUSTOMER NOTICE ANALOG DEVICES HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT ANALOG DEVICES APPLICATIONS ENGINEERING FOR ASSISTANCE.	APPROVALS		 www.analog.com Phone: (408)432-1900
	PCB DES.	KEN.M	
	APP ENG.	ERIC.H	
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