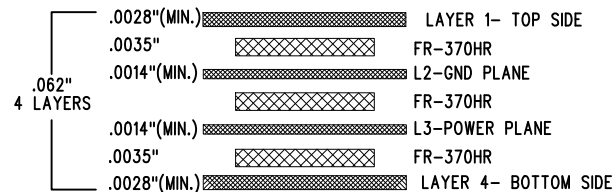


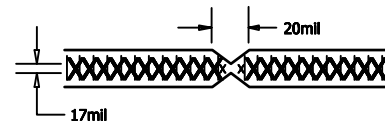
SIZE	QTY	SYM	PLATED	TOL
0.01	639	+	YES	±3Mil
0.187	4	×	NO	±3Mil
0.095	17	□	YES	±3Mil
0.07	2	◇	NO	±3Mil
0.035	26	⊗	YES	±3Mil
0.006	24	⊠	YES	+/-0.003


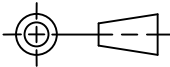
FIGURE 1 – STACKUP DETAILS



NOTES : UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: LEAD FREE ASSEMBLY COMPLIANT ISOLA
FR-370HR OR EQUIVLENT.
2 OZ. COPPER FINISH ON EXTERNAL LAYERS.
1 OZ. COPPER FINISH ON INTERNAL LAYERS.
THICKNESS .062 +/-0.006 TOTAL OF 4 LAYERS.
FLAMMABILITY RATING: 94 V-0 MINIMUM .
- SIZE: DIMENSIONS SHOWN AND TOLERANCES +/-0.010
0.00 ARE PRIMARY DATUMS.
- DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, .001 INCH THICK MIN. ALL
HOLE SIZES ARE SPECIFIED AFTER PLATING.
HOLE LOCATION TOLERANCES ARE +/-3Mil
INCH IN RELATION TO CENTER
- FINISH: SMOBC USING LPI BOTH SIDES COLOR GREEN.
GOLD IMMERSION BOTH SIDES.
- SILKSCREEN : USING WHITE NON-CONDUCTIVE EPOXY INK.
- DO NOT ALTER ARTWORK TO ADD LOGO OR DATE CODE BUT MAY
MODIFY PAD SIZE TO MEET END FINISH.
- PCB's SHOULD BE RoHS COMPLIANT.
- SCORING: FOR PANELIZED FABRICATION ONLY



UNLESS OTHERWISE SPECIFIED		 www.analog.com	
DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005"		TITLE: FABRICATION DRAWING 42V/2A+3X8V/1A, QUAD MONOLITHIC SYNCHRONOUS STEP-DOWN REGULATOR	
INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		SIZE N/A	IC NO. LT8692SEV EVAL-LT8692S-AZ REV 4
		FILENAME: EVAL-LT8692S-AZ-4.PCB	SHT 1 OF 1