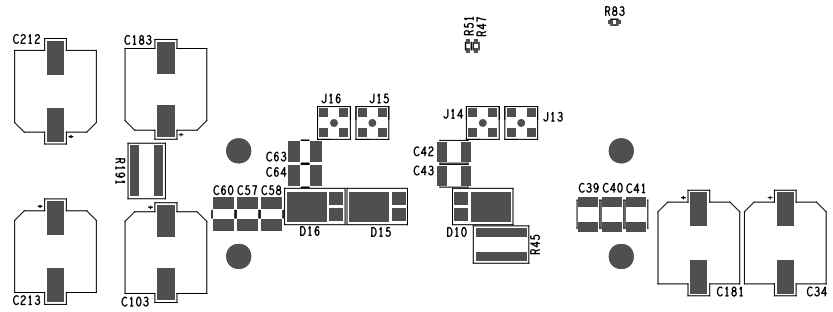
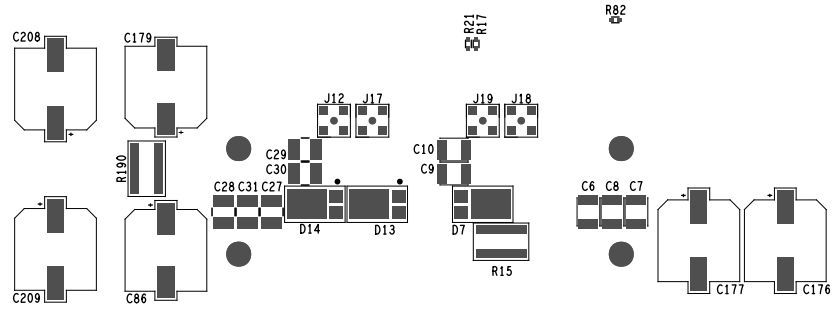
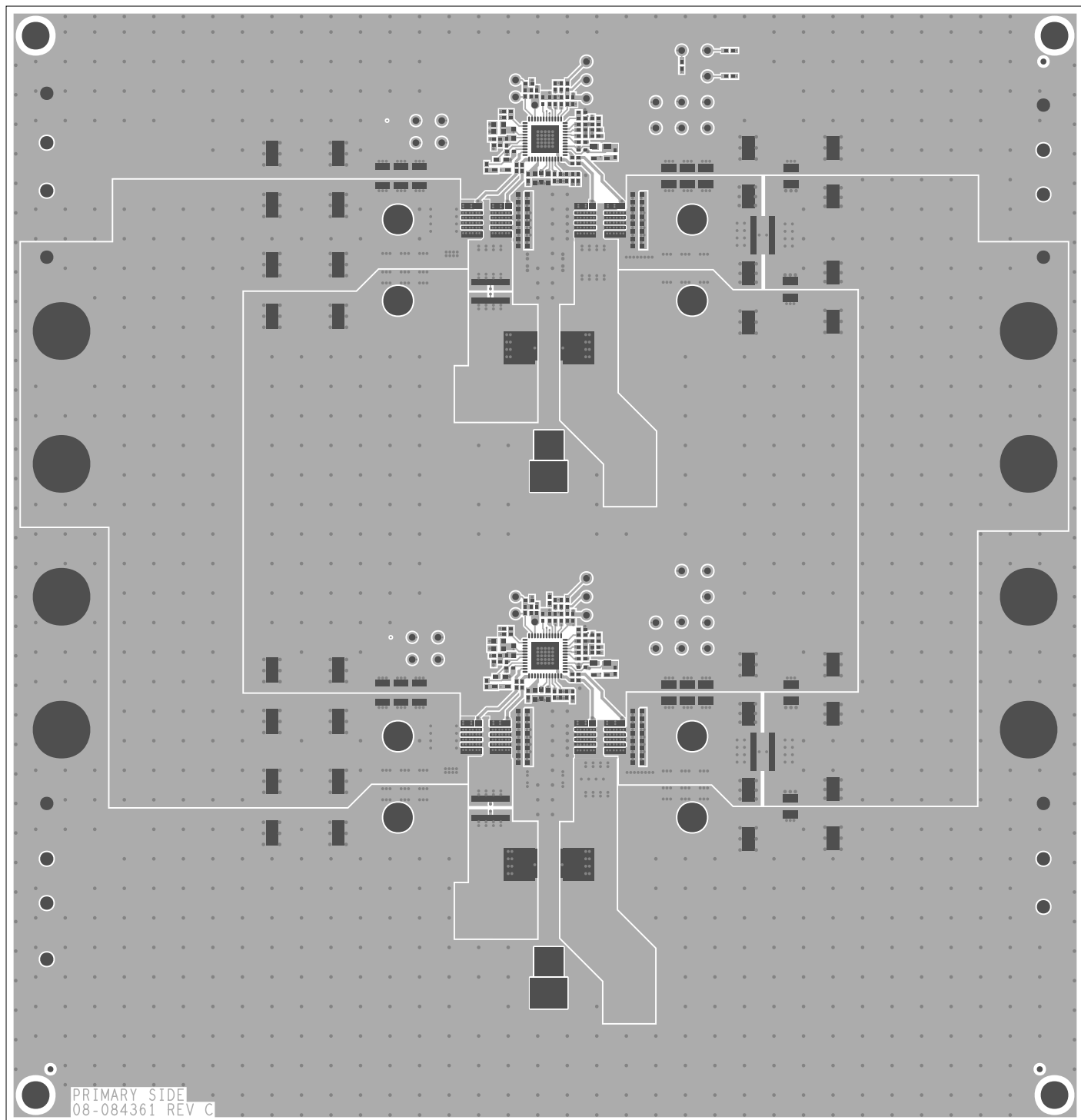
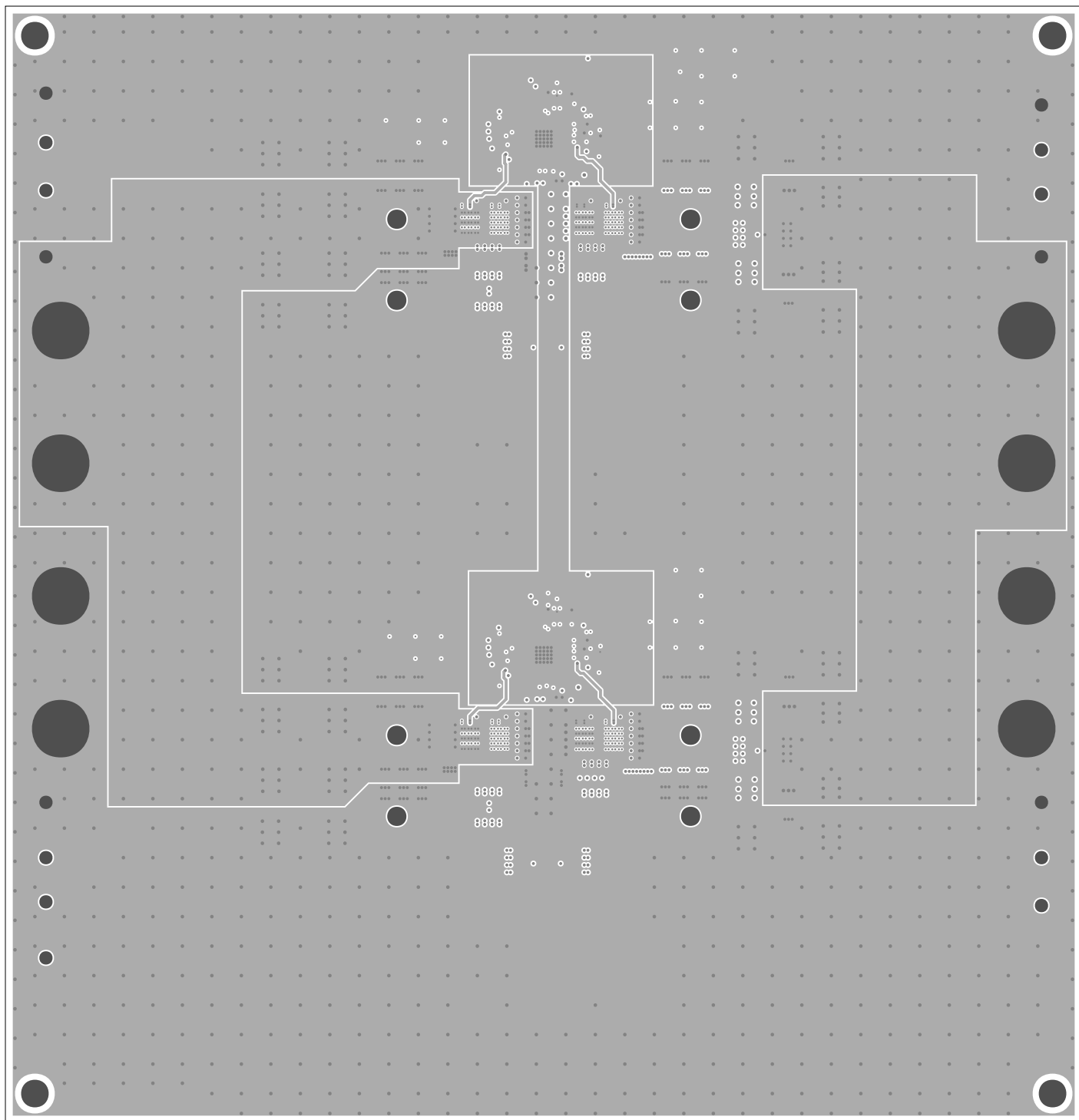
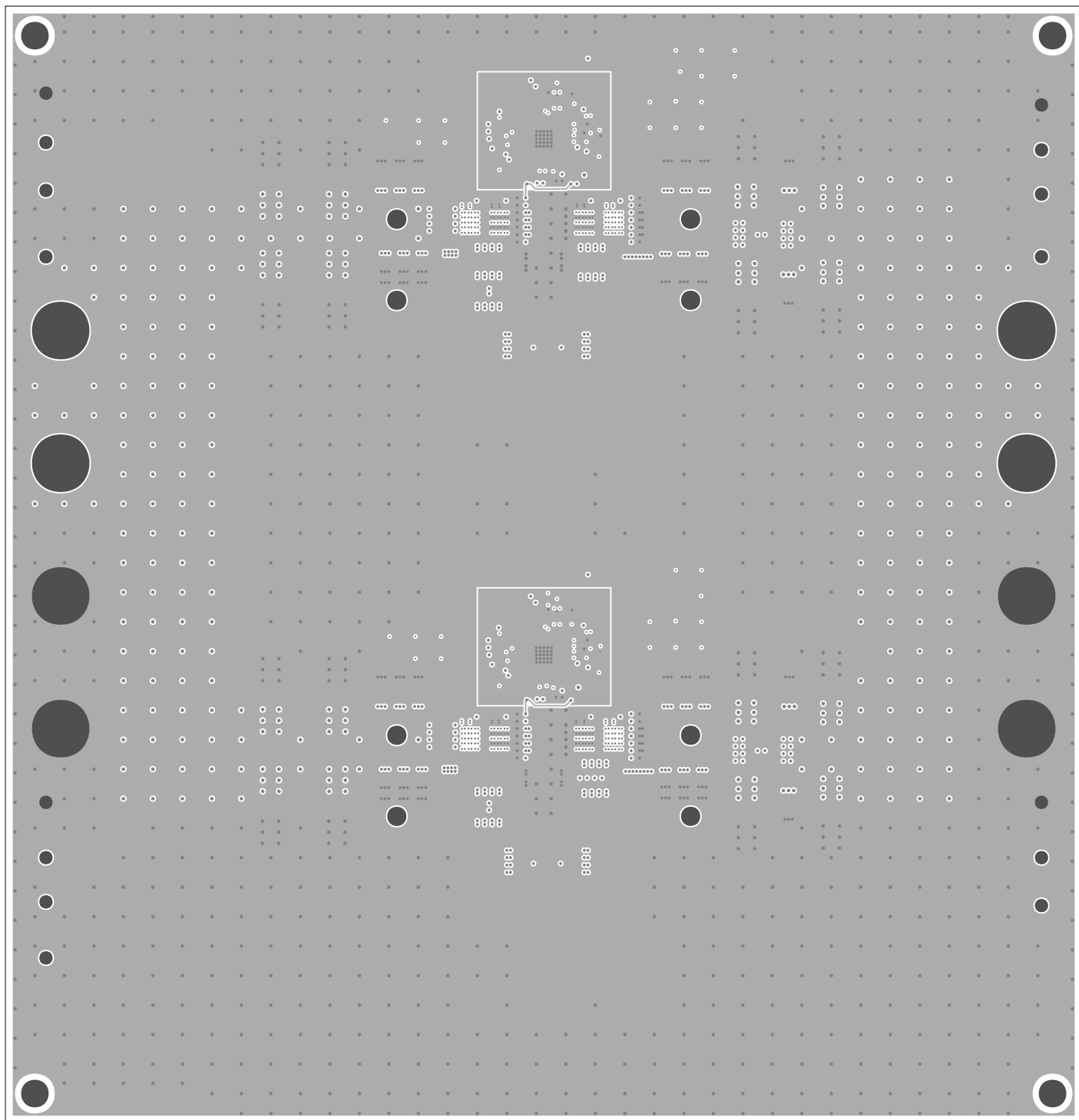
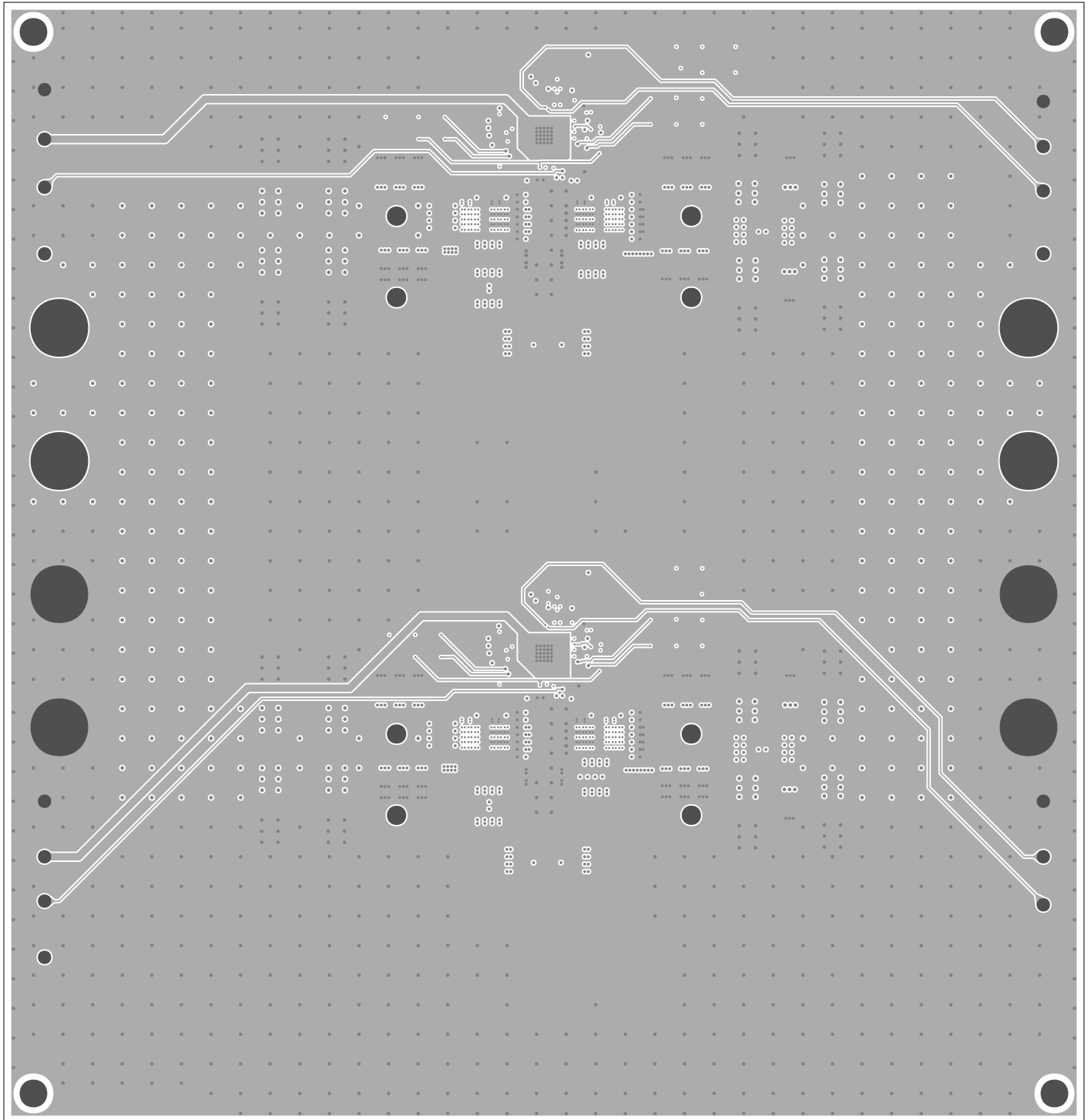


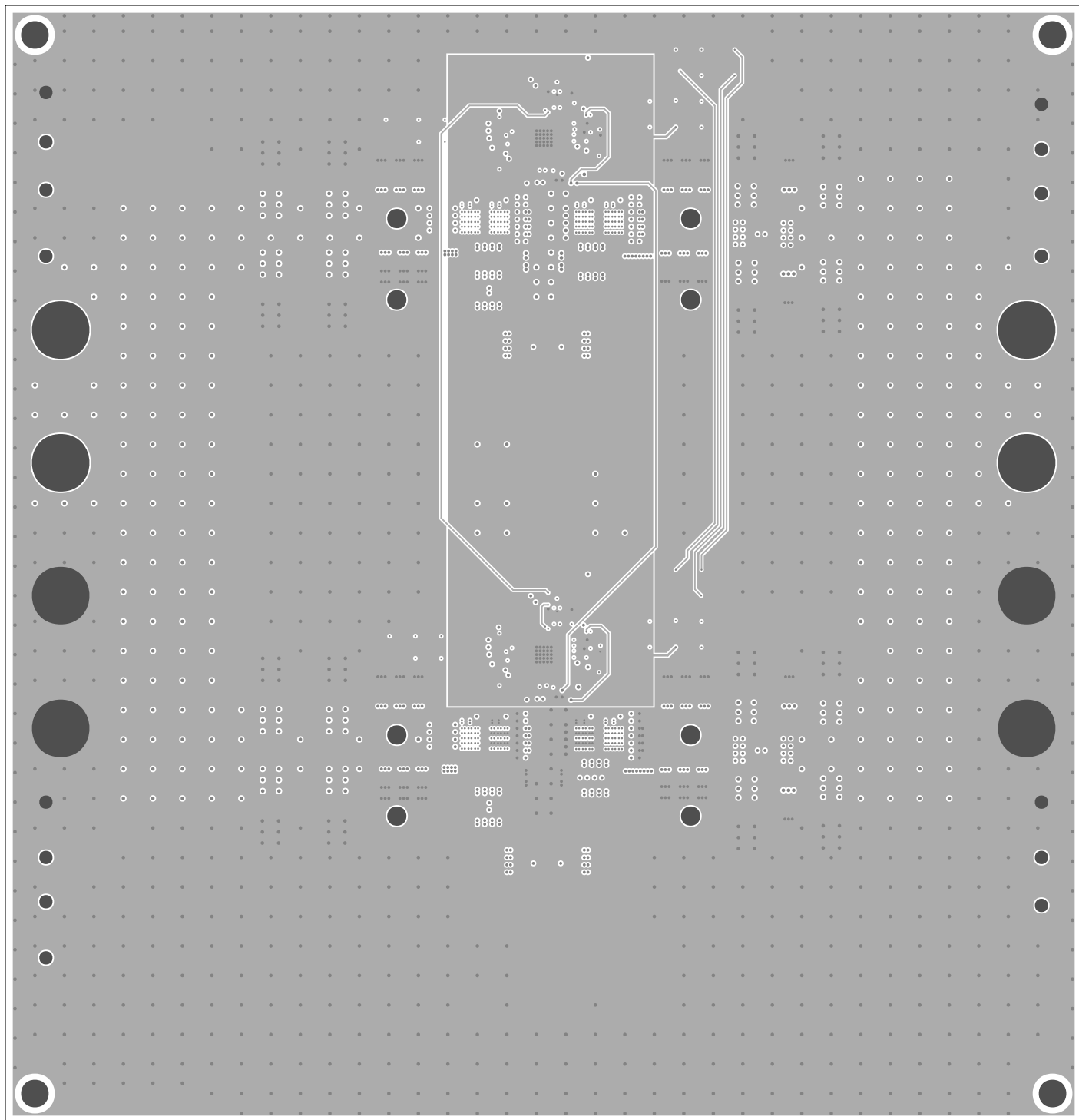


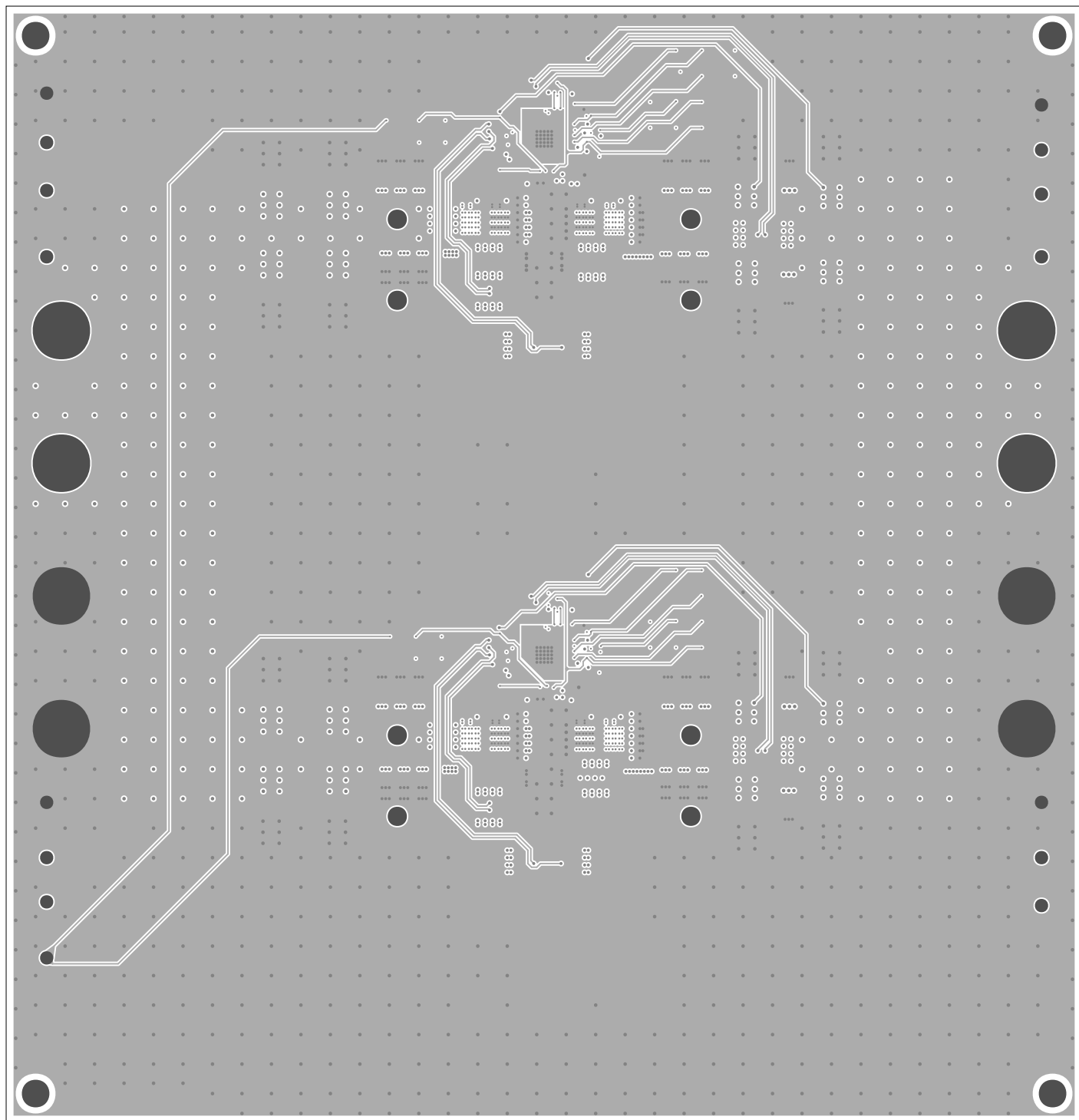


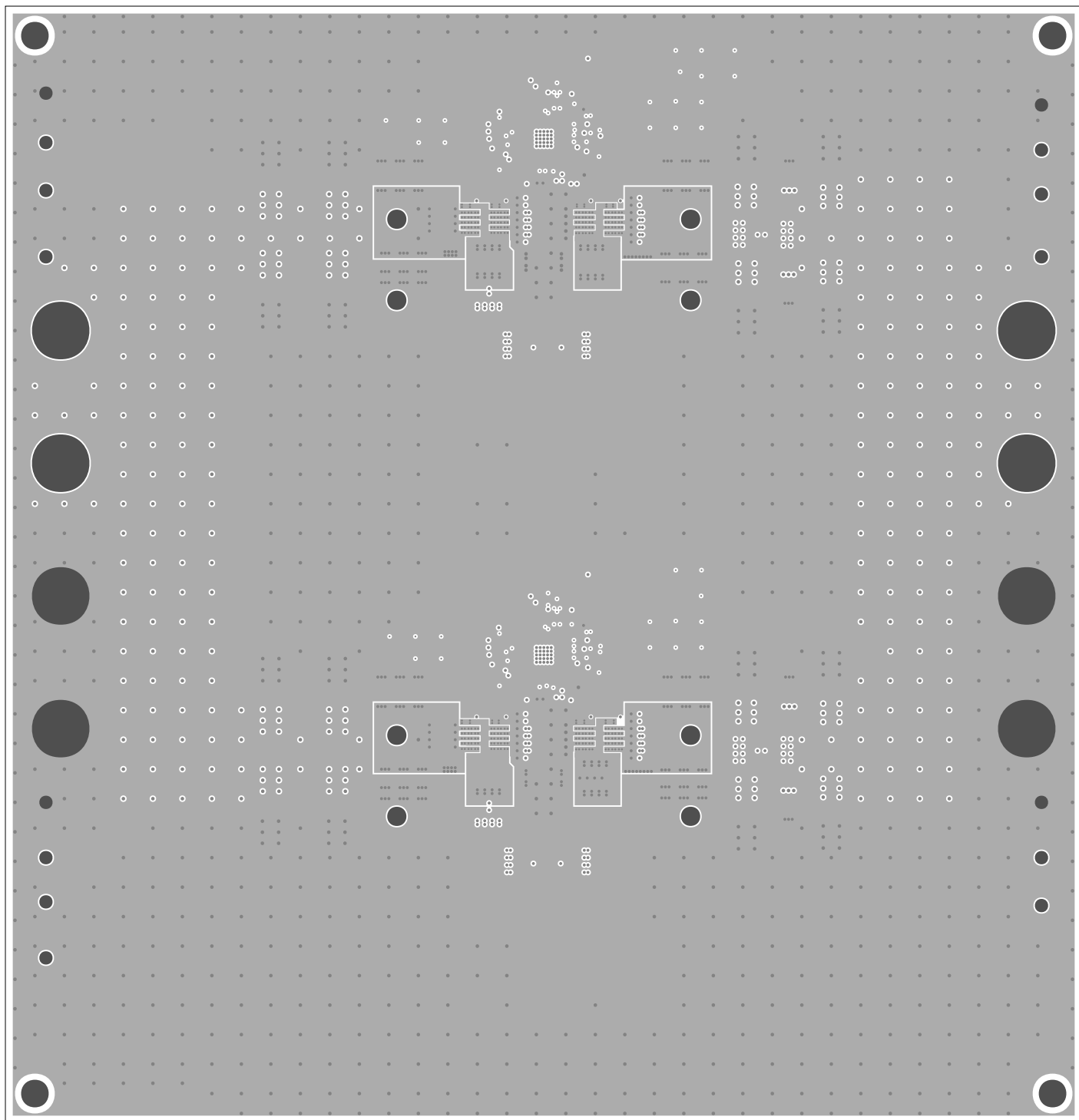


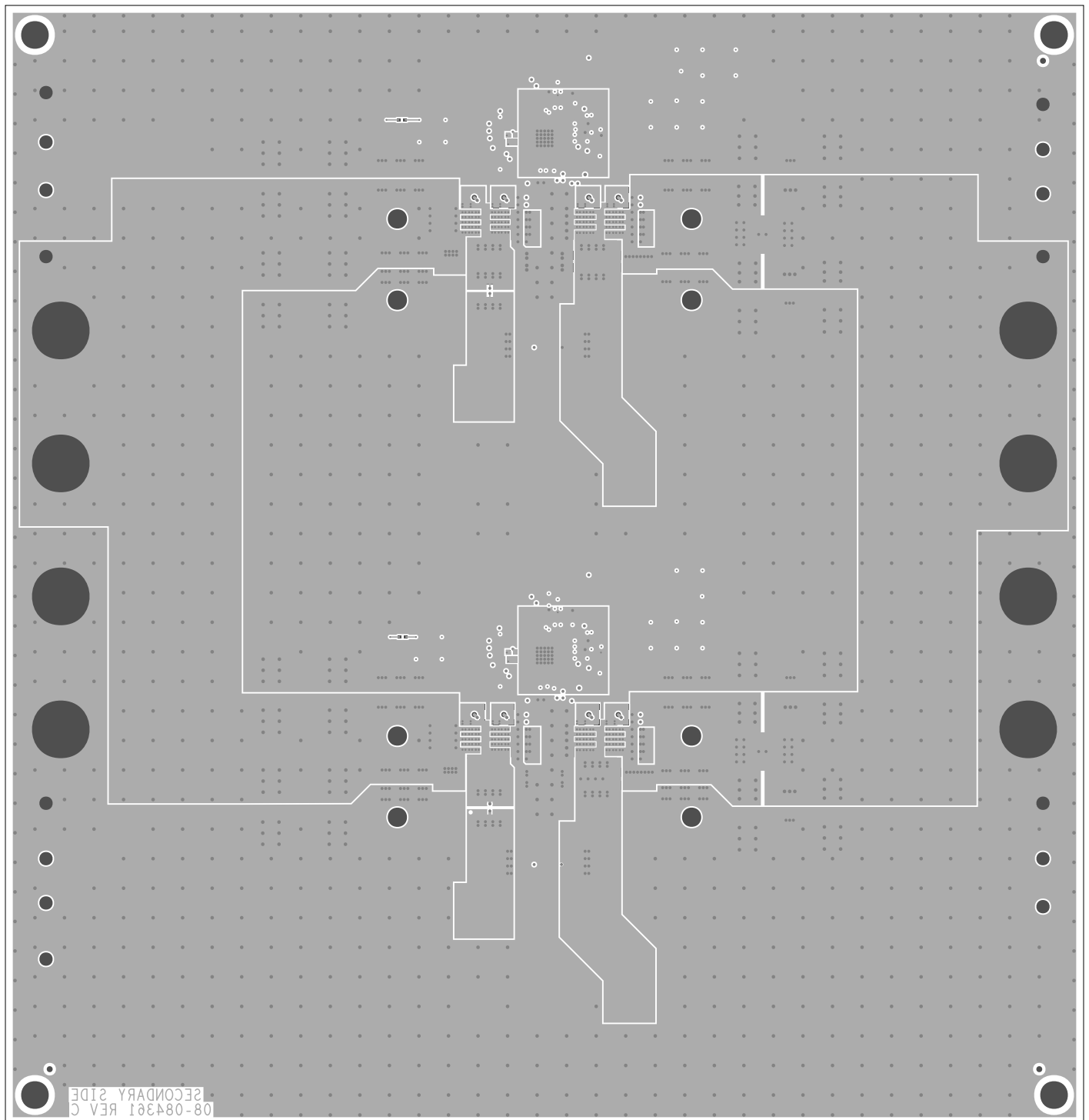


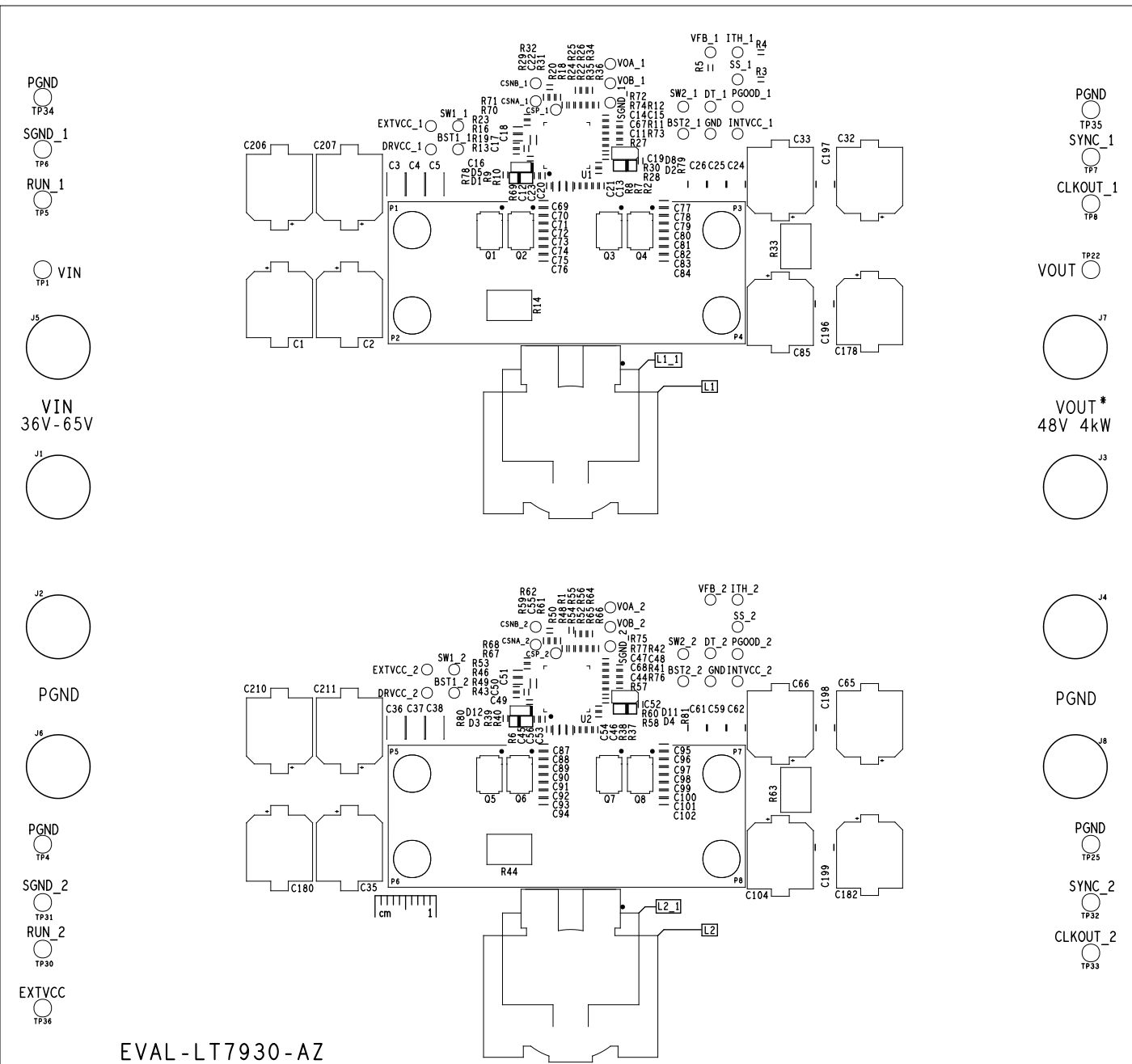








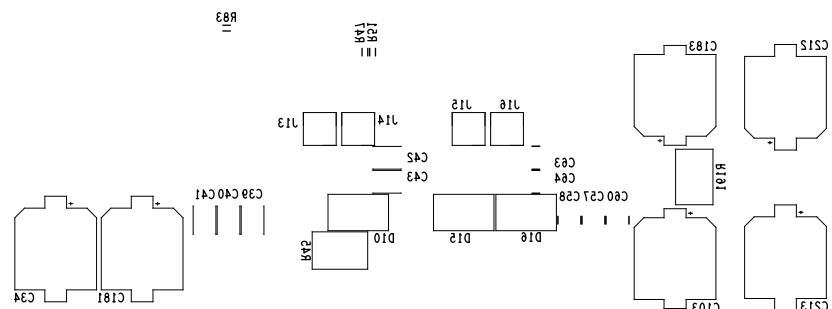


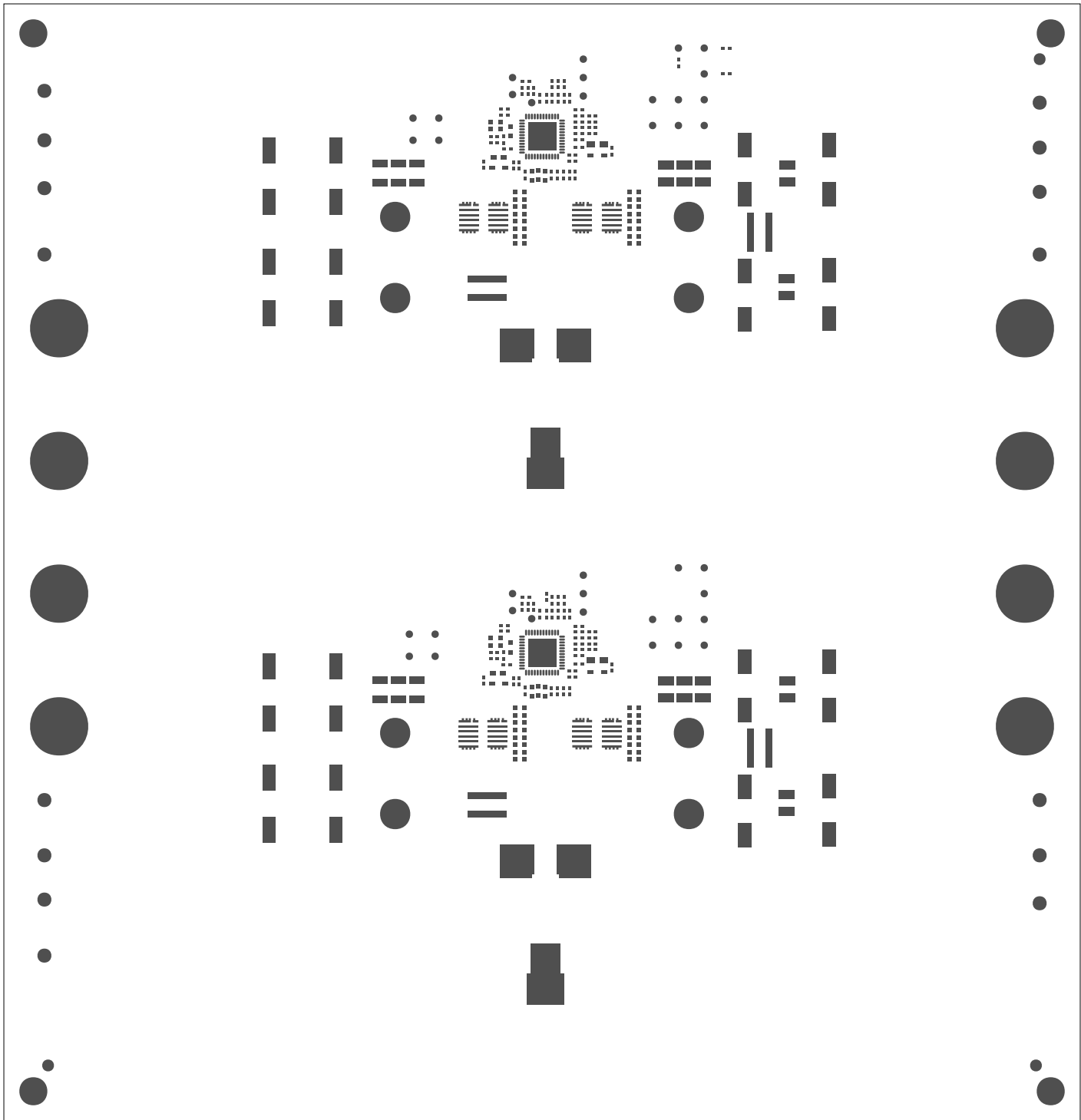


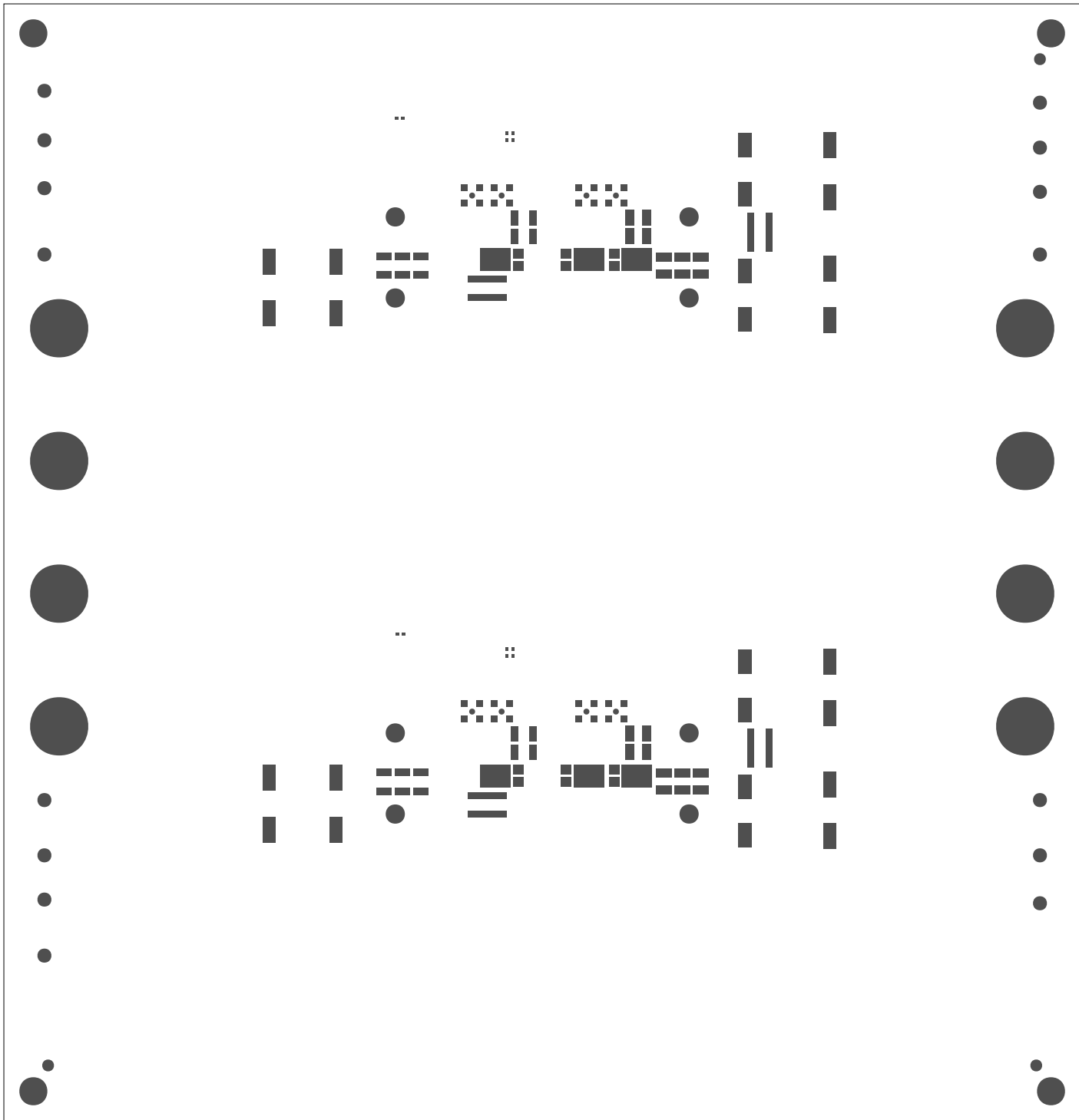
EVAL-LT7930-AZ
PARALLEL DUAL PHASE 4-SWITCH
BUCK-BOOST GAN FET CONTROLLER

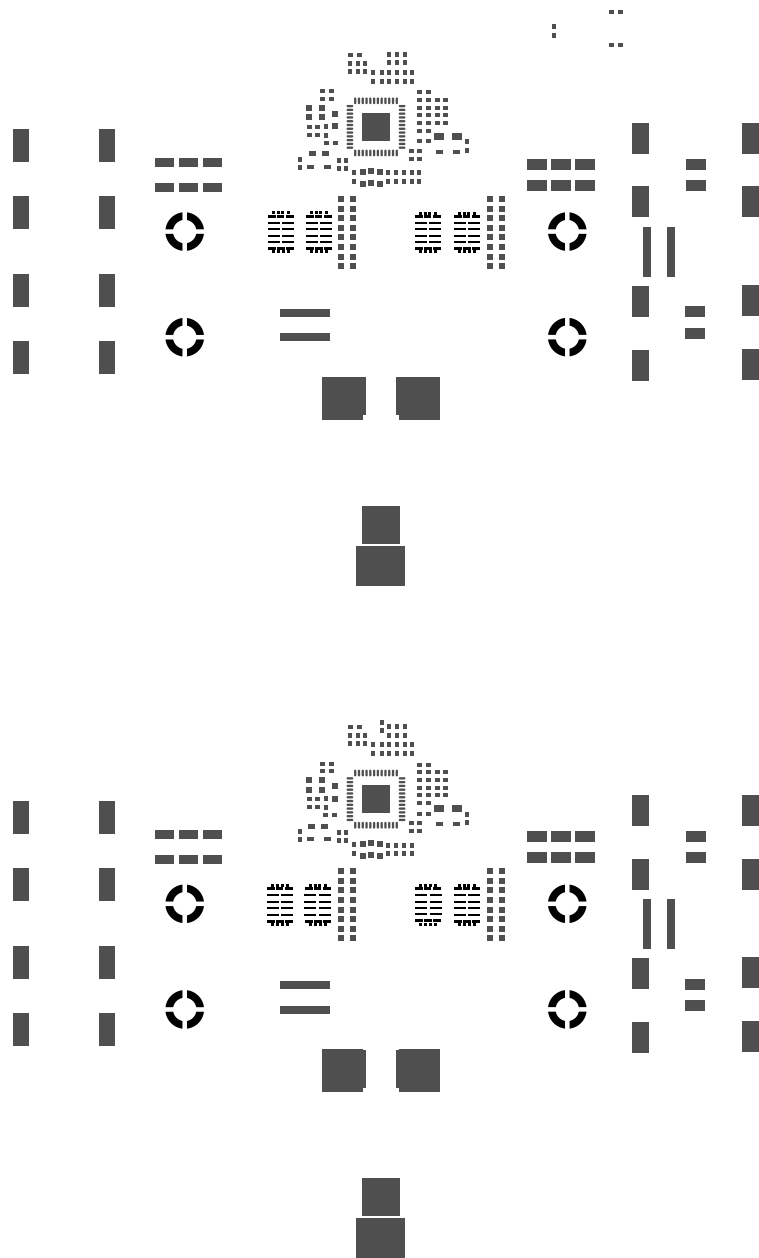


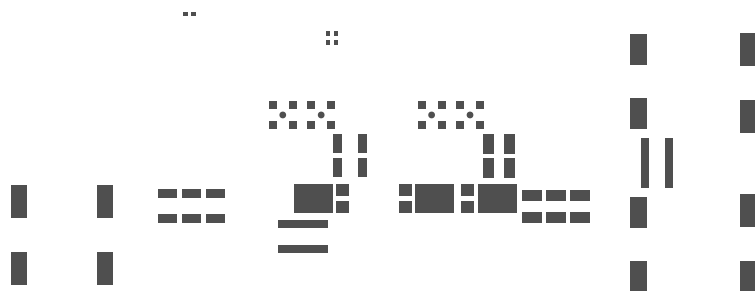
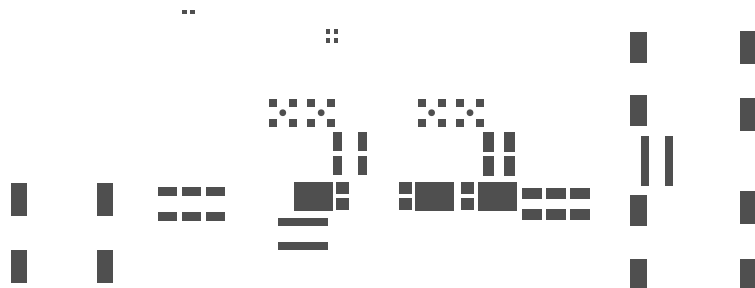
*SEE USER GUIDE FOR MAX POUT VS. VIN

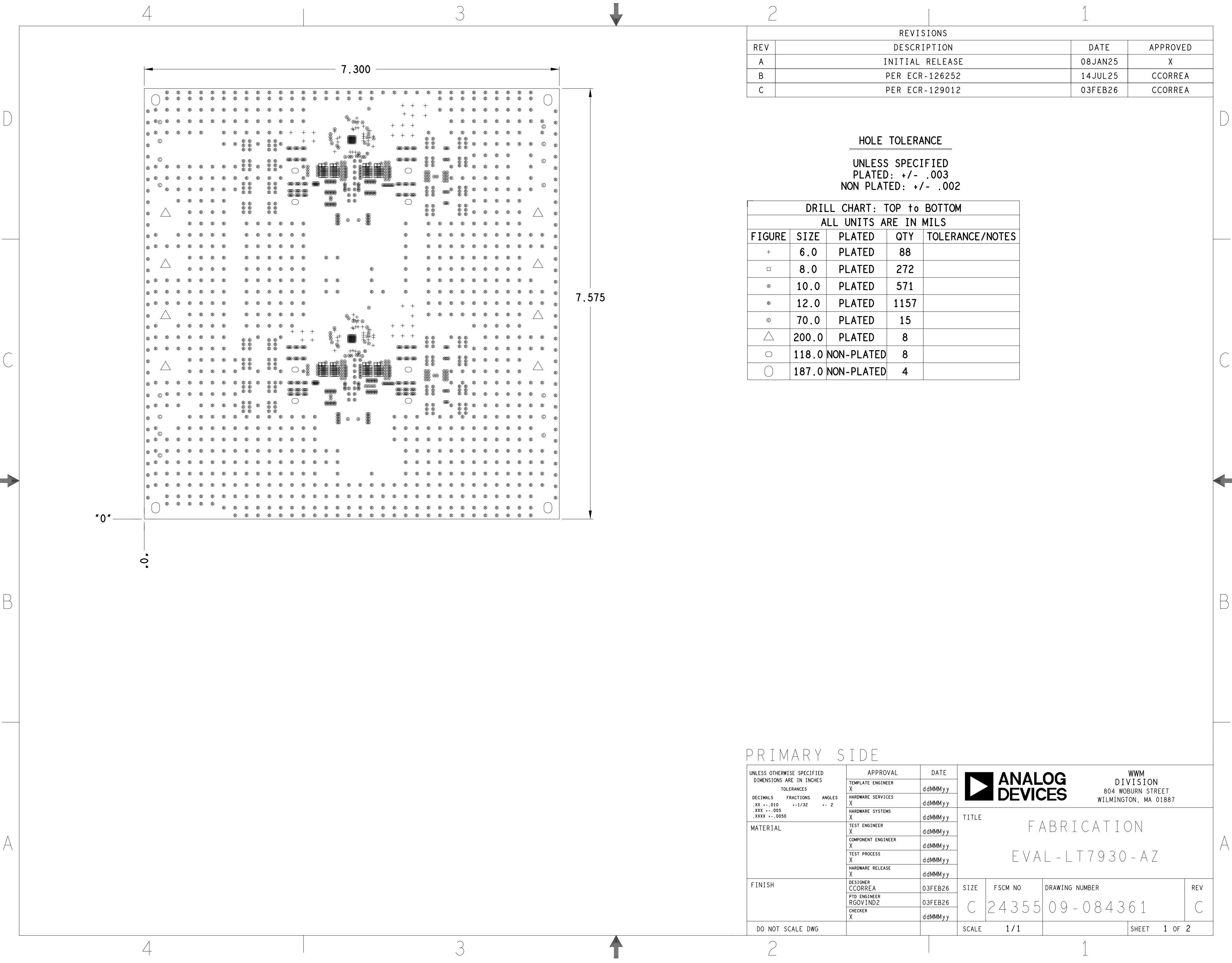












REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	08JAN25	X
B	PER ECR-126252	14JUL25	CCORREA
C	PER ECR-129012	03FEB26	CCORREA

HOLE TOLERANCE
UNLESS SPECIFIED
PLATED: +/- .003
NON PLATED: +/- .002

DRILL CHART: TOP to BOTTOM				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	PLATED	QTY	TOLERANCE/NOTES
+	6.0	PLATED	88	
□	8.0	PLATED	272	
•	10.0	PLATED	571	
•	12.0	PLATED	1157	
⊙	70.0	PLATED	15	
△	200.0	PLATED	8	
○	118.0	NON-PLATED	8	
○	187.0	NON-PLATED	4	

PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES .XX --.010 --1/32 -- 2 .XXX --.005 .XXXX --.0050			APPROVAL		DATE		 ANALOG DEVICES WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887							
			TEMPLATE ENGINEER X		ddMMMy									
			HARDWARE SERVICES X		ddMMMy									
			HARDWARE SYSTEMS X		ddMMMy									
MATERIAL			TEST ENGINEER X		ddMMMy		TITLE FABRICATION EVAL-LT7930-AZ							
			COMPONENT ENGINEER X		ddMMMy									
			TEST PROCESS X		ddMMMy									
			HARDWARE RELEASE X		ddMMMy									
FINISH			DESIGNER CCORREA		03FEB26		SIZE		FSCM NO 24355		DRAWING NUMBER 09-084361		REV C	
			PTD ENGINEER RGOVIND2		03FEB26									
			CHECKER X		ddMMMy									
DO NOT SCALE DWG							SCALE		1 / 1		SHEET 1 OF 2			

NOTES: UNLESS OTHERWISE SPECIFIED

1. DIMENSIONS ARE IN INCHES (EXCEPT WHERE NOTED).
ALL DOCUMENTS & SPECIFICATIONS REFERRED TO BELOW SHOULD BE THE LATEST REVISIONS.

MATERIAL: HOMOGENOUS MATERIALS IN THIS BOARD SHALL BE COMPLAINT WITH THE EU DIRECTIVE 2002/95/EC

2. BOARD MATERIAL:(USE CHECKED ITEMS)
- (X) ISOLA 370HR OR SI000-2 OR IT180 OR EQUIVALENT
 - () ISOLA-FR408HR OR EQUIVALENT
 - () ISOLA I5410
 - () MEGTRON 6
 - () NELCO-4000-13
 - () ROGERS 4350B
 - () ROGERS 3003
 - () ARLON 85N
 - () EM370D
 - () OTHER _____
3. ALL LAMINATES & BONDING MATERIALS SHOULD BE SELECTED FROM IPC-4101 OR IPC-4103.(TG>170 DEGC TD>300 DEGC)
UL FLAMMABILITY RATING 94V-0. BOARD MATERIAL & CONSTRUCTION SHALL MEET THE REQUIREMENTS OF UL796/UL796F.
4. REFER TO IPC-6010 SERIES, CLASS 2 FOR FABRICATION. WORKMANSHIP SHALL CONFORM TO IPC-A-600, CLASS 2.
5. REFER TO LAMINATION DIAGRAM FOR OVERALL BOARD THICKNESS, TOLERANCE APPLIES AFTER ALL LAMINATION AND PLATING PROCESSES. FINISHED THICKNESS MEASURED FROM TOP COPPER TO BOTTOM COPPER.
6. BOW & TWIST NOT TO EXCEED 0.0075 INCHES (0.75%) PER LINEAR INCH AND SHOULD BE MEASURED PER IPC-TM-650, METHOD 2.4.22.
7. ACCEPTABILITY PER ADI SPECIFICATION TST00115.

TOOLING:

8. IMPEDANCE REQUIREMENTS: IF NO STACKUP IS DEFINED, THE VENDOR IS ALLOWED TO ADJUST THE DIELECTRIC THICKNESS & TRACE WIDTHS TO MEET THE IMPEDANCE REQUIREMENT. IF SPECIFIED, THE VENDOR MUST MEET THE REQUIREMENTS LISTED IN THE IMPEDANCE TABLE. ANY ADJUSTMENT MADE TO THE DEFINED STACKUP, TRACE WIDTH & SPACING THAT IMPACT THE REQUIREMENTS MUST HAVE WRITTEN APPROVAL FROM ADI.
9. FILLET OPTIONS TO ENHANCE RELIABILITY AT PAD JUNCTIONS WHERE SPACING PERMITS.
- () FILLETS ALLOWED
 - (X) FILLETS NOT ALLOWED
10. THIEVING:
- () VENDOR MAY ADD THIEVING TO COMPENSATE FOR LOW COPPER DENSITY AREAS MAINTAINING A MINIMUM 0.100 INCH CLEARANCE FROM ALL COPPER FEATURES.
 - (X) VENDOR MAY NOT ADD THIEVING TO COMPENSATE FOR LOW COPPER DENSITY AREAS.
11. LAYER TO LAYER REGISTRATION SHALL BE WITHIN 0.003 INCHES.

FINISH:

12. DRILL SIZES ARE FINISHED HOLE SIZES. ALL HOLES SHALL BE LOCATED WITHIN 0.005 INCHES DTP,UNLESS SPECIFIED.
MINIMUM BARREL PLATING OF 0.001 INCHES. PLATED HOLES SHALL NOT BE ROUGH OR IRREGULAR SO AS TO HINDER PROPER SOLDER WICKING. BARREL RELIEF ON SOLDERMASK ALLOWED IN UNFILLED VIA IN PAD HOLES.
13. PLATING SPECIFICATION:
- (X) REFER TO LAMINATION DIAGRAM FOR FINISHED COPPER WEIGHT/THICKNESS REQUIRMENTS
 - THE STARTING COPPER WEIGHT/THICKNESS CAN VARY AS LONG AS THE FINISHED COPPER WEIGHT/THICKNESS IS NOT LESS THAN THE SPECIFIED VALUE.
14. SURFACE FINISH:
- (X) IMMERSION GOLD (ENIG) 1.58-3.94 MICRO INCHES OVER 118-236 MICRO INCHES MIN. OF ELECTROLESS NICKEL PER IPC-4552
 - () OSP (ORGANIC SOLDERABILITY PRESERVATIVE)
 - () IMMERSION SILVER
 - () SOFT WIRE BONDABLE GOLD 30-50 MICRO INCHES OF SOFT WIRE
 - BONDABLE GOLD OVER 100-150 MICRO INCHES OF NICKEL
 - () EDGE CONNECTOR FINGERS ARE TO BE PLATED WITH 100 MICRO-INCHES(.0001") OF LOW STRESS NICKEL UNDER 30 MICRO-INCHES (.0003") OF GOLD
 - () OTHER _____
15. SOLDERMASK:
- SOLDERMASK OVER BARE COPPER OR BARE GOLD (BOTH SIDES) TO MEET IPC-SM-840.
- IF PRESENT,DO NOT MODIFY SOLDERMASK DEFINED PADS (MASK OPENINGS LESS THAN COPPER PAD) WITHOUT APPROVAL.
- (X) LPI
 - () OTHER _____
- COLOR
- (X) GREEN
 - () OTHER _____
16. APPLY SILKSCREEN TO BOTH SIDES USING A NON-CONDUCTIVE, EPOXY BASED INK PER ARTWORK.
- (X) WHITE
 - () OTHER _____

TESTING:

17. FINAL ELECTRICAL TEST TO BE PERFORMED USING PROVIDED IPC-D-356A NETLIST OR ODB++ FORMAT FILE.
THE PCB SHALL HAVE A VERIFICATION STAMP.
18. A TIME DOMAIN REFLECTOMETER REPORT (TDR) FOR EACH IMPEDANCE CONTROLLED LAYER & A CERTIFICATE OF COMPLIANCE SHALL BE PROVIDED BY VENDOR AT TIME OF SHIPMENT. INSTANCES WHERE TDR TESTING CAN'T BE PERFORMED BECAUSE THE TRACE LENGTH IS TOO SHORT ON THE OUTER LAYERS AT THE PIN ESCAPES IS ACCEPTABLE. ALL OTHER INSTANCES MUST BE REPORTED.

MISCELLANEOUS:

19. IF PRESENT, ALL BLIND/BURIED VIAS WITH AN ASPECT RATIO <1:1 TO BE PLATED SHUT WITH COPPER WHEN USED AS VIA-IN-PAD OR AS A STACKED VIA. BLIND/BURIED VIAS WITH AN ASPECT RATIO >1:1 TO BE FILLED WITH NON-CONDUCTIVE EPOXY.
20. FOR VIA FILL INFORMATION REFER TO DRILL CHART:
- (X) NON-CONDUCTIVE EPOXY FILL ALL VIA IN PAD DRILLED VIAS
 - () COPPER FILL ALL 0.XXXX INCHES DRILLED VIAS
21. INTENTIONAL SHORTS:
- IF AN INTENTIONAL SHORT REPORT IS SUPPLIED AND DOES NOT MATCH THE FAB DATA THEN ADI APPROVAL IS REQUIRED.
22. PEMNUTS:
- () PEMNUTS TO BE INSTALLED BY FABRICATOR
 - () PEMNUTS NOT TO BE INSTALLED BY FABRICATOR
 - (X) NOT APPLICABLE
23. MANUFACTURER TO ETCH/STAMP WITH PERMANENT NON-CONDUCTIVE INK ON SECONDARY SIDE UNLESS OTHERWISE SPECIFIED:
- A. UL CODE-FLAMMABILITY RATING FOR THOSE APPROVED MATERIALS(IF APPLICABLE)
 - B. DATE CODE
 - C. LOT NUMBER
 - D. MANUFACTURER LOGO
25. PANELIZATION:
- BOARDS TO BE SHIPPED IN ARRAY AND KEPT INTACT
- PANEL TO BE SUBJECTED TO CUSTOMERS APPROVAL
- PANEL SOLDER PASTE STENCIL GERBER TO BE PROVIDED TO ANALOG
27. MINIMUM DESIGN LINE WIDTH IS .006 INCH.
28. MINIMUM DESIGN LINE SPACING IS .006 INCH.

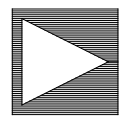
FAB NOTES REVISION: 2ND NOVEMBER 2022

LAMINATION DIAGRAM

LAYER NUMBER	LAYER NAME	COPPER THICKNESS (OZ, INCH)	DIELECTRIC THICKNESS (INCH)	MATERIALS
1	TOP	2 OZ, 0.0028"		FINAL CU (THICKNESS AFTER PLATING)
				ISOLA 370HR/EQUIVALENT
2	LAYER_2	2 OZ, 0.0028"		CU CLAD
				ISOLA 370HR/EQUIVALENT
3	LAYER_3	2 OZ, 0.0028"		CU CLAD
				ISOLA 370HR/EQUIVALENT
4	LAYER_4	2 OZ, 0.0028"		CU CLAD
				ISOLA 370HR/EQUIVALENT
5	LAYER_5	2 OZ, 0.0028"		CU CLAD
				ISOLA 370HR/EQUIVALENT
6	LAYER_6	2 OZ, 0.0028"		CU CLAD
				ISOLA 370HR/EQUIVALENT
7	LAYER_7	2 OZ, 0.0028"		CU CLAD
				ISOLA 370HR/EQUIVALENT
8	BOTTOM	2 OZ, 0.0028"		FINAL CU (THICKNESS AFTER PLATING)

THE FINISHED PCB THICKNESS TO BE: 0.062" +/-10%

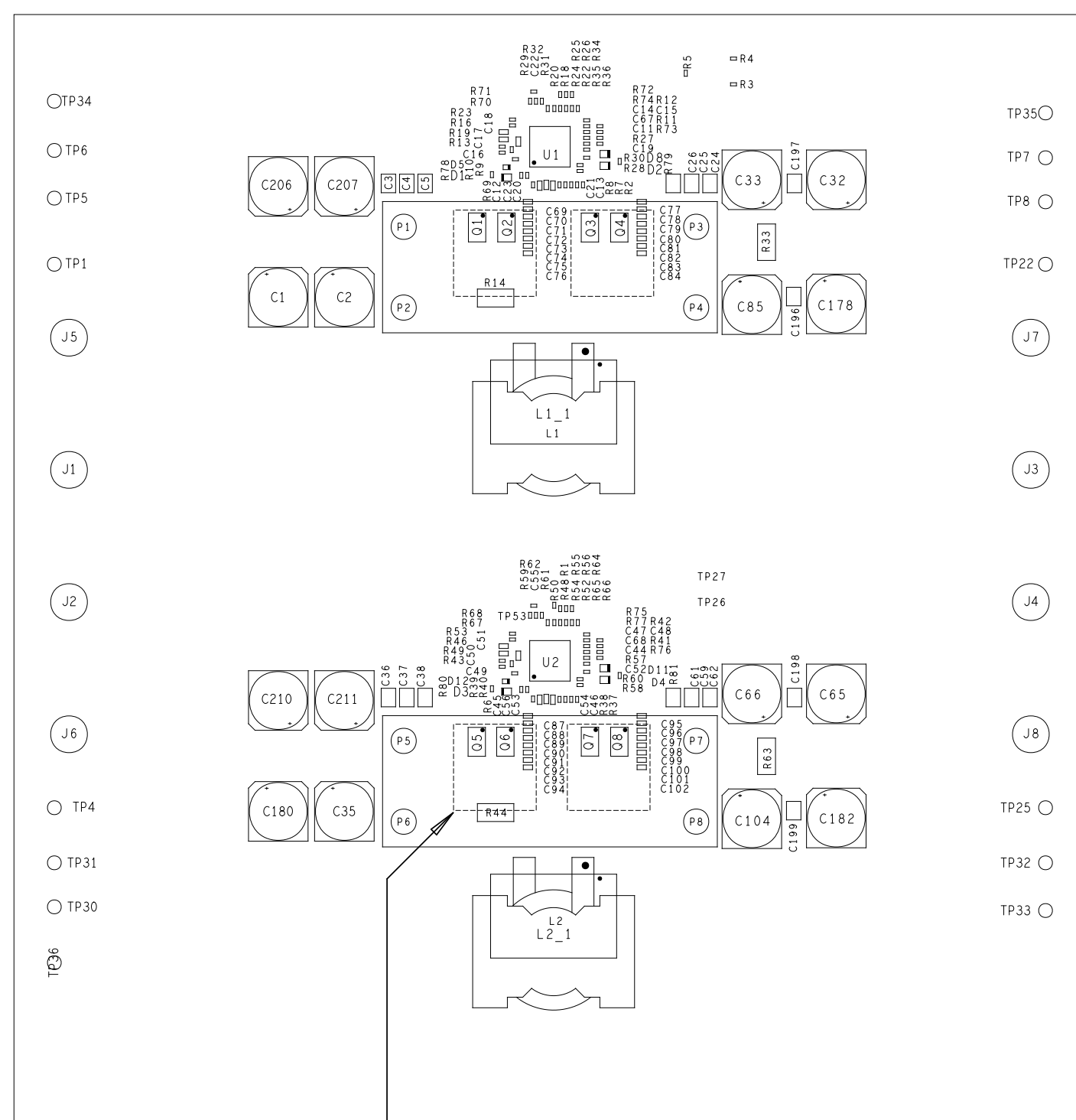
PRIMARY SIDE



**ANALOG
DEVICES**

**WWM
DIVISION**
804 WOBURN STREET
WILMINGTON, MA 01887

SIZE	FSCM NO	DRAWING NUMBER	REV
C	24355	09-084361	C
SCALE	1/1		SHEET 2 OF 2



DO NOT INSTALL

DO NOT INSTALL

APPLY THERMAL PAD TO (4x) Q1&Q2, Q3&Q4, Q9&Q10, Q11&Q12

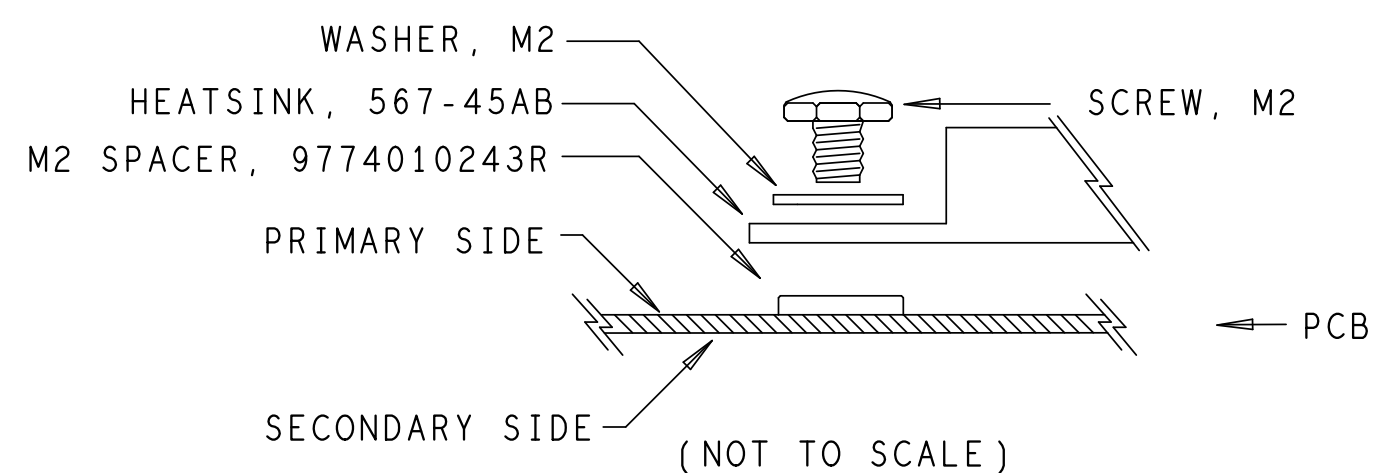
HEATSINK HARDWARE (LOCATION P1-P4 & P5-P8):

-Wakefield-Vette, 567-45AB

- SCREW, M2x0.4, 5mm, PAN HEAD, PHILLIPS

- WASHER, M2

- THERMAL INTERFACE PAD. T-GLOBAL, TG-A6200-15-15-1.0

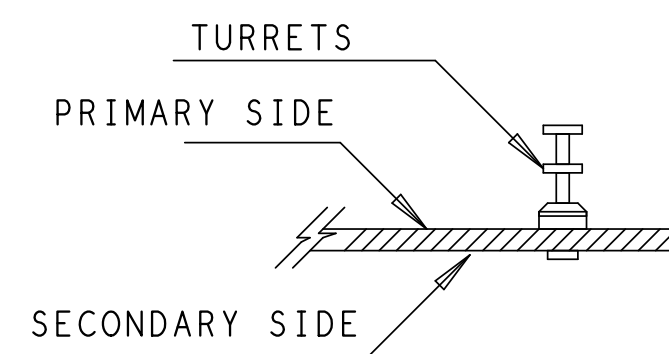
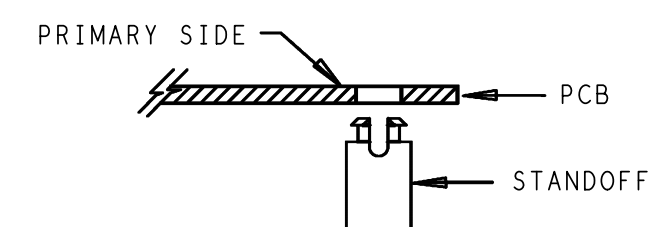


REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
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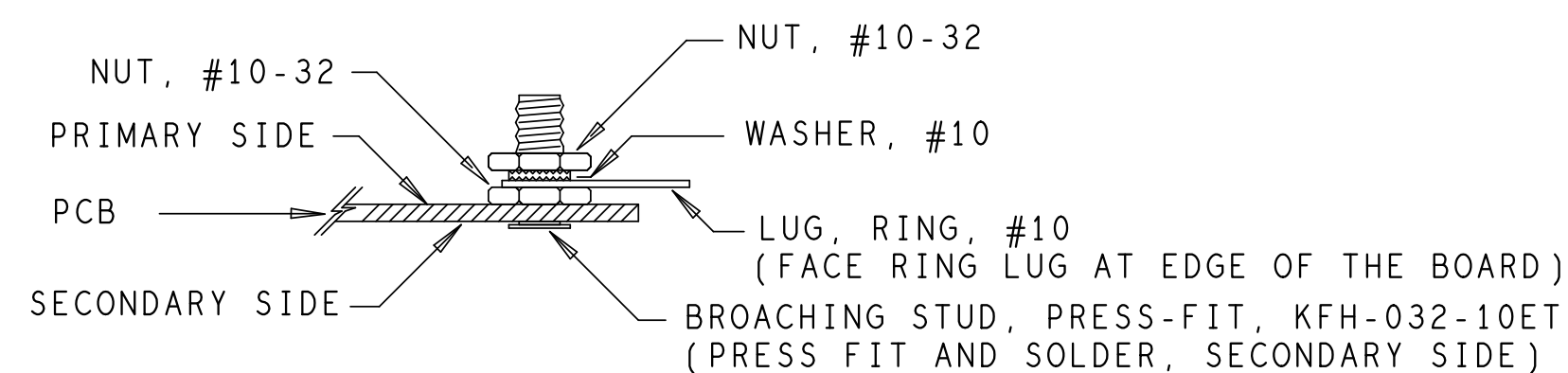
ASSEMBLY NOTES:

1. BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).
2. REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.
3. REPAIRS ARE NOT ALLOWED IN SOLDERMASK FREE AREAS ON EITHER SIDE OF THE BOARD.

INSTALL STANDOFFS AS SHOWN BELOW:

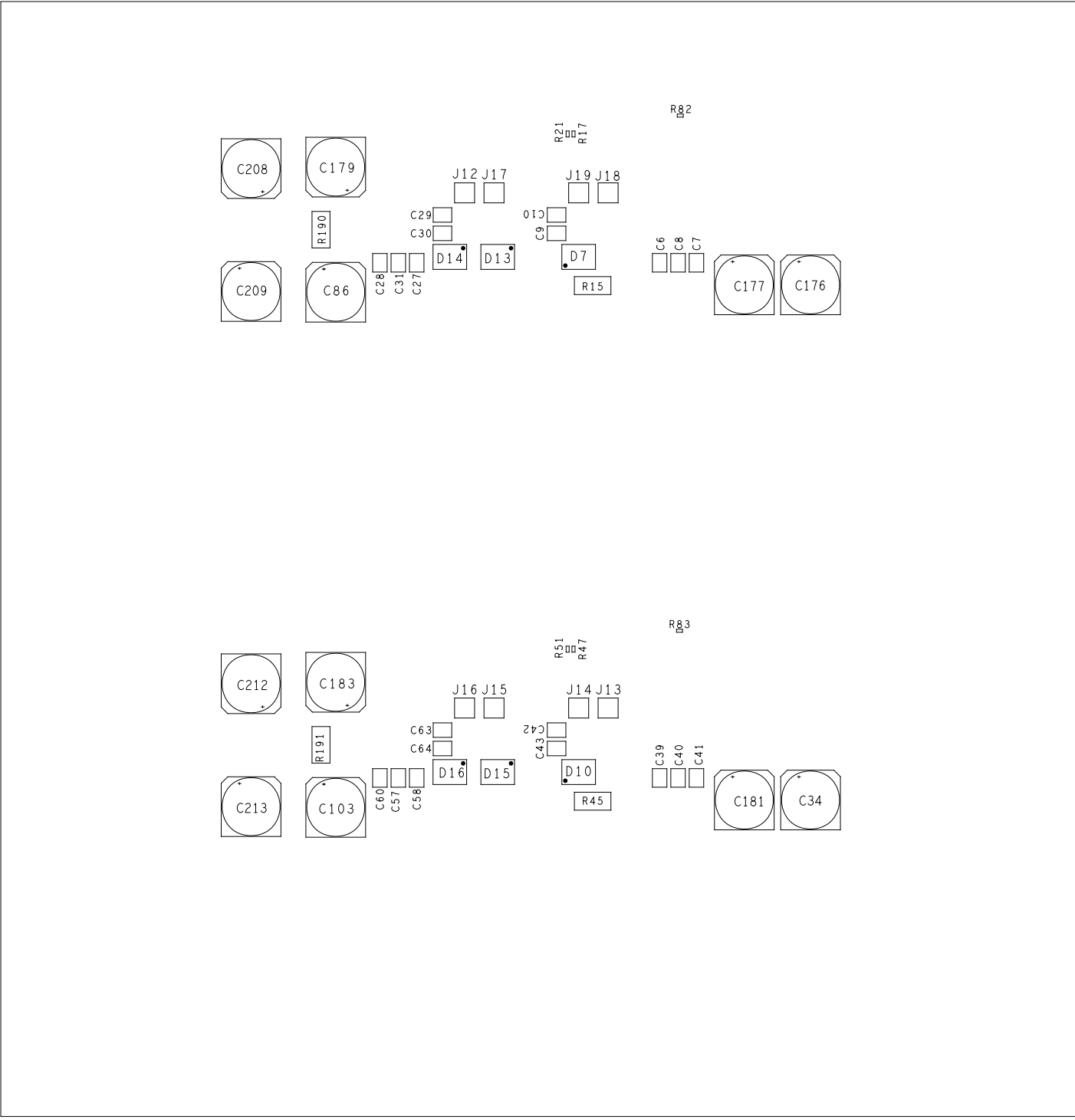


J1 - J8



PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES			APPROVAL		DATE		 ANALOG DEVICES		WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887		
TOLERANCES			TEMPLATE ENGINEER X		ddMMMyy		TITLE ASSEMBLY EVAL-LT7930-AZ				
DECIMALS FRACTIONS ANGLES			HARDWARE SERVICES X		ddMMMyy						
.XX --.010 --1/32 -- 2			HARDWARE SYSTEMS X		ddMMMyy						
.XXX --.005			TEST ENGINEER X		ddMMMyy						
.XXXX --.0050			COMPONENT ENGINEER X		ddMMMyy						
MATERIAL			TEST PROCESS X		ddMMMyy						
			HARDWARE RELEASE X		ddMMMyy						
			DESIGNER CCORREA		03FEB26						
			PTD ENGINEER RGOVIND2		03FEB26						
FINISH			CHECKER X		ddMMMyy						
					C 24355 01-084361		C				
DO NOT SCALE DWG					SCALE 1/1		SHEET 1 OF 2				



SECONDARY SIDE

