

8

7

6

5

4

3

2

1

THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.

RELAY CONTROL CHART

CONTROL	CODE	DEVICE	FUNCTION	CONNECTOR

JUMPER TABLE		
JP#	ON	OFF
1		
2		
3		
4		
5		

* SEE ASSEMBLY INSTRUCTIONS

REVISIONS

REV	DESCRIPTION	DATE	APPROVED

SCHEMATIC

TEMPLATE ENGINEER
-

HARDWARE SERVICES
-

HARDWARE SYSTEMS
-

TEST ENGINEER
-

COMPONENT ENGINEER
-

TEST PROCESS
-

HARDWARE RELEASE
-

DESIGNER
-

PTD ENGINEER
-

CHECKER
-

DATE

HW TYPE : Customer Evaluation

Product(s): ADRV903x

: N/A

PACKAGE : LeadCount-lead BodySize PackageType-family

: Pitch-pitch StyleVendor Style

Low Band

ADRV903X-LB/PCBZ

600 MHz to 2800 MHz

MASTER PROJECT TEMPLATE
TBD

TESTER TEMPLATE
no_template

DRAWING NO.
02_064540-06

REV.
C

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES

TOLERANCES

DECIMALS
X.XX +-0.010
X.XXX +-0.005

FRACTIONS
+1/32

ANGLES
+2

SIZE
D

SCALE
1:1

CODE ID NO.
CodeID

SHEET 1 OF 16

A

B

C

D

8

7

6

5

4

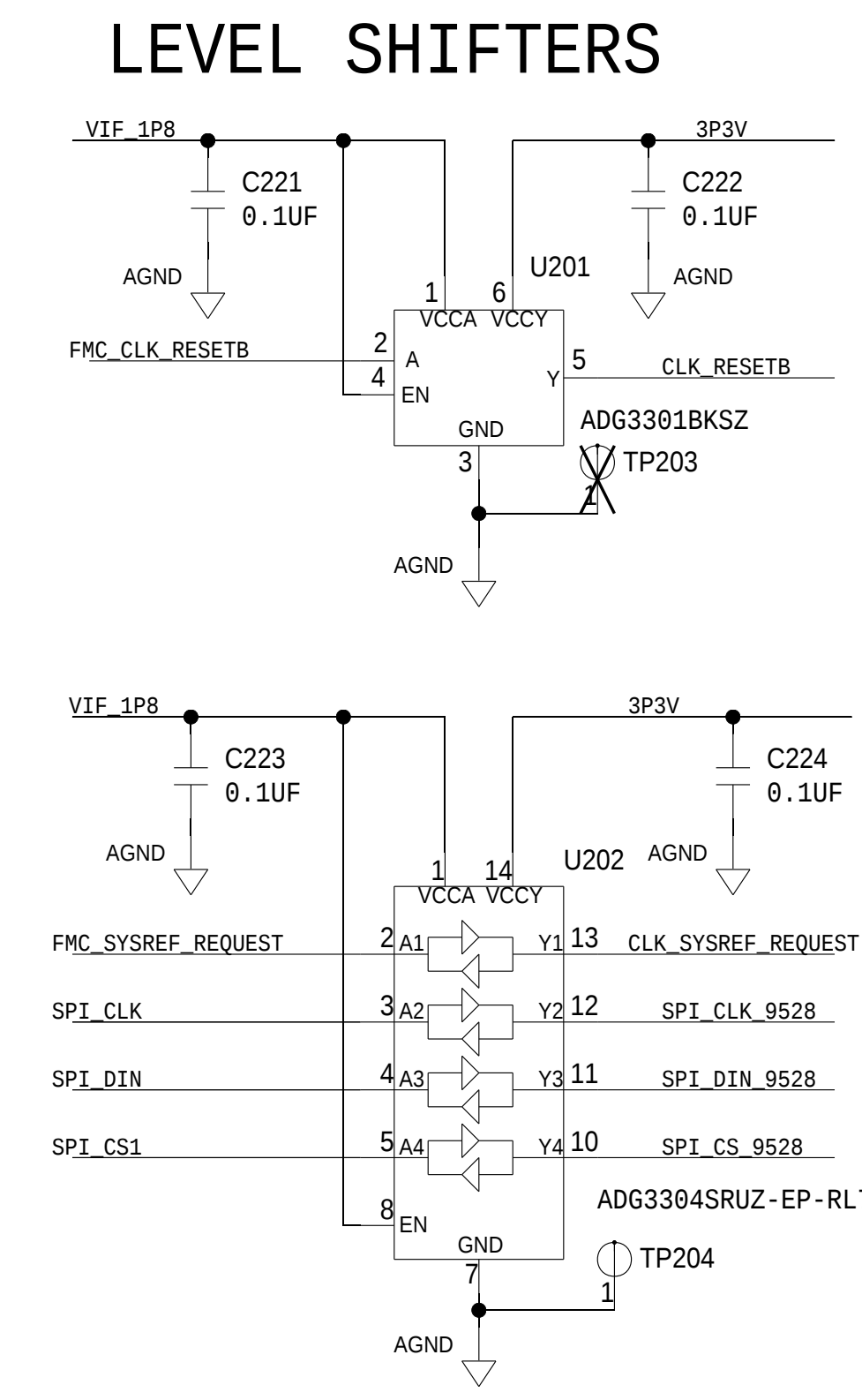
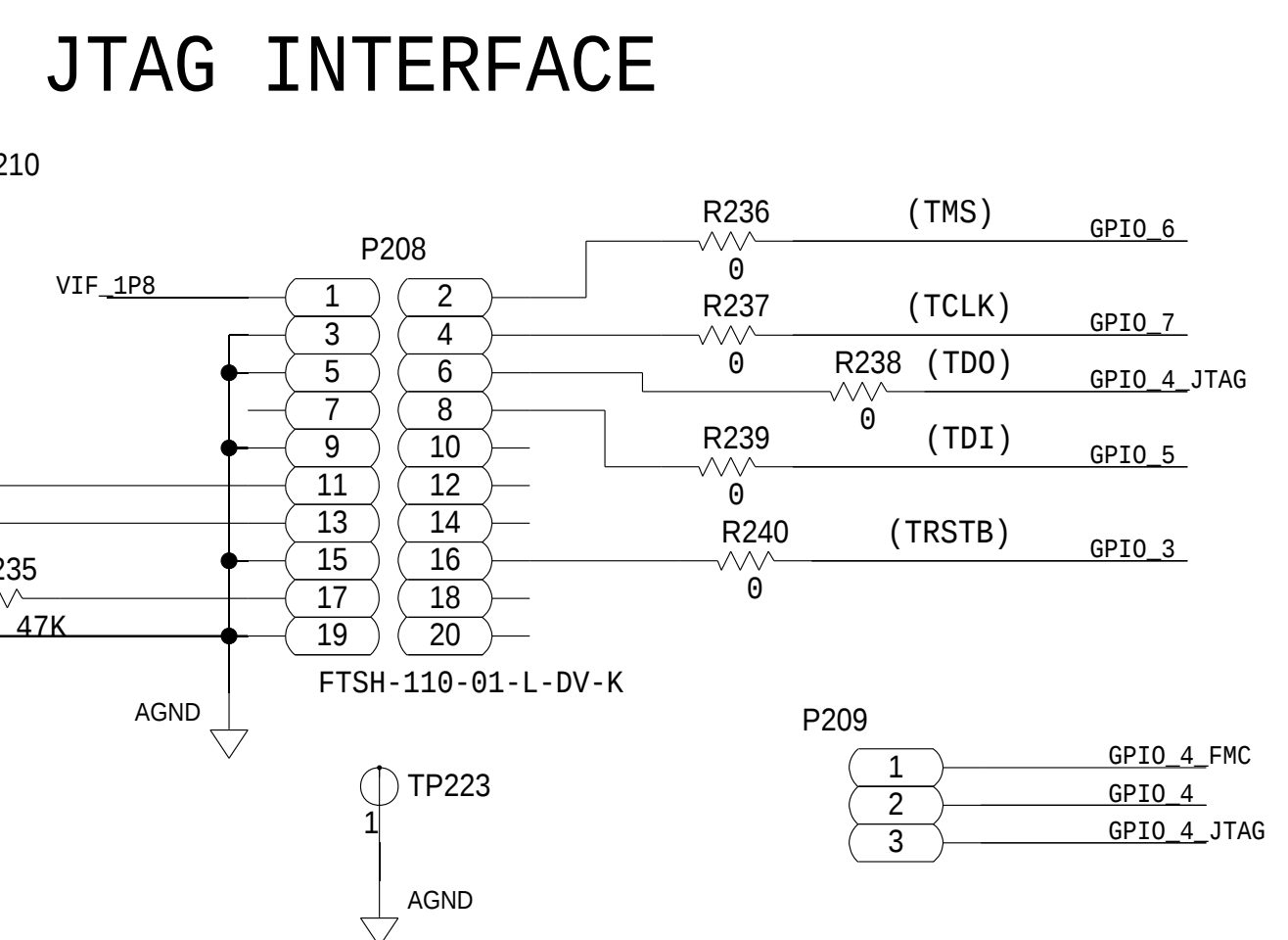
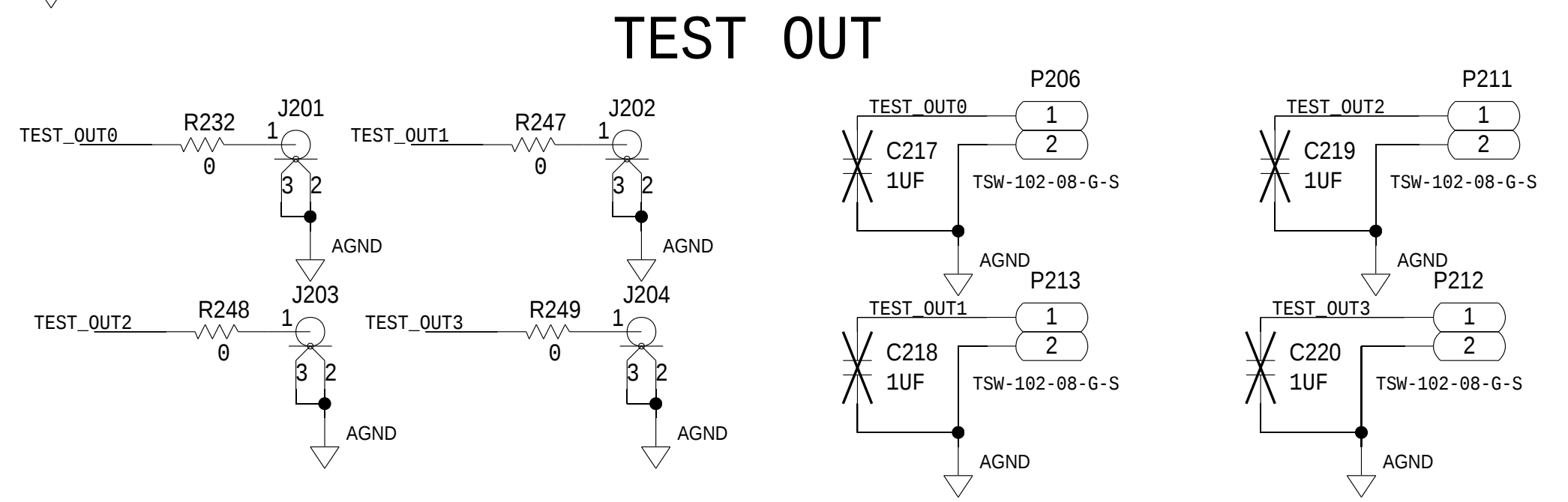
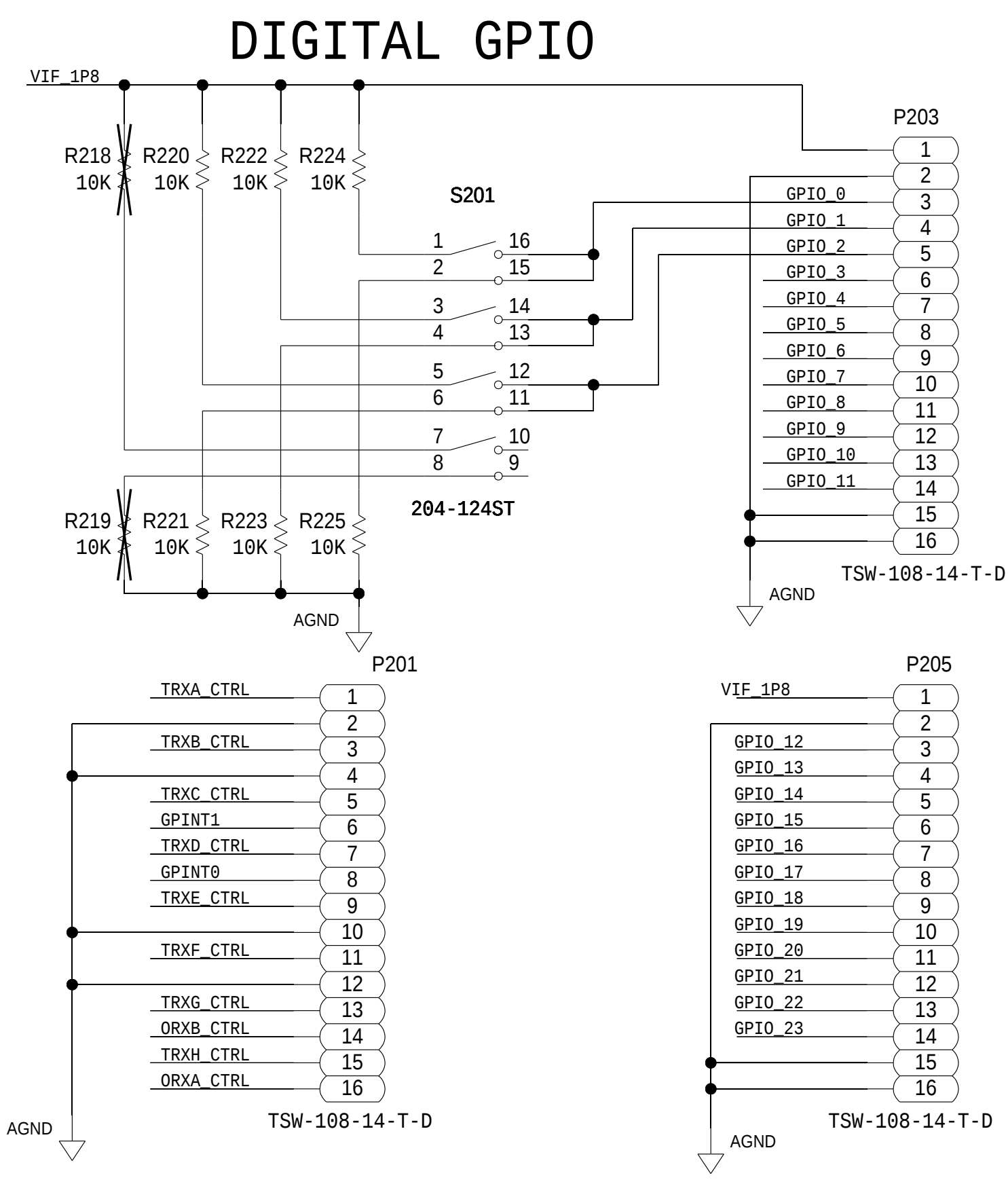
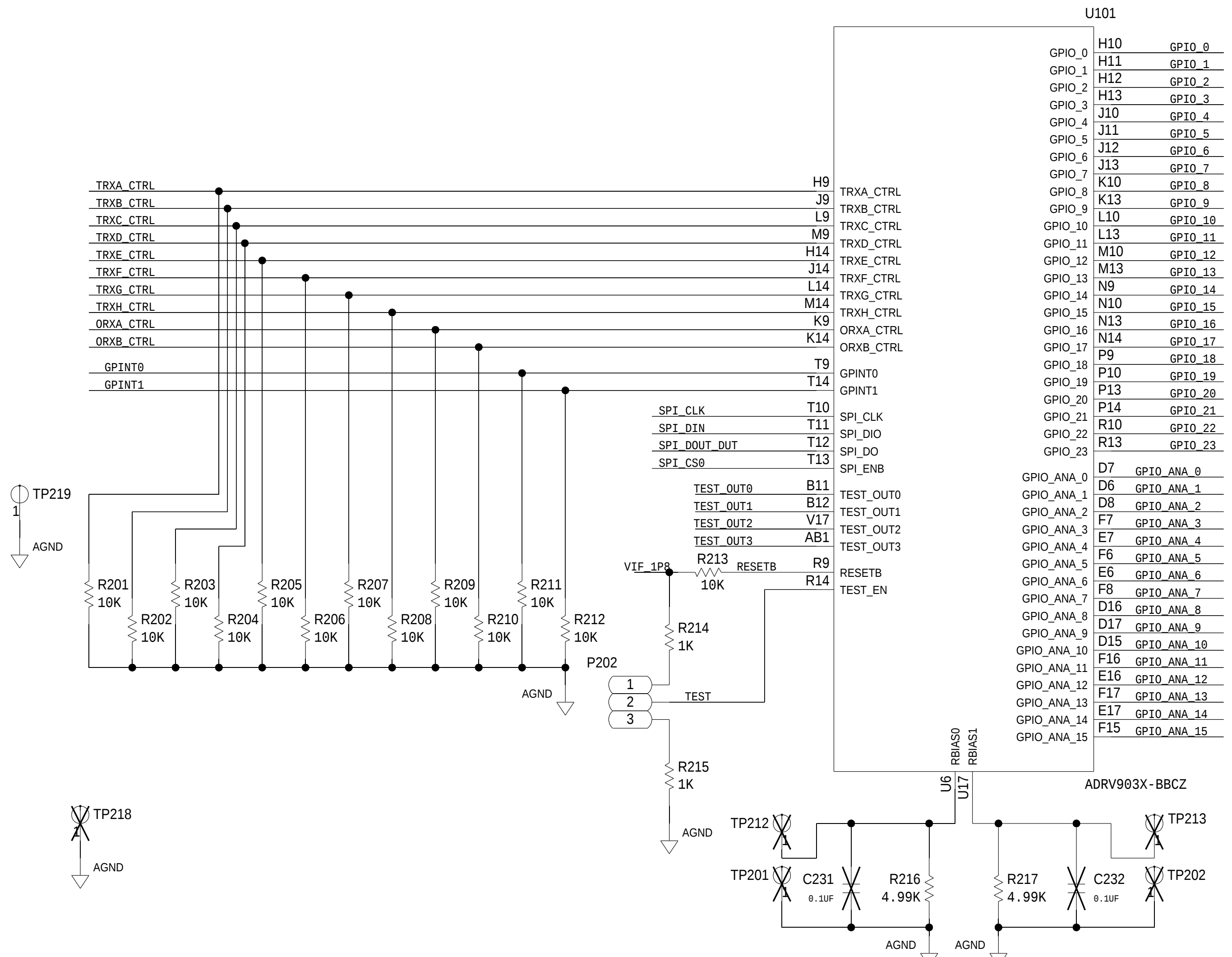
3

2

1

DIGITAL/ANALOG I/O

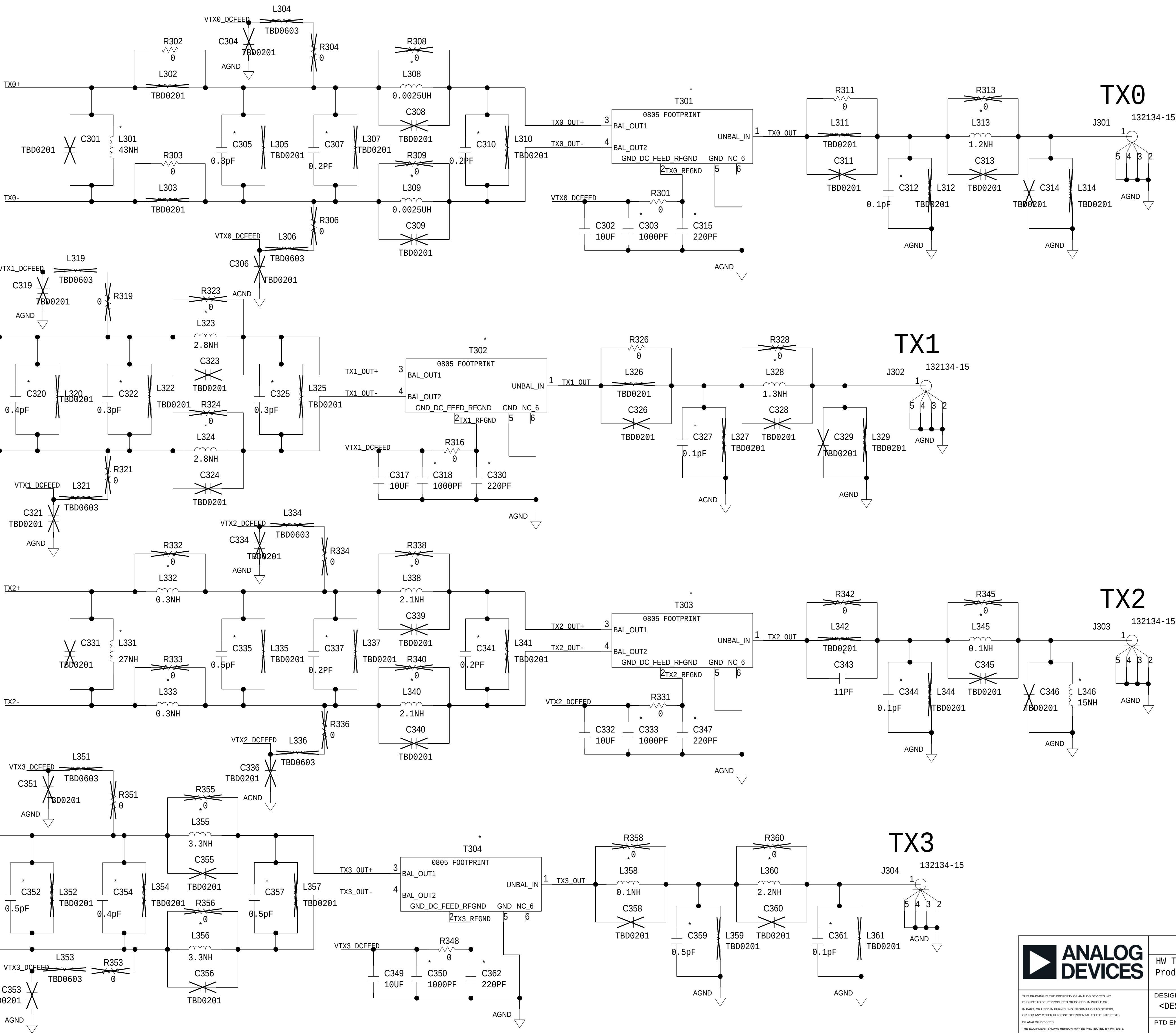
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



TRANSMITTER RF OUTPUT

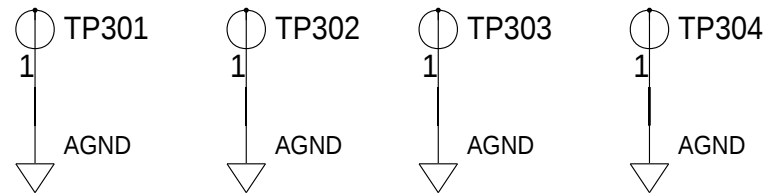
EVB MATCH HAS BEEN DESIGNED TO SUPPORT THE WIDEST MATCH POSSIBLE;
MATCHING TOPOLOGY COULD BE REDUCED/SIMPLIFIED DEPENDING ON THE SYSTEM REQUIREMENTS.

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

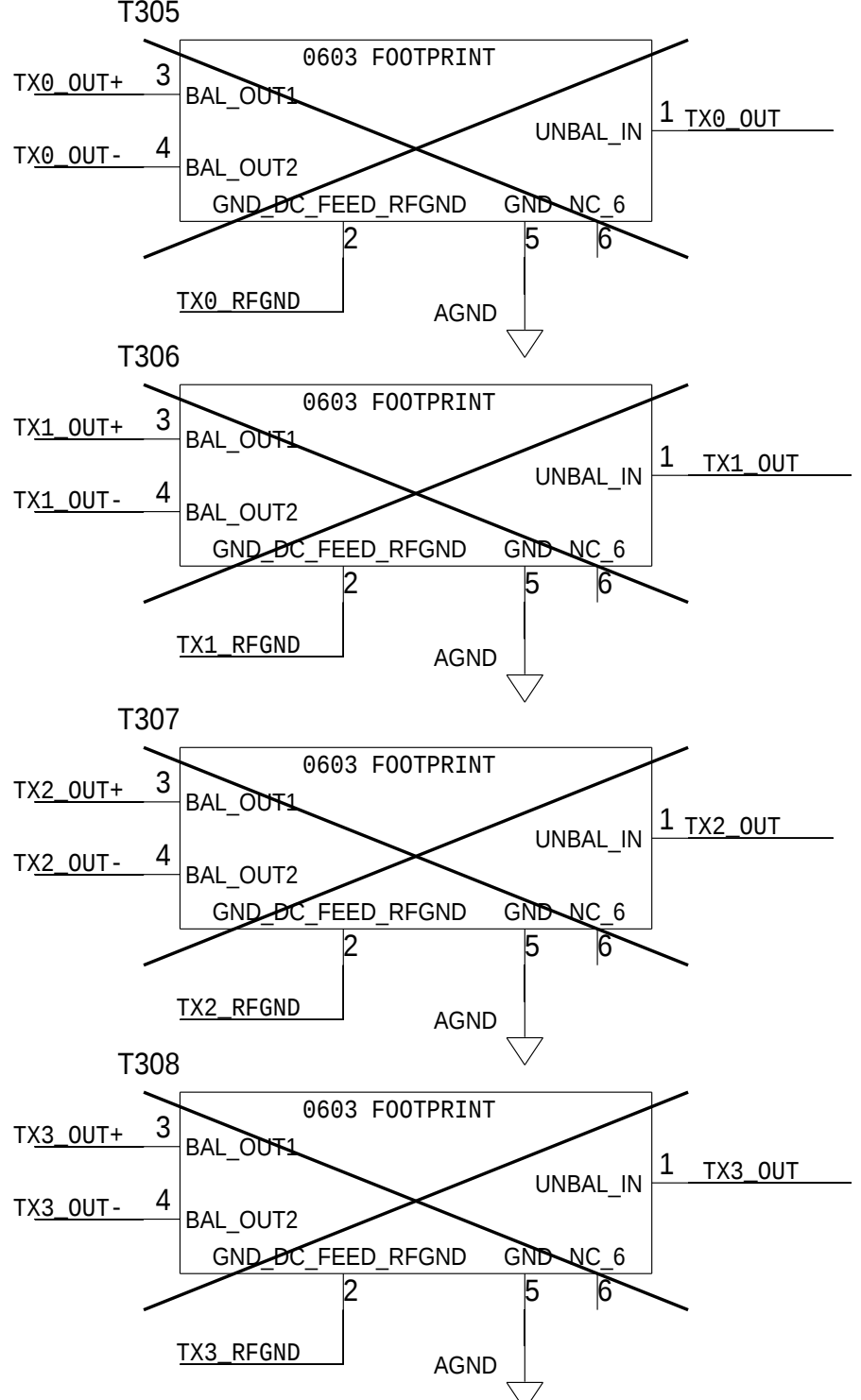


U101	
TX0P	C1 TX0+
TX0N	B1 TX0-
TX1P	G1 TX1+
TX1N	F1 TX1-
TX2P	R1 TX2+
TX2N	P1 TX2-
TX3P	W1 TX3+
TX3N	B22 TX4+
TX4P	C22 TX4-
TX4N	F22 TX5+
TX5P	G22 TX5-
TX5N	P22 TX6+
TX6P	R22 TX6-
TX7P	V22 TX7+
TX7N	W22 TX7-

ADRV903X-BBCZ



OVERLAP TX BALUNS

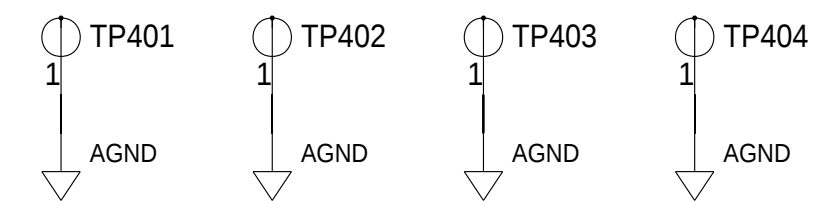
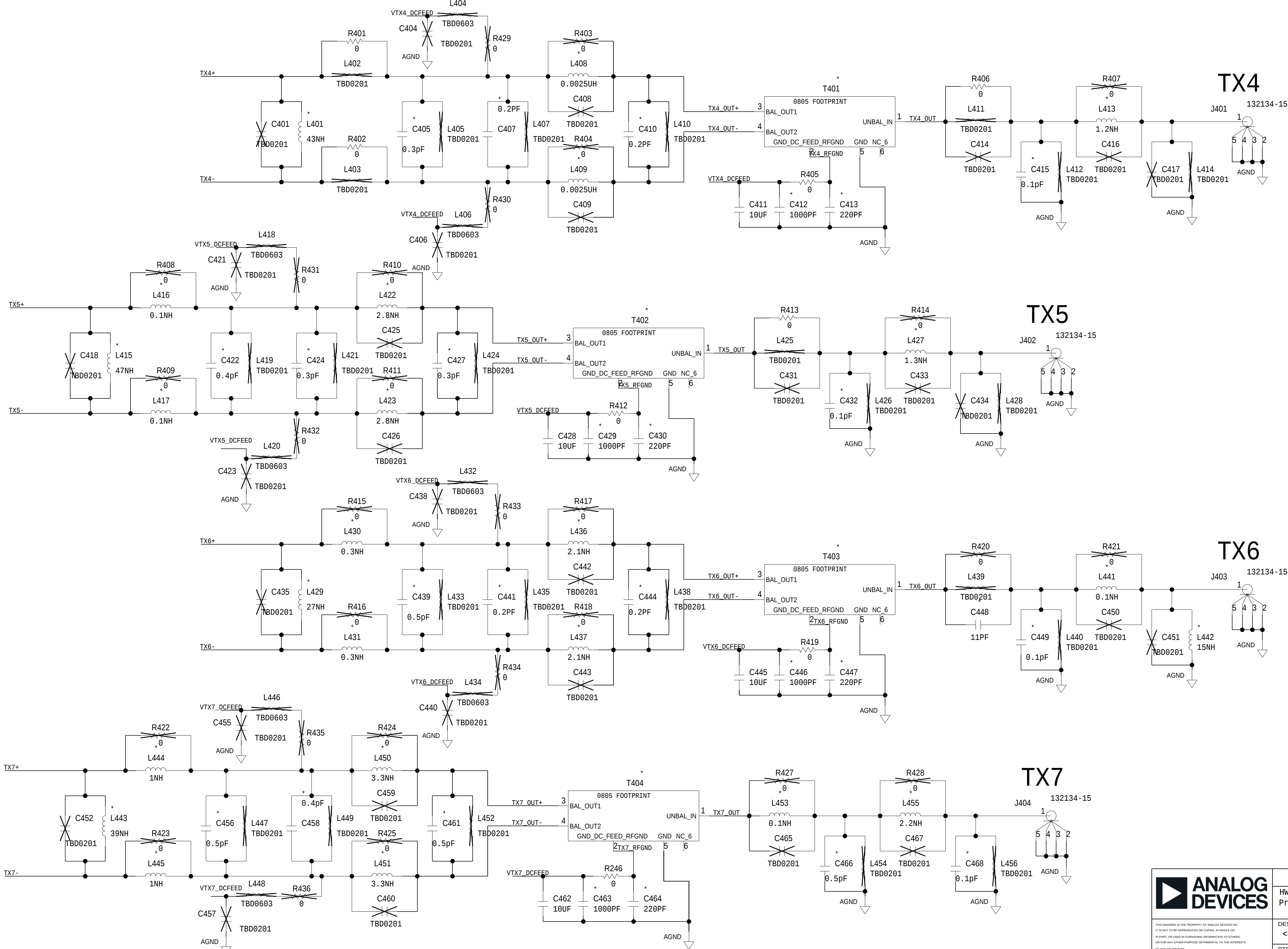


THIS DOCUMENT IS THE PROPERTY OF ANALOG DEVICES INC.
IT IS NOT TO BE REPRODUCED OR COPIED IN WHOLE OR
IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS,
OR FOR ANY OTHER PURPOSES DETRIMENTAL TO THE INTERESTS
OF ANALOG DEVICES.
THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS
OWNED OR CONTROLLED BY OWNED ANALOG DEVICES.

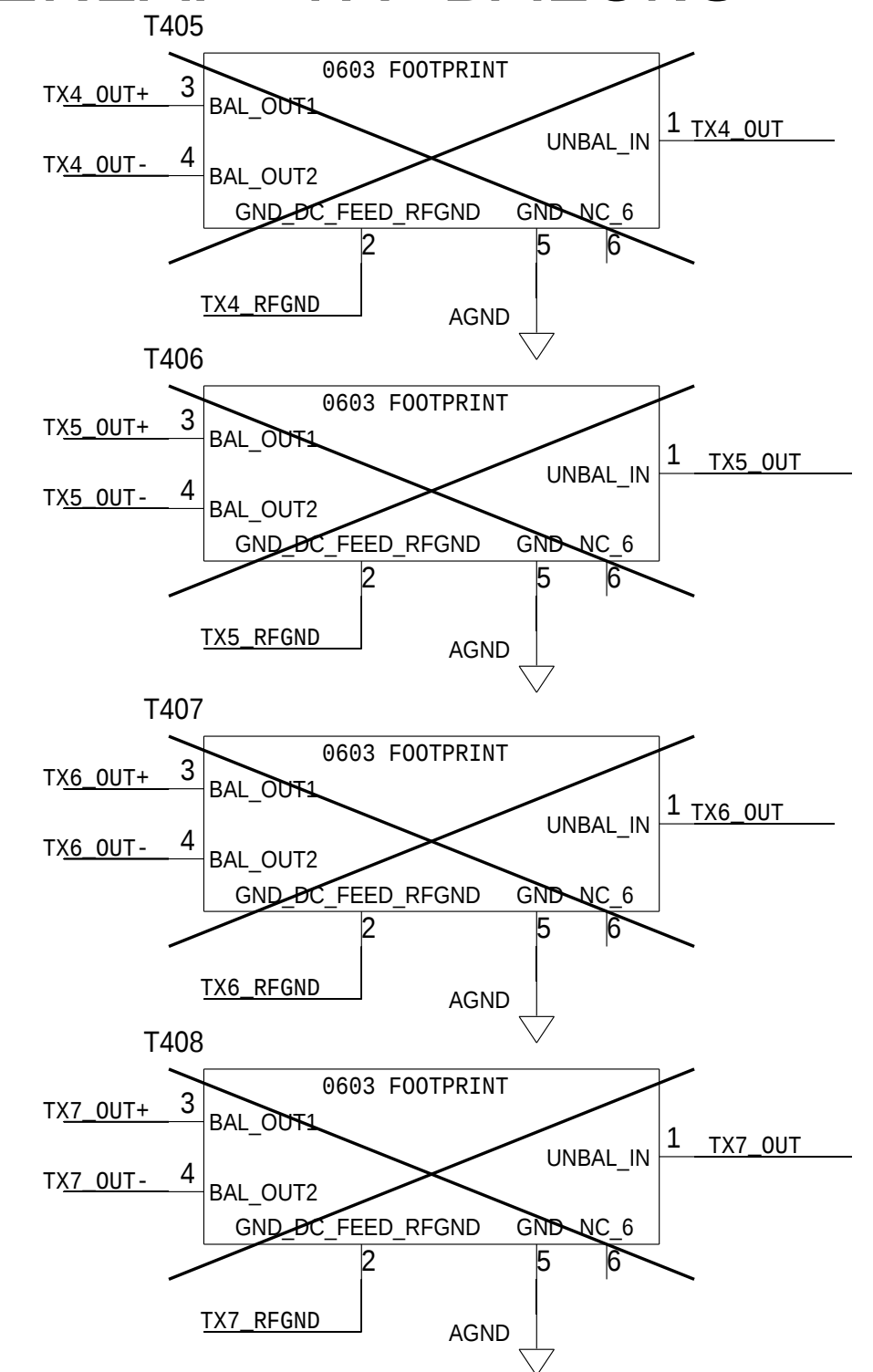
SCHEMATIC			
HW TYPE : Customer Evaluation Product(s) : ADRV903x : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064540-06	REV C	
PTD ENGINEER -	SIZE D	SCALE 1:1	SHEET 3 OF 16

EVb MATCH HAS BEEN DESIGNED TO SUPPORT THE WIDEST MATCH POSSIBLE;
MATCHING TOPOLOGY COULD BE REDUCED/SIMPLIFIED DEPENDING ON THE SYSTEM REQUIREMENTS.

REVISIONS			
NO	DESCRIPTION	DATE	APPROVED



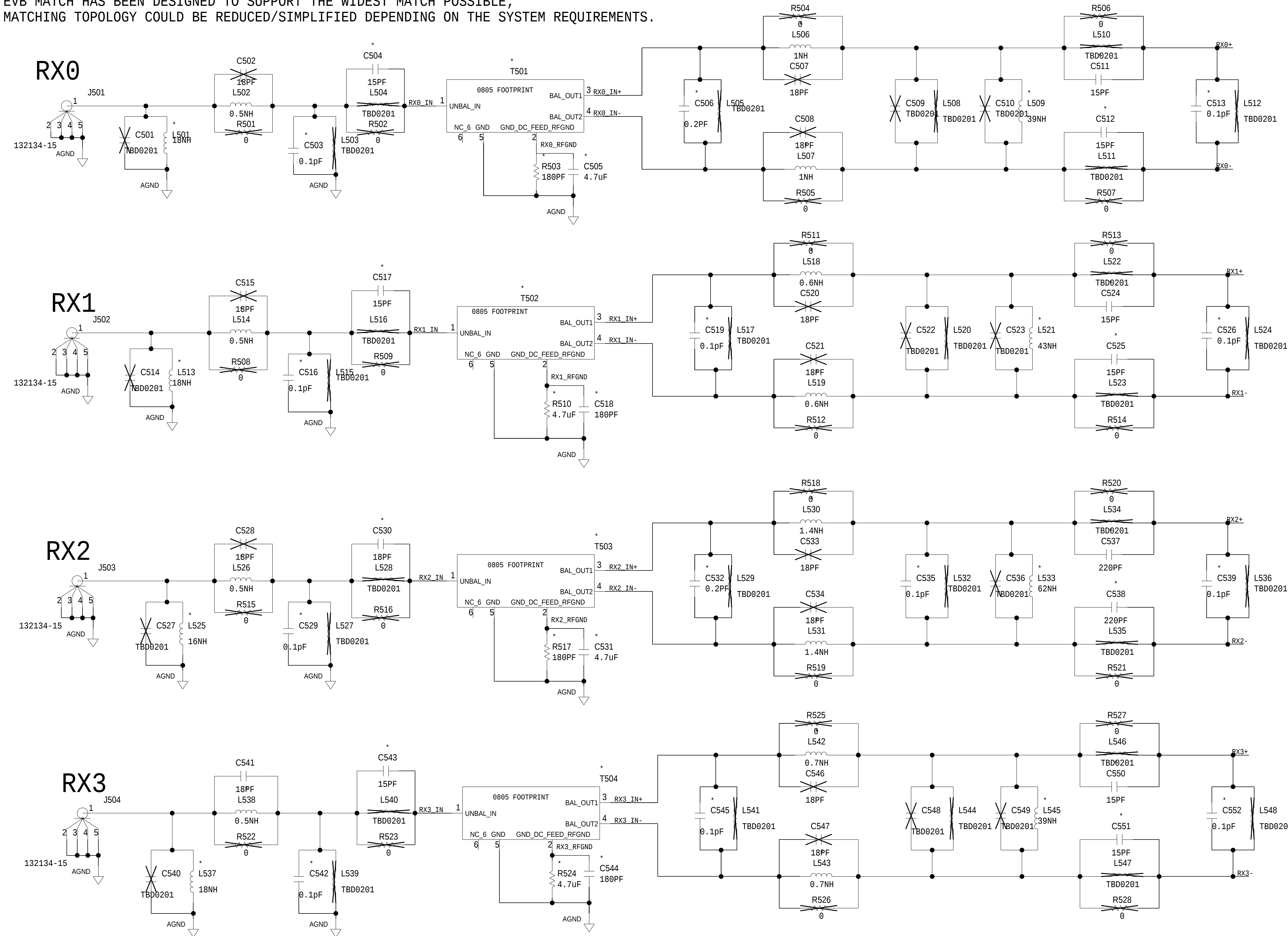
OVERLAP TX BALUNS



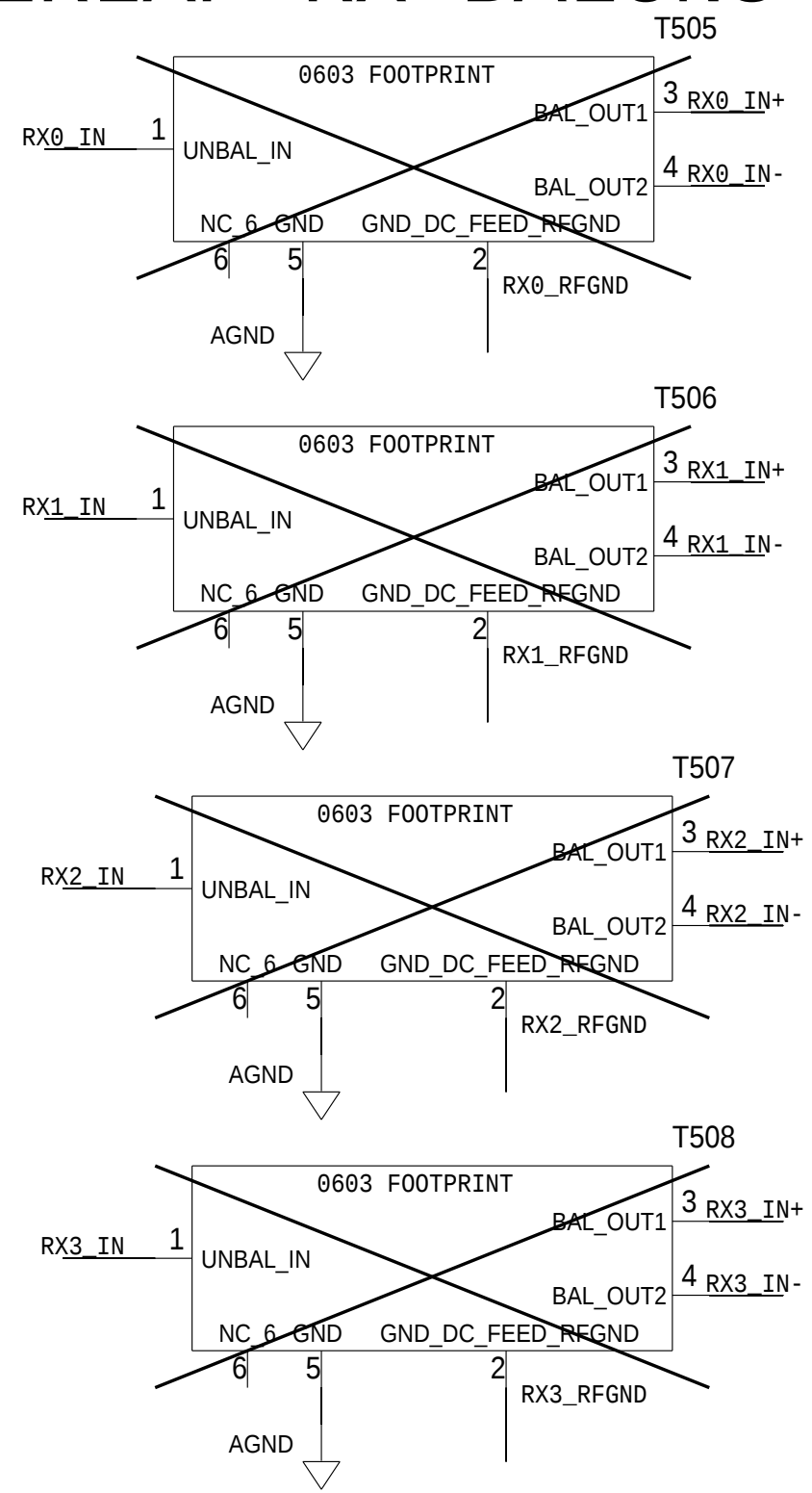
 ANALOG DEVICES	SCHEMATIC				
	HW TYPE : Customer Evaluation Product(s) : ADRV903X : N/A				
THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IF IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES.		DESIGN VIEW <DESIGN_VIEW>		DRAWING NO. 02_064540-06	REV C
THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY OWNED ANALOG DEVICES.		PTD ENGINEER -	SIZE D	SCALE 1:1	SHEET 4 OF 16

RECEIVER RF INPUT

EVb MATCH HAS BEEN DESIGNED TO SUPPORT THE WIDEST MATCH POSSIBLE;
MATCHING TOPOLOGY COULD BE REDUCED/SIMPLIFIED DEPENDING ON THE SYSTEM REQUIREMENTS.



OVERLAP RX BALUNS

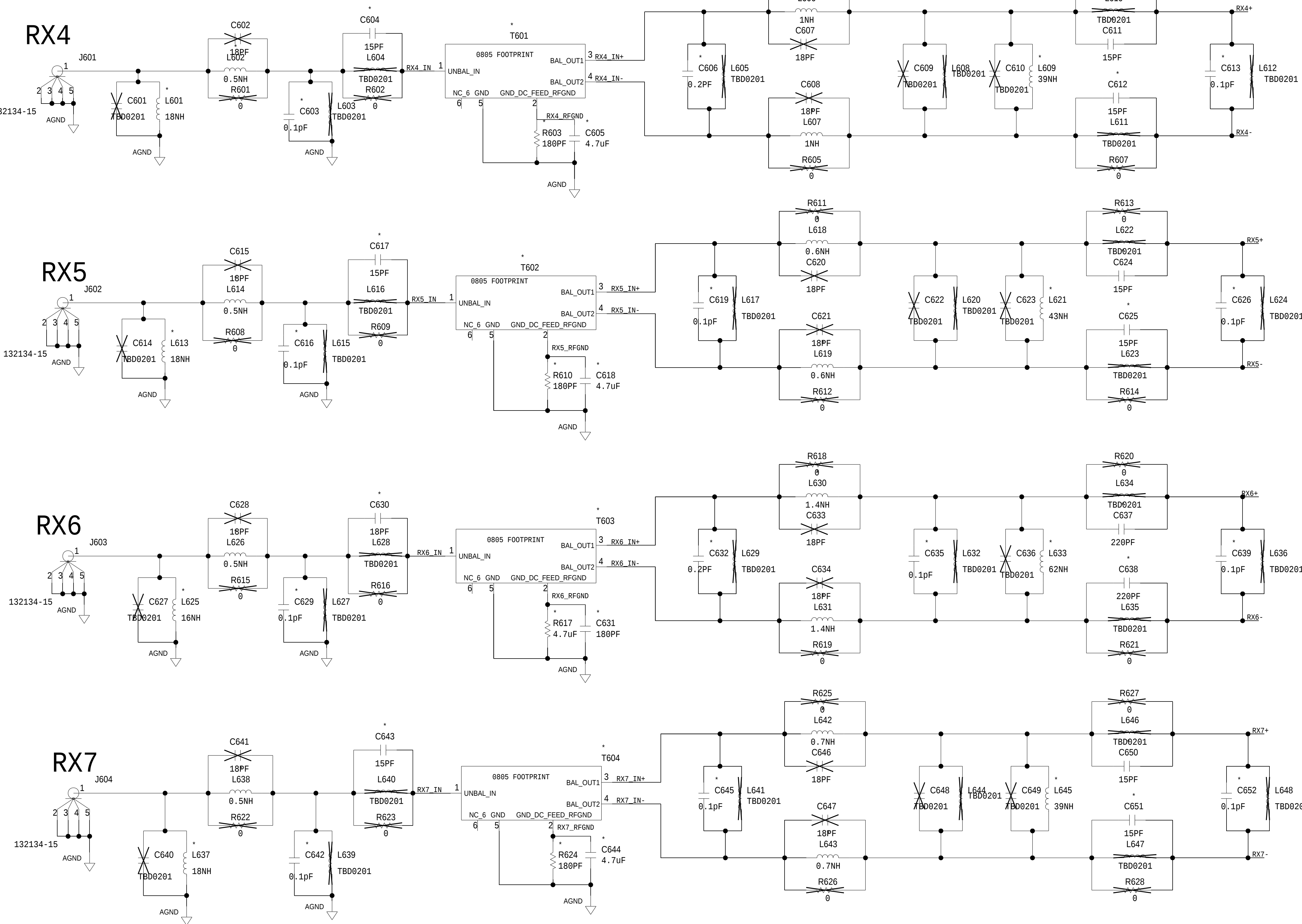


THIS DOCUMENT IS THE PROPERTY OF ANALOG DEVICES INC.
IT IS NOT TO BE REPRODUCED OR COPIED IN WHOLE OR
IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS,
OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS
OF ANALOG DEVICES.
THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS
OWNED OR CONTROLLED BY OWNED ANALOG DEVICES.

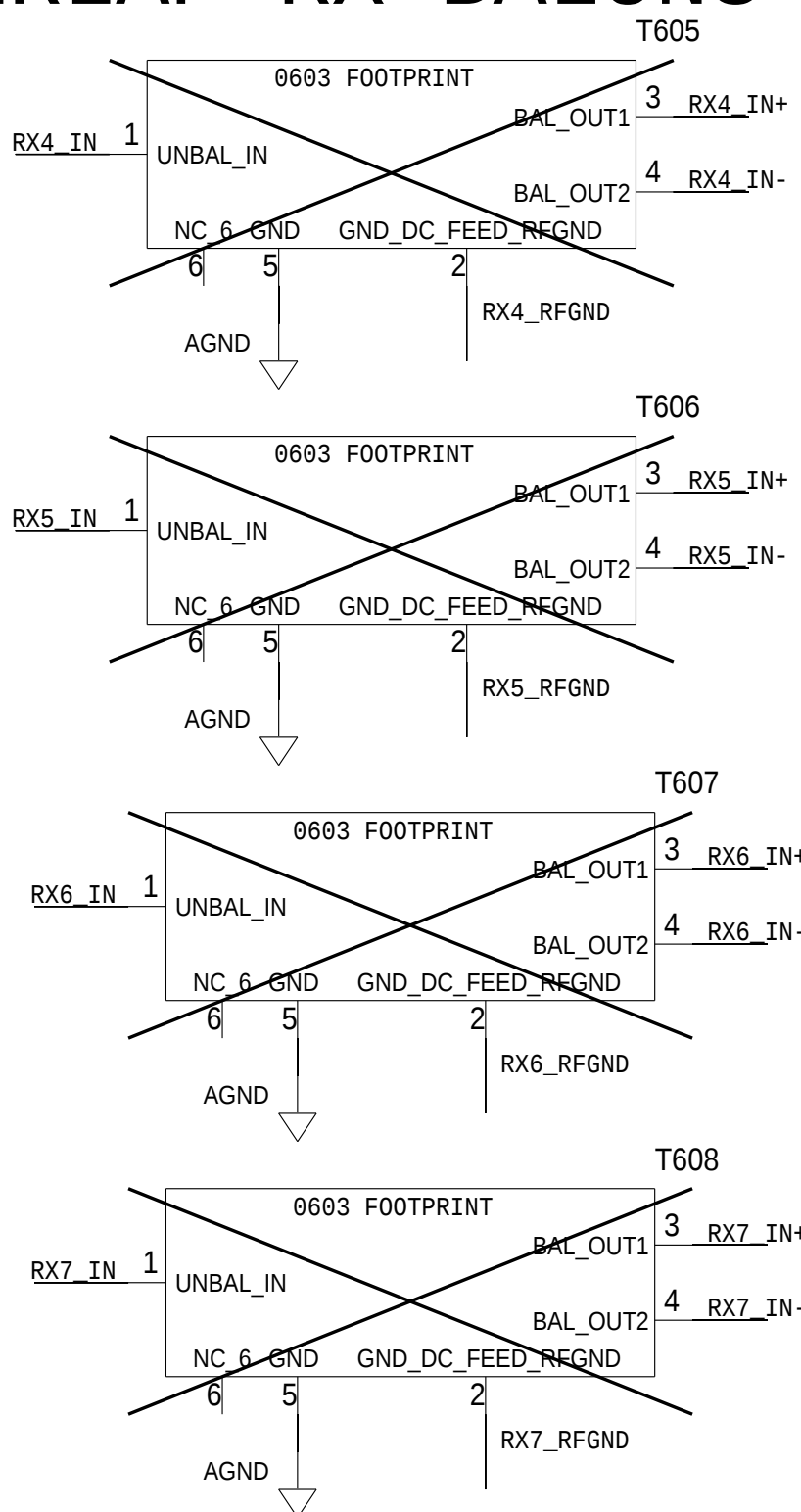
SCHEMATIC				
HW TYPE : Customer Evaluation				
Product(s): ADRV903x				
: N/A				
DESIGN VIEW	DRAWING NO.			REV
<DESIGN_VIEW>	02_064540-06			C
PTD ENGINEER	SIZE	SCALE	SHEET 5 OF 16	
-	D	1:1		

RECEIVER RF INPUT

EVb MATCH HAS BEEN DESIGNED TO SUPPORT THE WIDEST MATCH POSSIBLE;
MATCHING TOPOLOGY COULD BE REDUCED/SIMPLIFIED DEPENDING ON THE SYSTEM REQUIREMENTS.



OVERLAP RX BALUNS



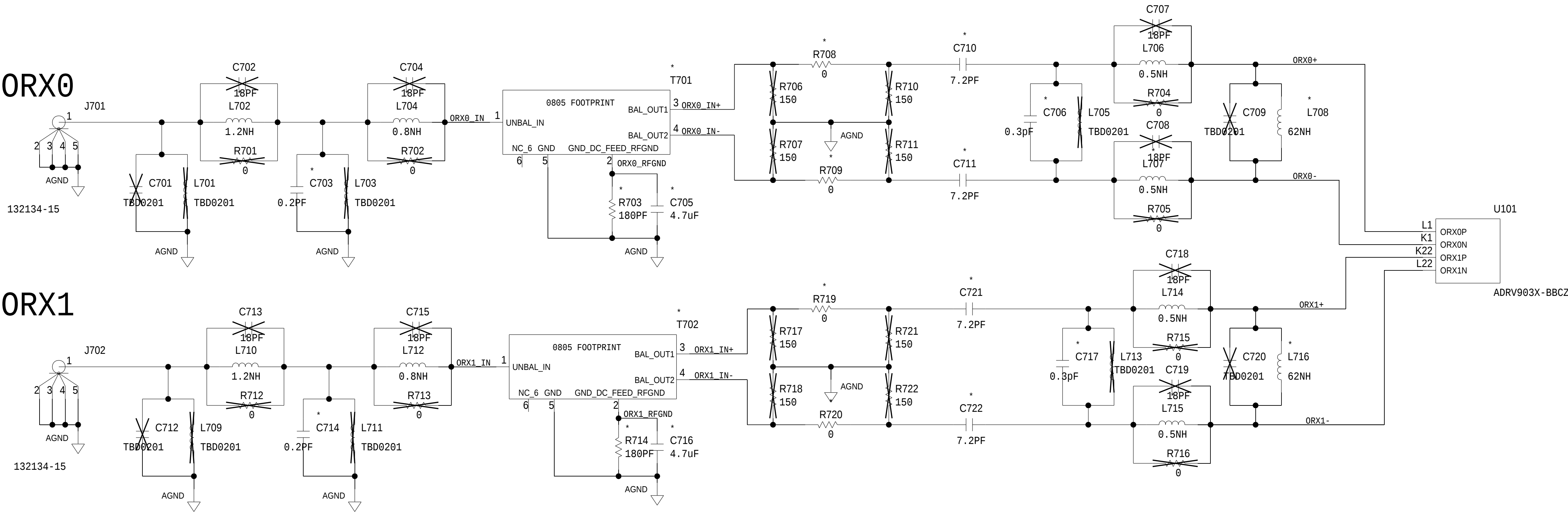
THIS DOCUMENT IS THE PROPERTY OF ANALOG DEVICES INC.
IT IS NOT TO BE REPRODUCED OR COPIED IN WHOLE OR
IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS,
OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS
OF ANALOG DEVICES.
THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS
OWNED OR CONTROLLED BY OWNED ANALOG DEVICES.

SCHEMATIC			
HW TYPE : Customer Evaluation			
Product(s): ADRV903x			
: N/A			
DESIGN VIEW	DRAWING NO.		REV
<DESIGN_VIEW>	02_064540-06		C
PTD ENGINEER	SIZE	SCALE	SHEET 6 OF 16
-	D	1:1	

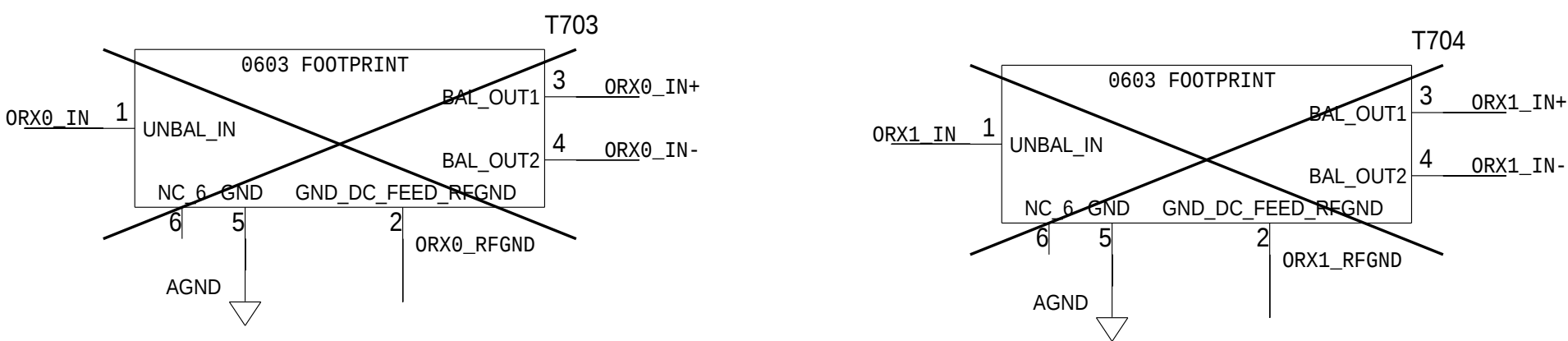
OBSERVATION RX RF INPUT

EVb MATCH HAS BEEN DESIGNED TO SUPPORT THE WIDEST MATCH POSSIBLE;
MATCHING TOPOLOGY COULD BE REDUCED/SIMPLIFIED DEPENDING ON THE SYSTEM REQUIREMENTS.

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



OVERLAP ORX BALUNS



THIS DOCUMENT IS THE PROPERTY OF ANALOG DEVICES INC.
IT IS NOT TO BE REPRODUCED OR COPIED IN WHOLE OR
IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS,
OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS
OF ANALOG DEVICES.
THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS
OWNED OR CONTROLLED BY OWNED ANALOG DEVICES.

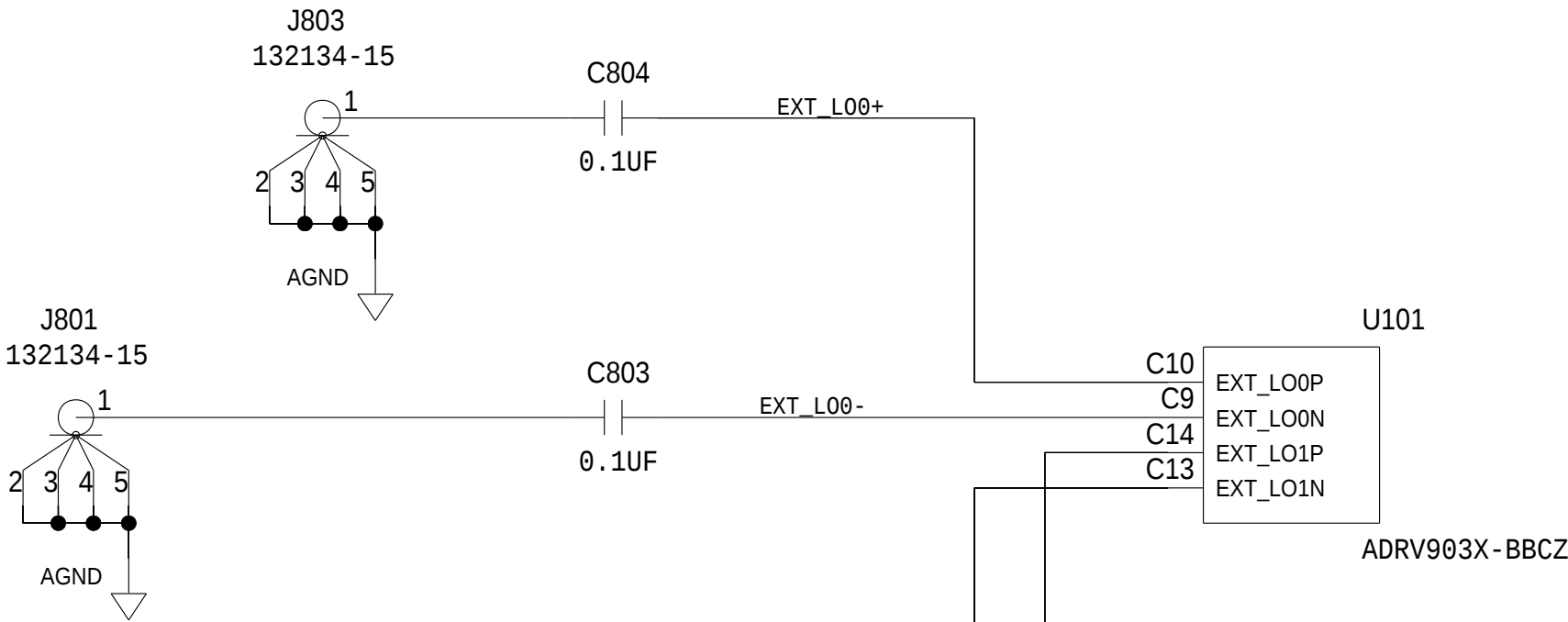
SCHEMATIC			
HW TYPE : Customer Evaluation Product(s): ADRV903x : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064540-06	REV C	
PTD ENGINEER -	SIZE D	SCALE 1:1	SHEET 7 OF 16

EXTERNAL L0 IN/OUT

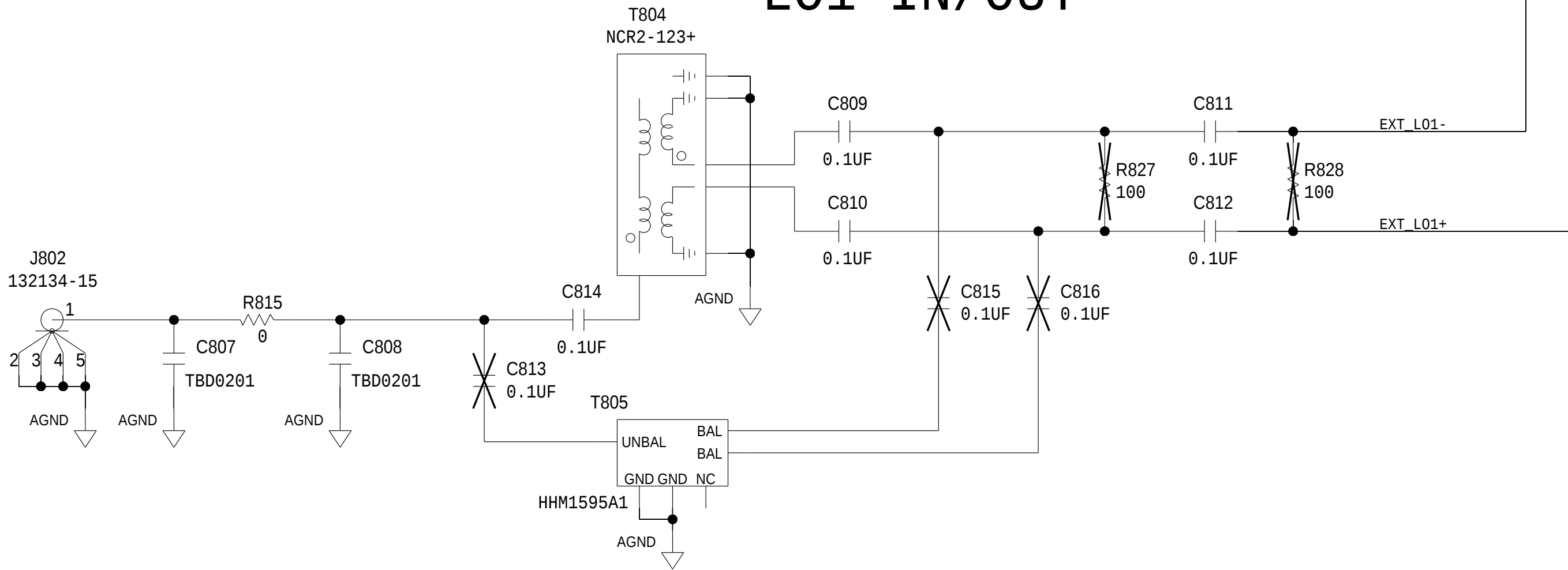
EVb MATCH HAS BEEN DESIGNED TO SUPPORT THE WIDEST MATCH POSSIBLE;
MATCHING TOPOLOGY COULD BE REDUCED/SIMPLIFIED DEPENDING ON THE SYSTEM REQUIREMENTS.

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

L00 IN/OUT



L01 IN/OUT



SCHEMATIC

HW TYPE : Customer Evaluation Product(s): ADRV903x : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064540-06	REV C	
PTD ENGINEER -	SIZE D	SCALE 1:1	SHEET 8 OF 16

CLOCK GENERATION/DISTRIBUTION

REVISIONS

REV	DESCRIPTION	DATE	APPROVED

EVB REF CLK

DEVCLK+	E11	DEVCLKP	EXT_CLKP	V12 EXT_CLK+
DEVCLK-	E12	DEVCLKN	EXT_CLKN	V11 EXT_CLK-
SYSREF+	G11	SYSREFP		
SYSREF-	G12	SYSREFN		

ADR903X-BBCZ

CLKPLL OUT



SCHEMATIC

HW TYPE : Customer Evaluation
Product(s) : ADRV903x
: N/A

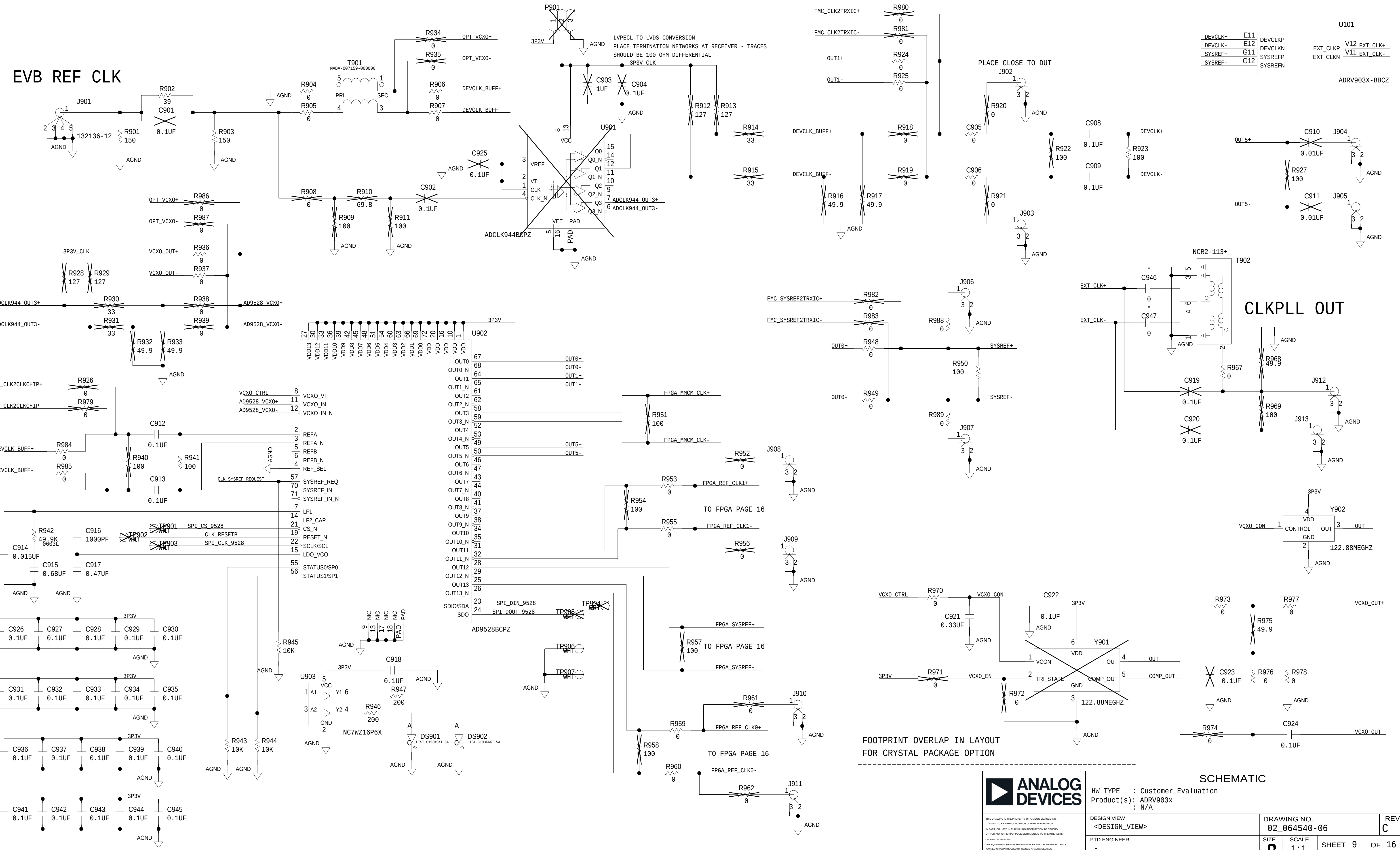
DESIGN VIEW
<DESIGN_VIEW>

PTD ENGINEER
-

DRAWING NO.
02_064540-06

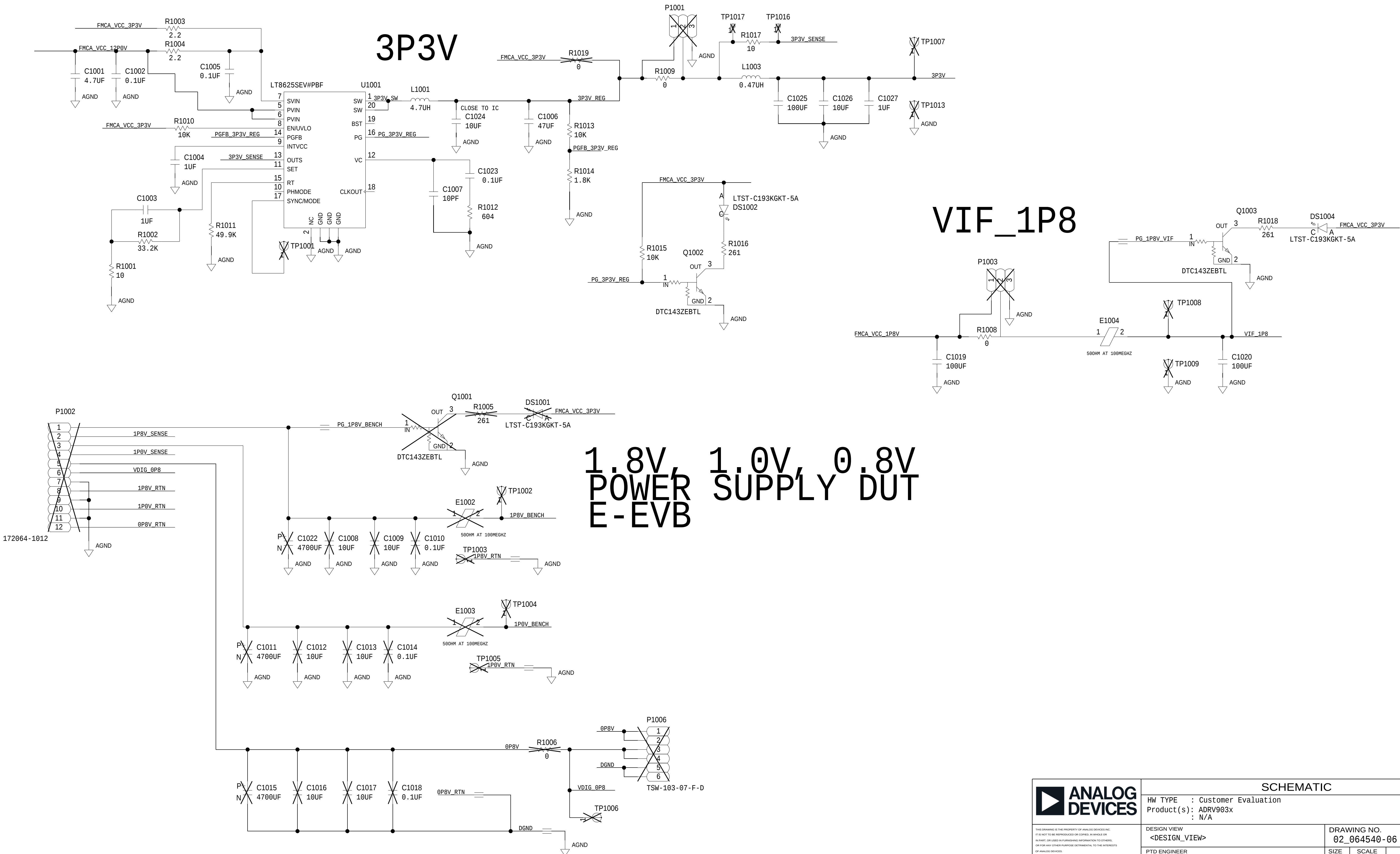
REV
C

SIZE **D** SCALE 1:1 SHEET 9 OF 16



POWER SUPPLY

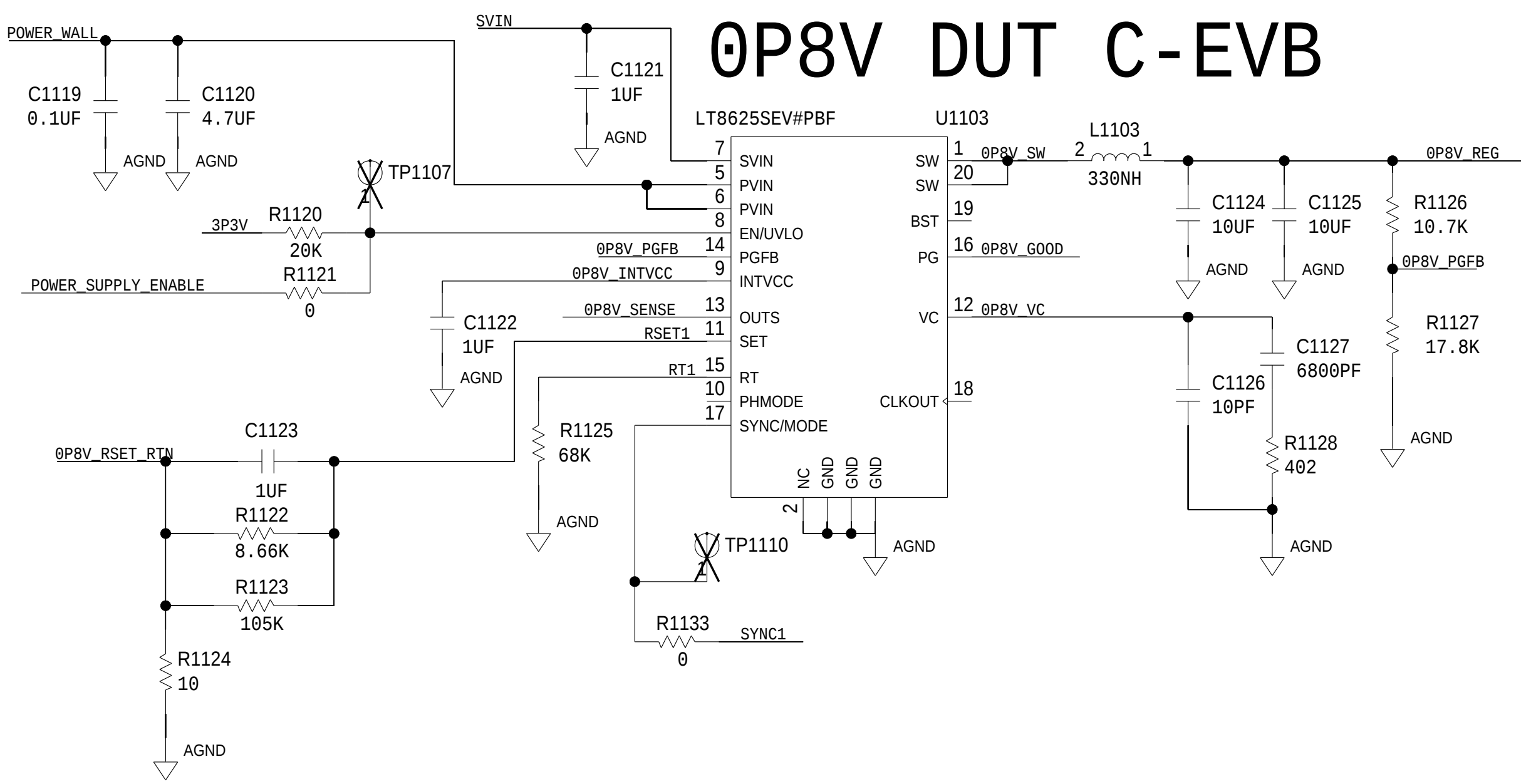
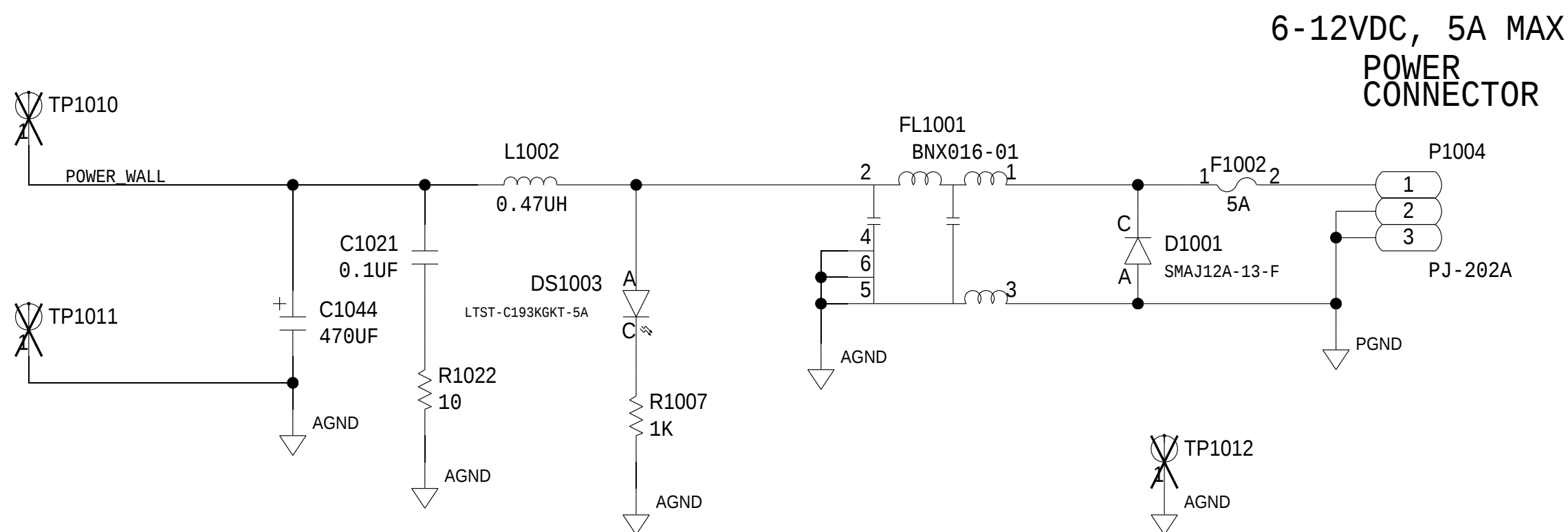
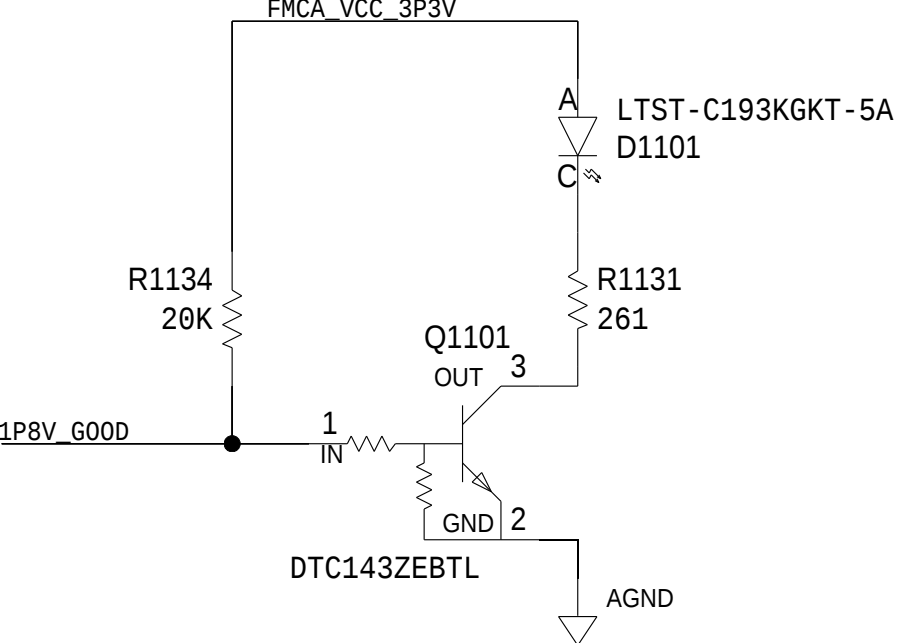
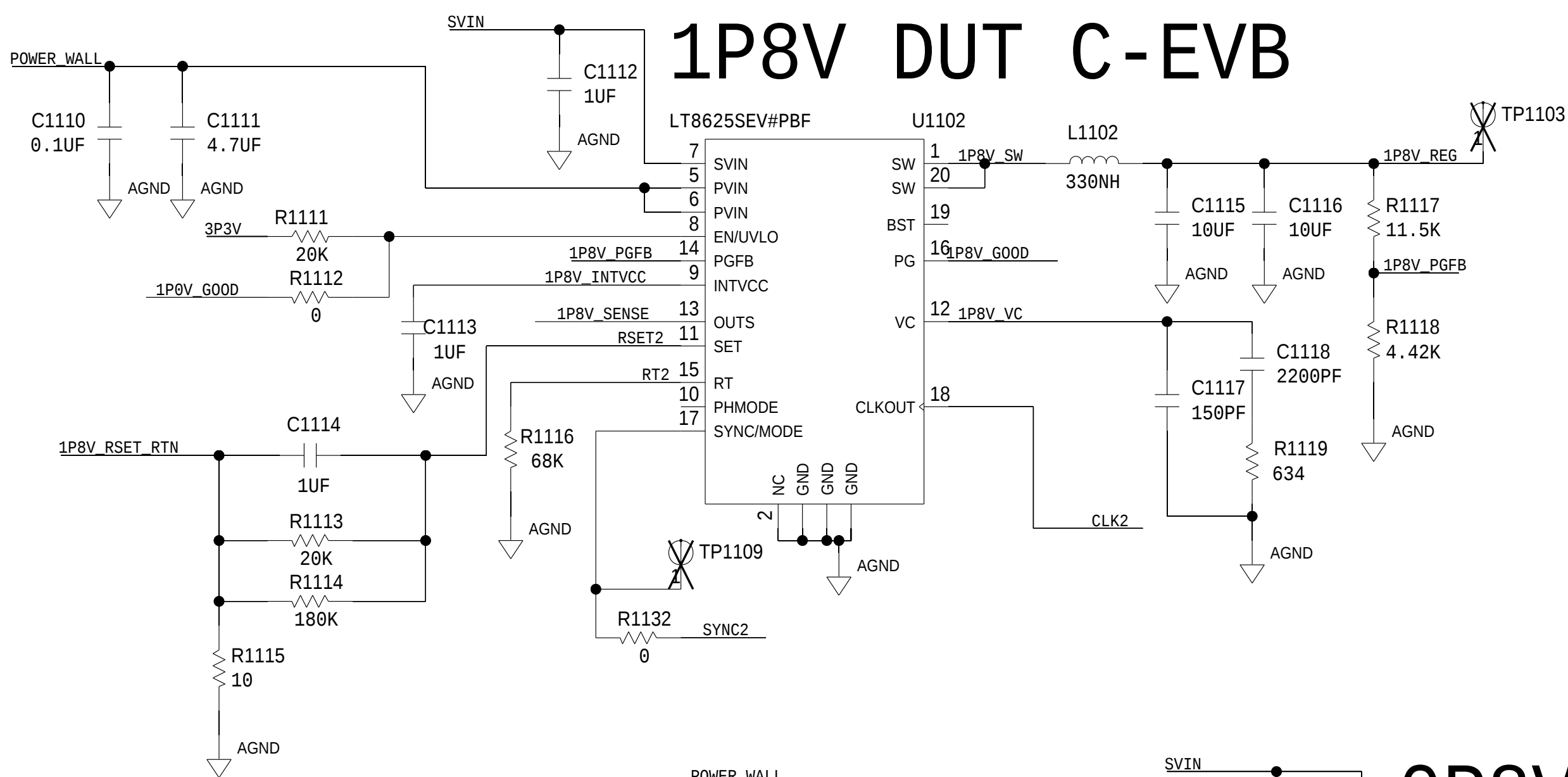
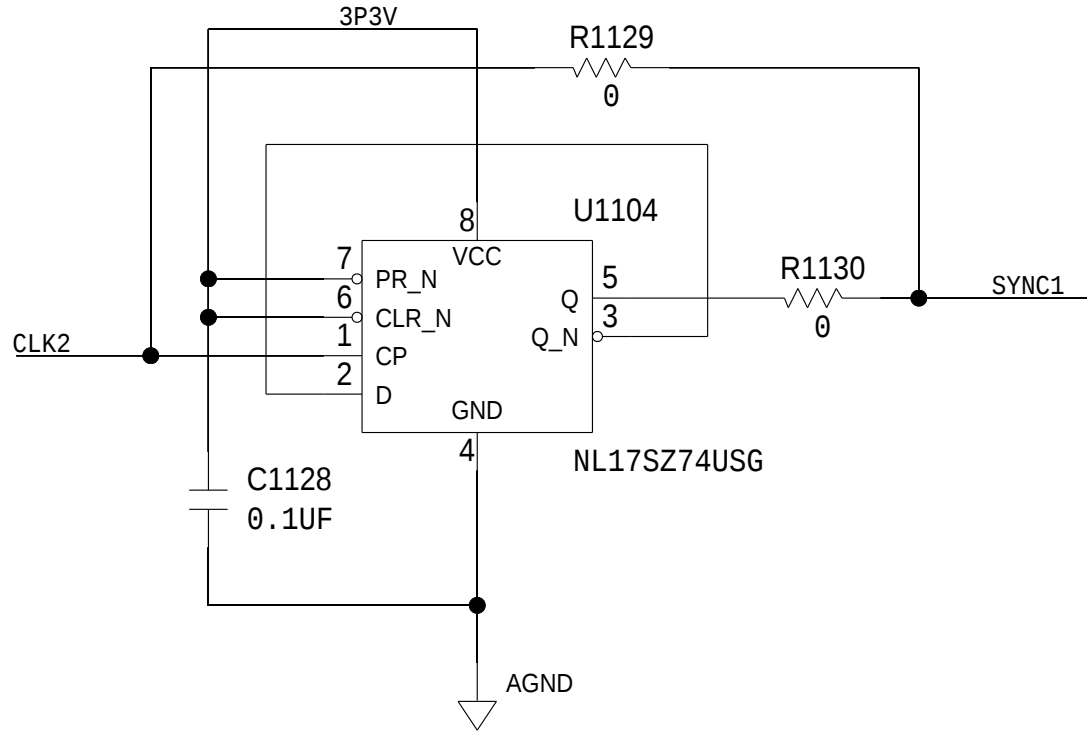
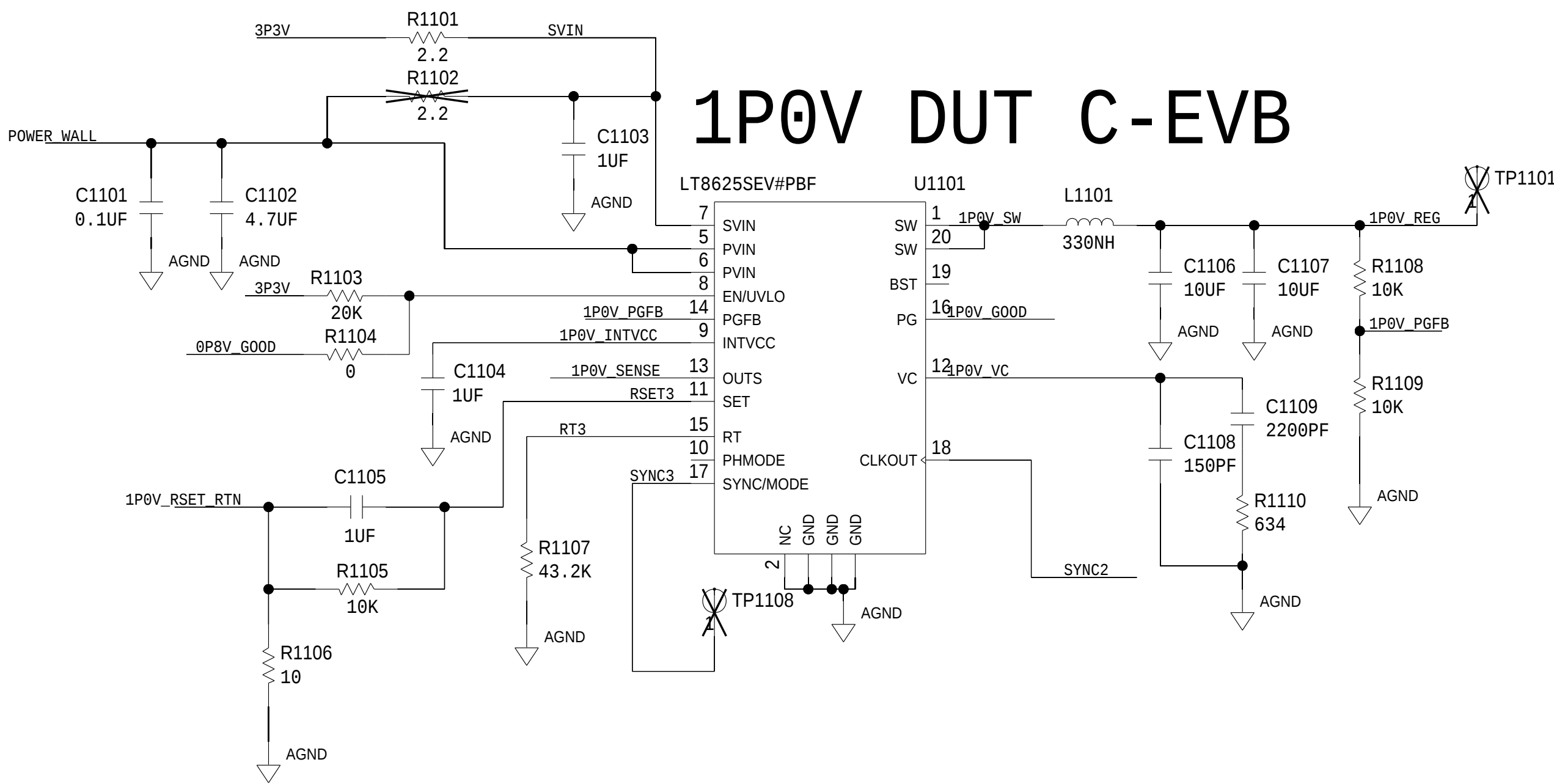
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



SCHEMATIC			
HW TYPE : Customer Evaluation Product(s): ADRV903x : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064540-06	REV C	
PTD ENGINEER -	SIZE D	SCALE 1:1	SHEET 10 OF 16

POWER SILENT SWITCHER

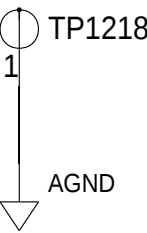
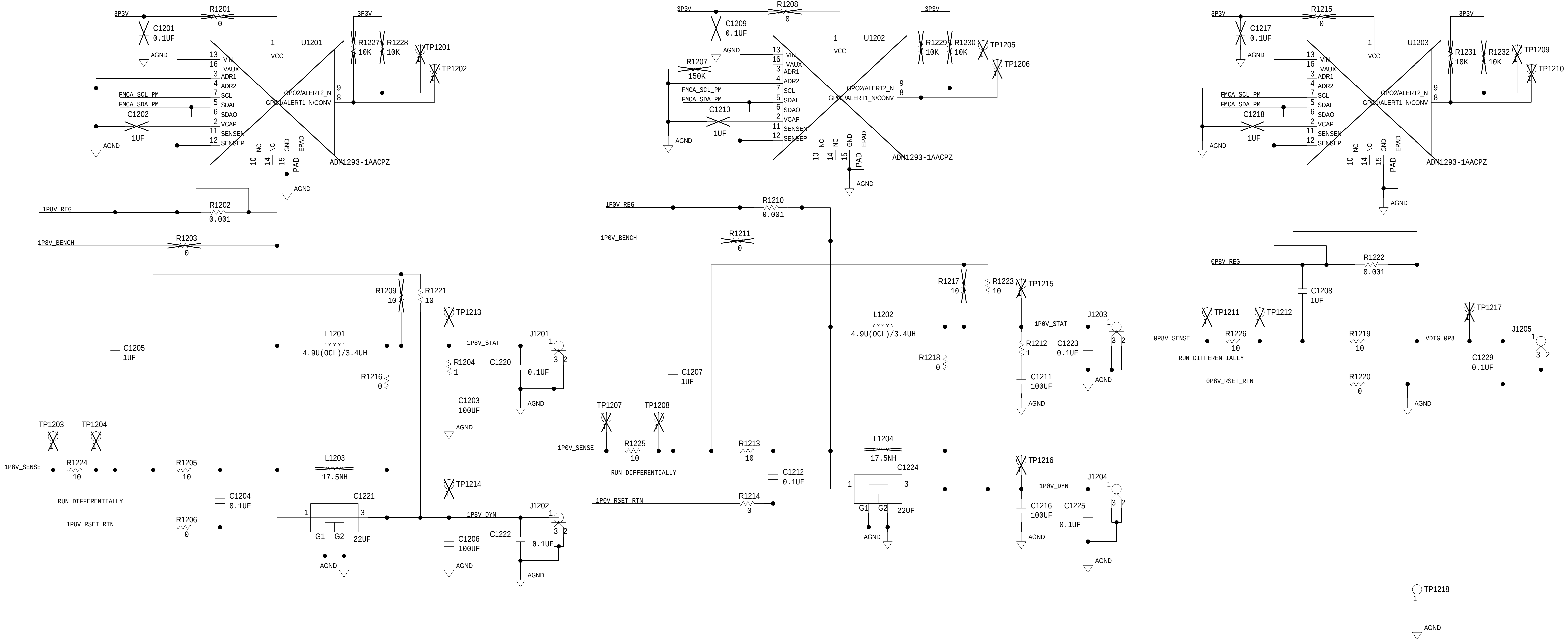
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



SCHEMATIC			
HW TYPE : Customer Evaluation Product(s): ADRV903x : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064540-06	REV C	
PTD ENGINEER -	SIZE D	SCALE 1:1	SHEET 11 OF 16

POWER MEASUREMENT & FILTERING

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

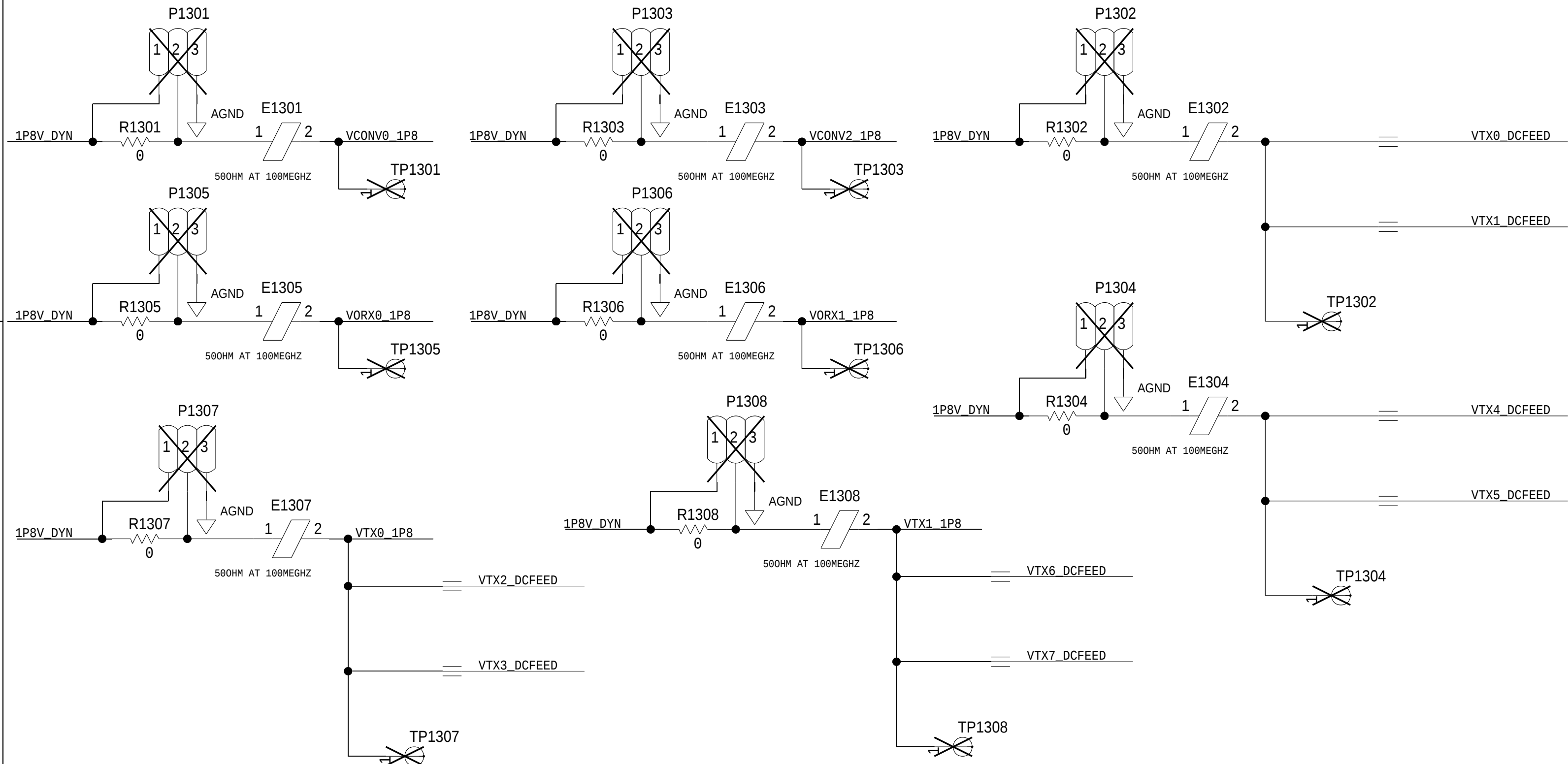


THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC.
IT IS NOT TO BE REPRODUCED OR COPIED IN WHOLE OR
IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS,
OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS
OF ANALOG DEVICES.
THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS
OWNED OR CONTROLLED BY ANALOG DEVICES.

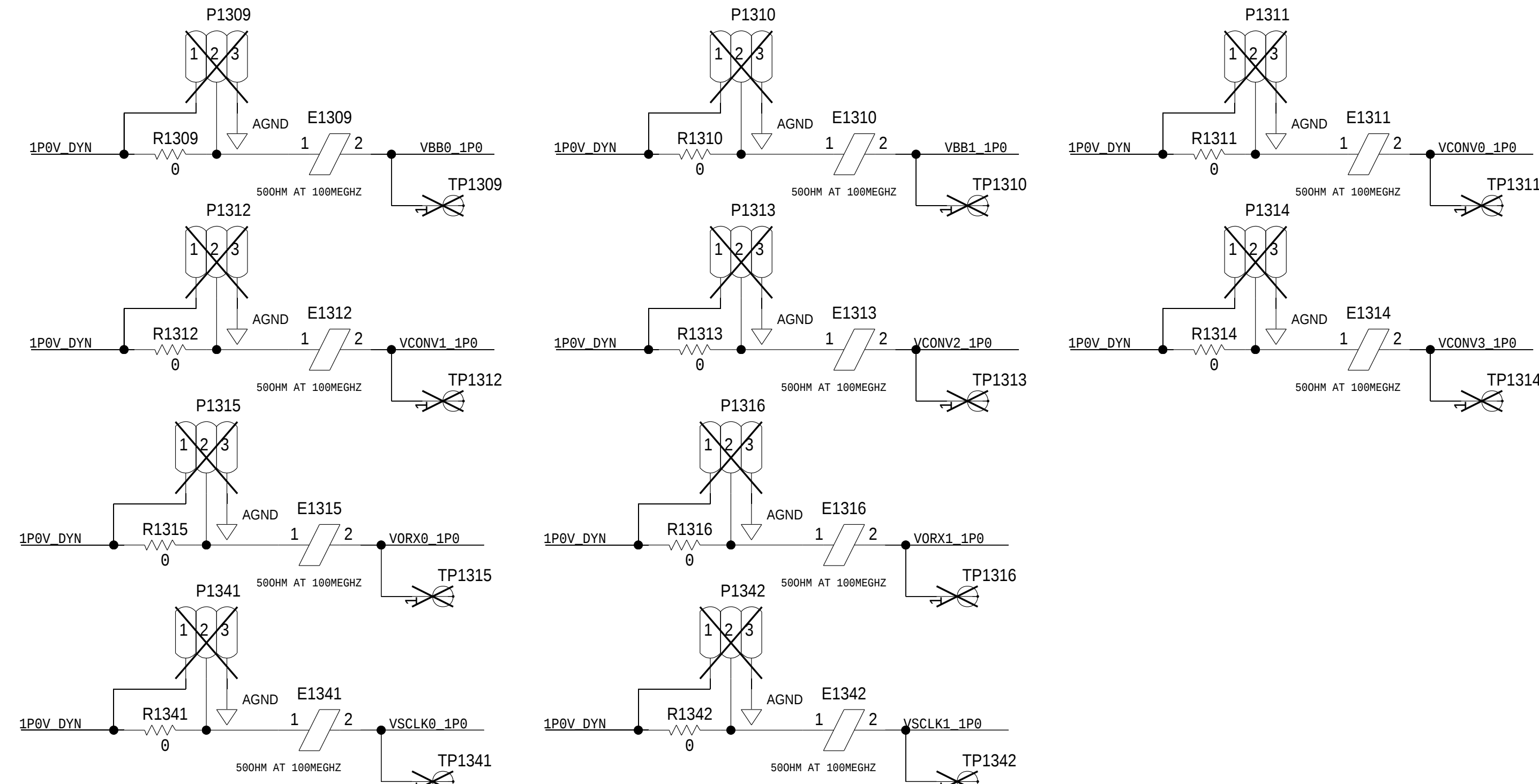
SCHEMATIC			
HW TYPE : Customer Evaluation Product(s): ADRV903x : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064540-06	REV C	
PTD ENGINEER -	SIZE D	SCALE 1:1	SHEET 12 OF 16

POWER DISTRIBUTION

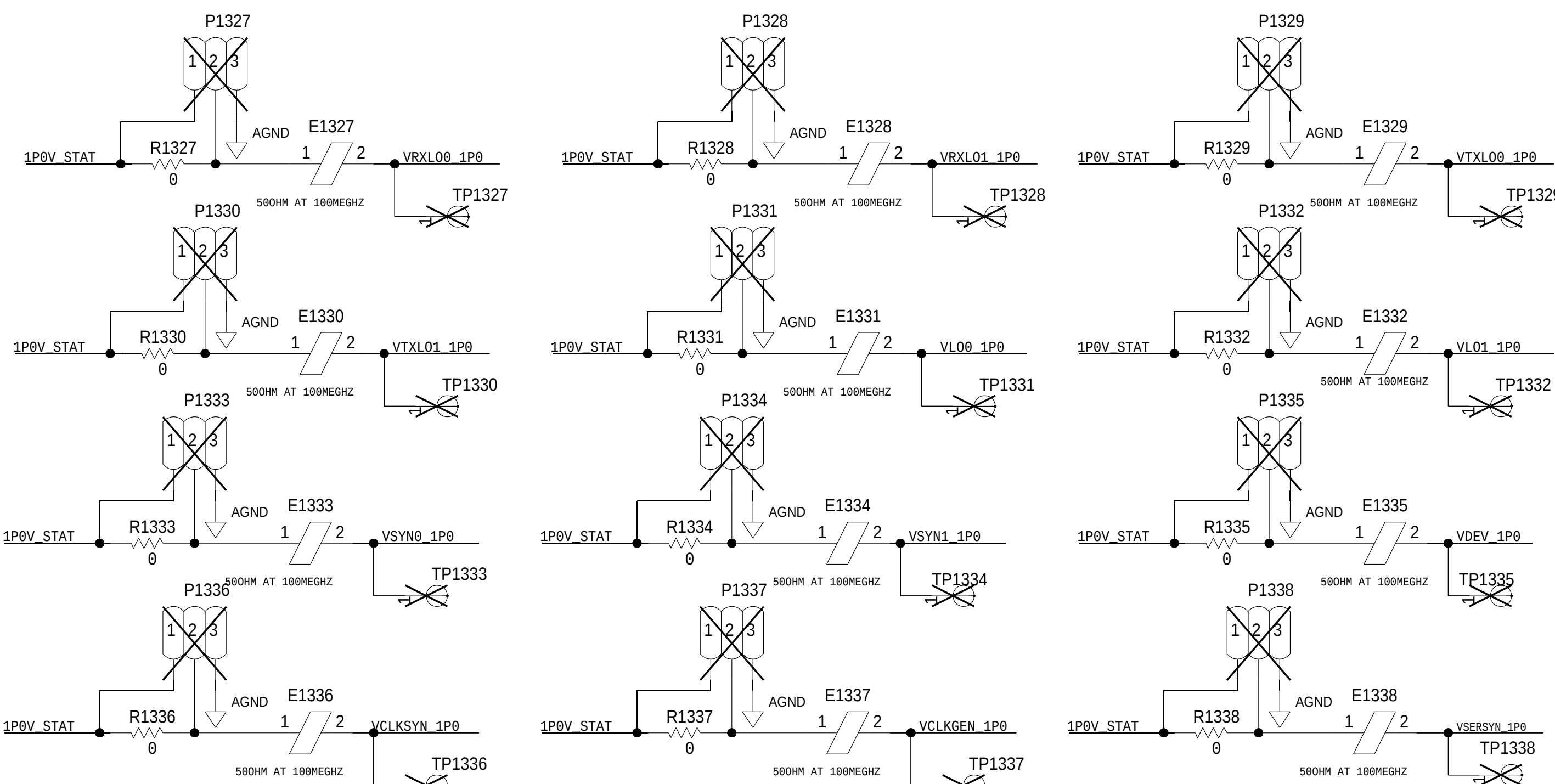
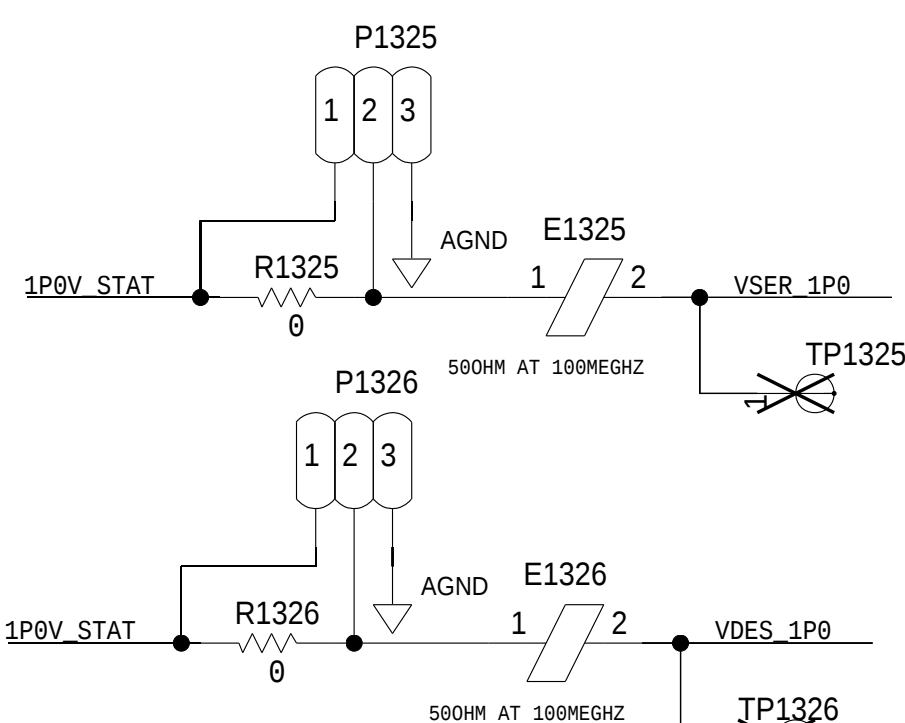
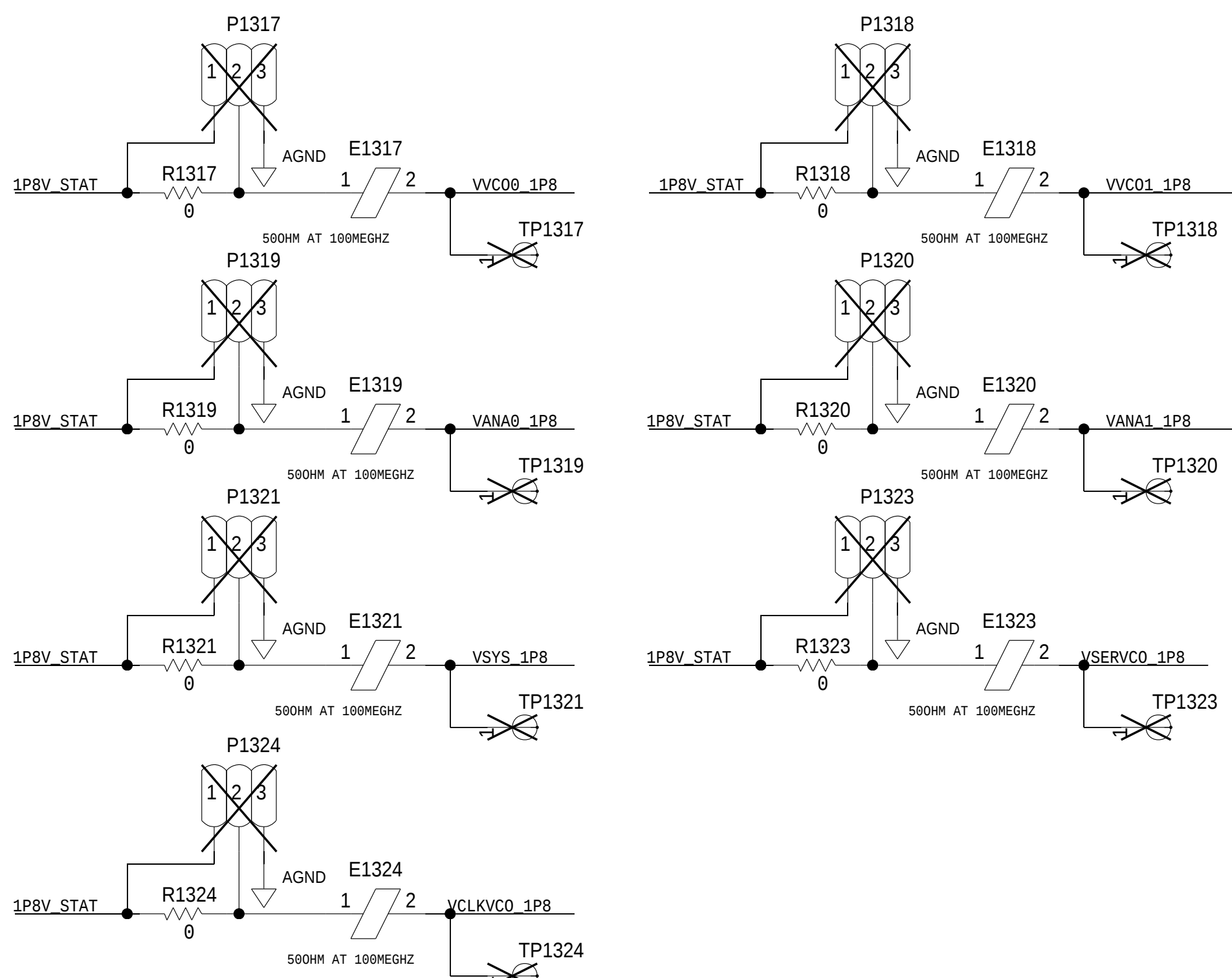
1.8V DYNAMIC SUPPLIES



1.0V DYNAMIC SUPPLIES



1.8V STATIC SUPPLIES



REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

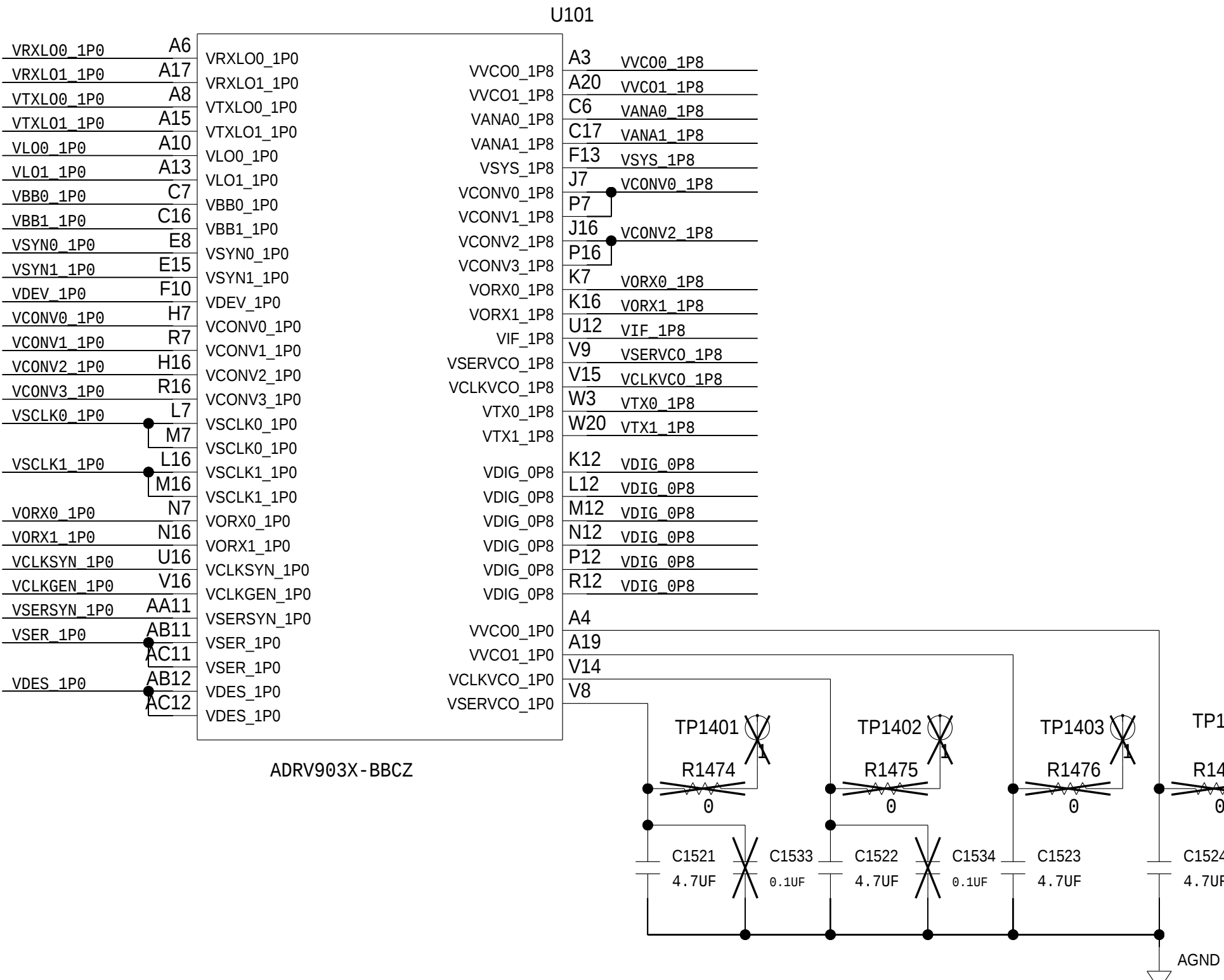
ANALOG DEVICES		SCHEMATIC	
HW TYPE : Customer Evaluation Product(s): ADRV903x : N/A		DESIGN VIEW <DESIGN_VIEW>	
PTD ENGINEER -		DRAWING NO. 02_064540-06	REV C
SIZE D	SCALE 1:1	SHEET 13 OF 16	

POWER INPUT

1.8V CAPS

0.8V CAPS

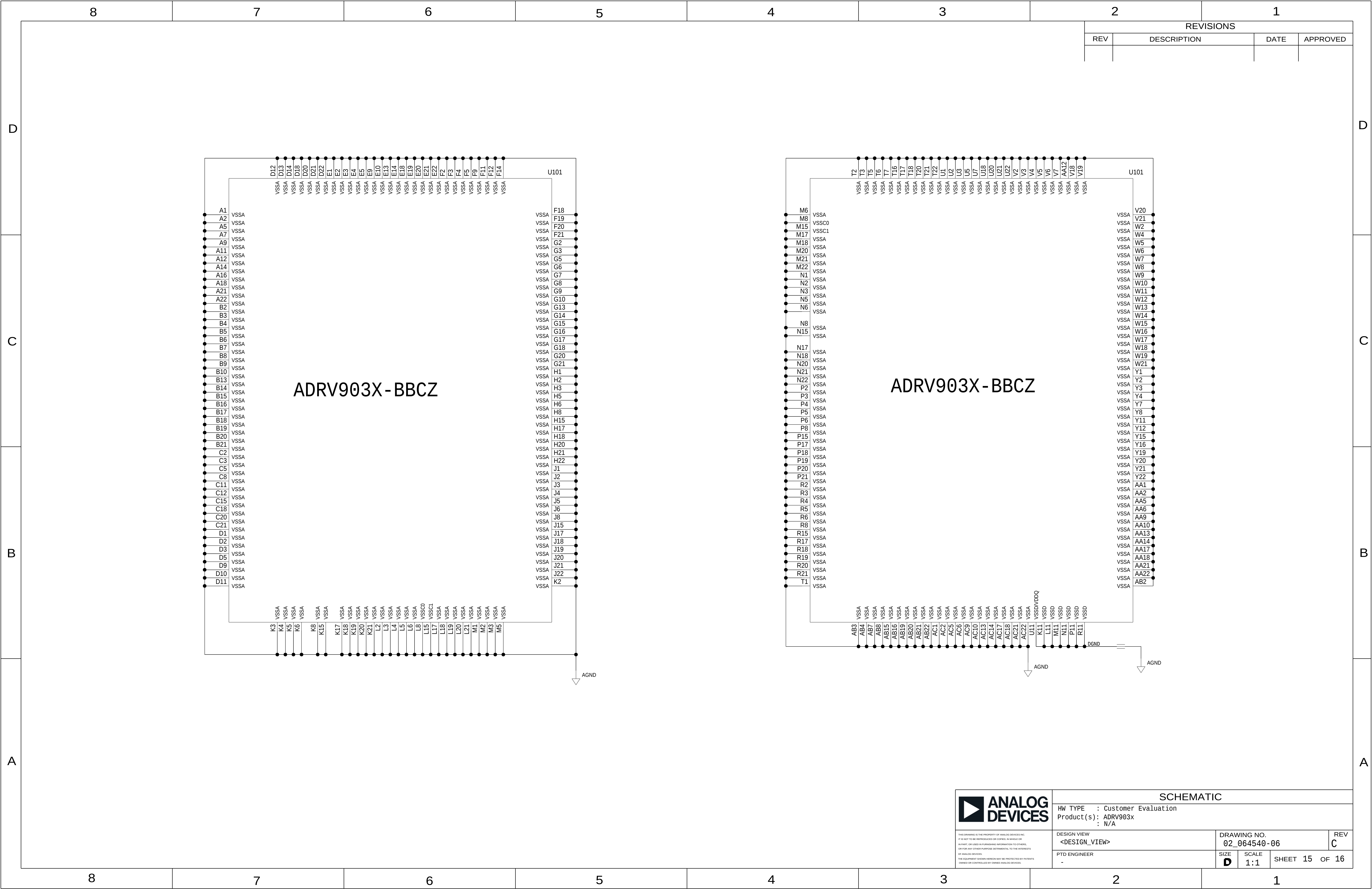
1.0V CAPS





THIS DOCUMENT IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED IN WHOLE OR IN PART, OR USED IN ANY MANNER THAT WOULD BE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREIN MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.

SCHEMATIC			
HW TYPE : Customer Evaluation Product(s): ADRV903x : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064540-06	REV C	
PTD ENGINEER -	SIZE D	SCALE 1:1	SHEET 14 OF 16



[illegible]