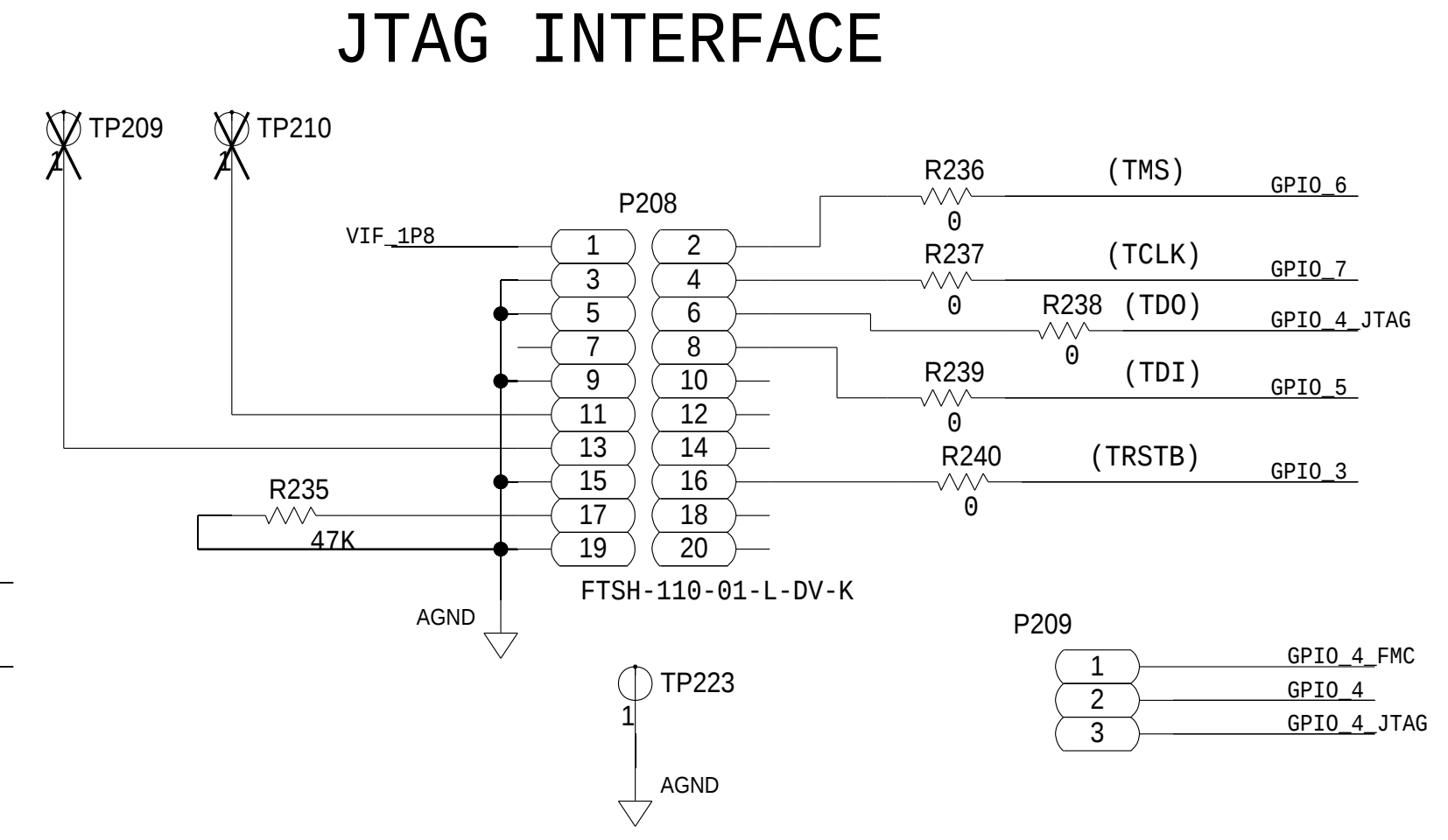
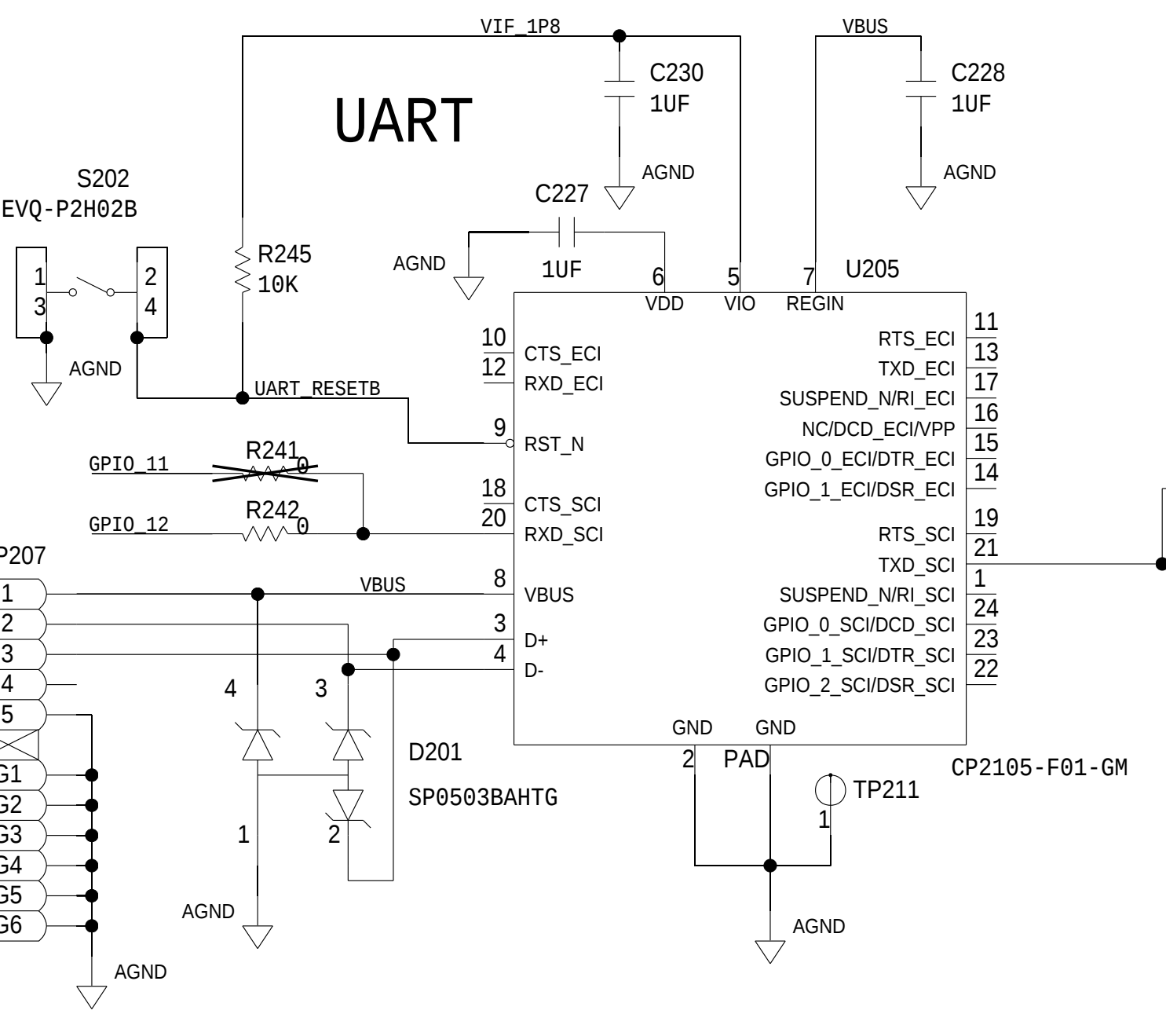
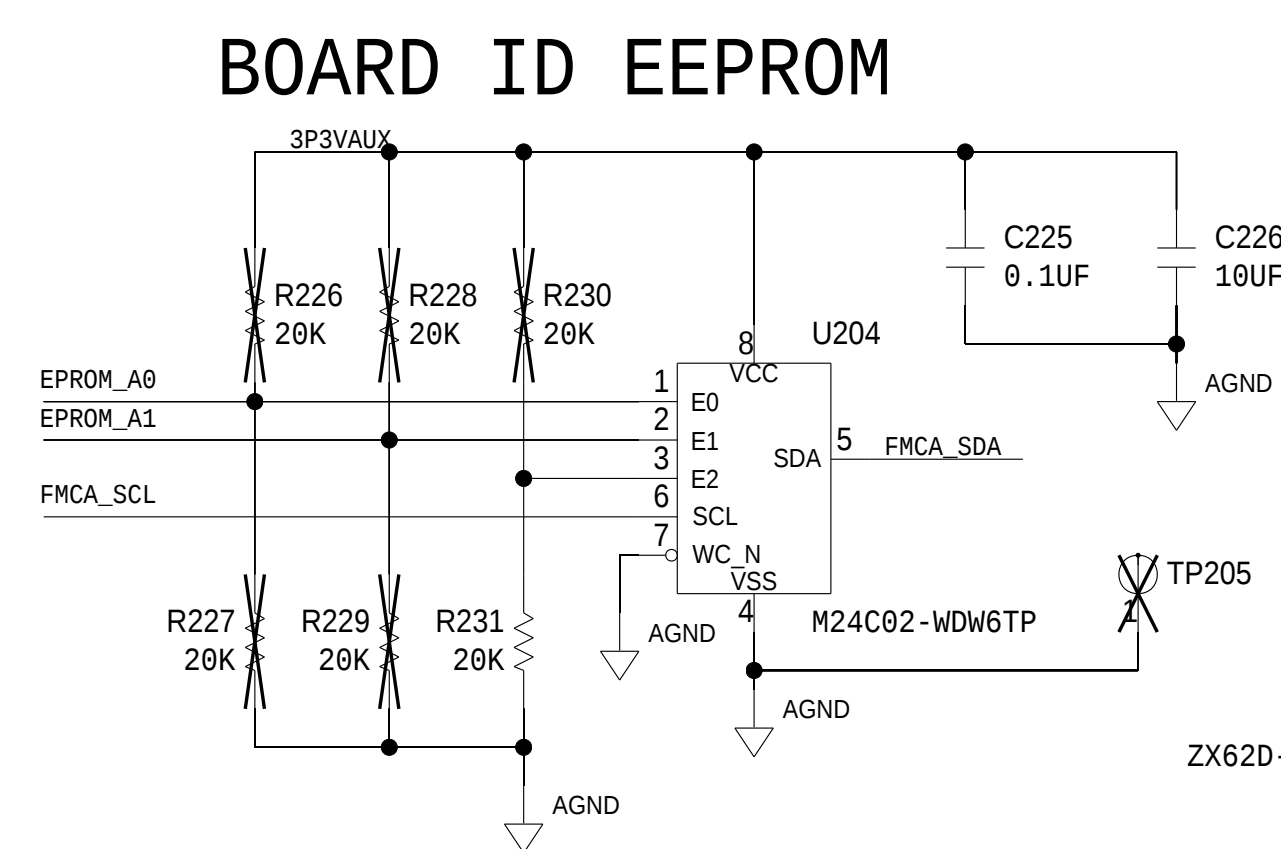
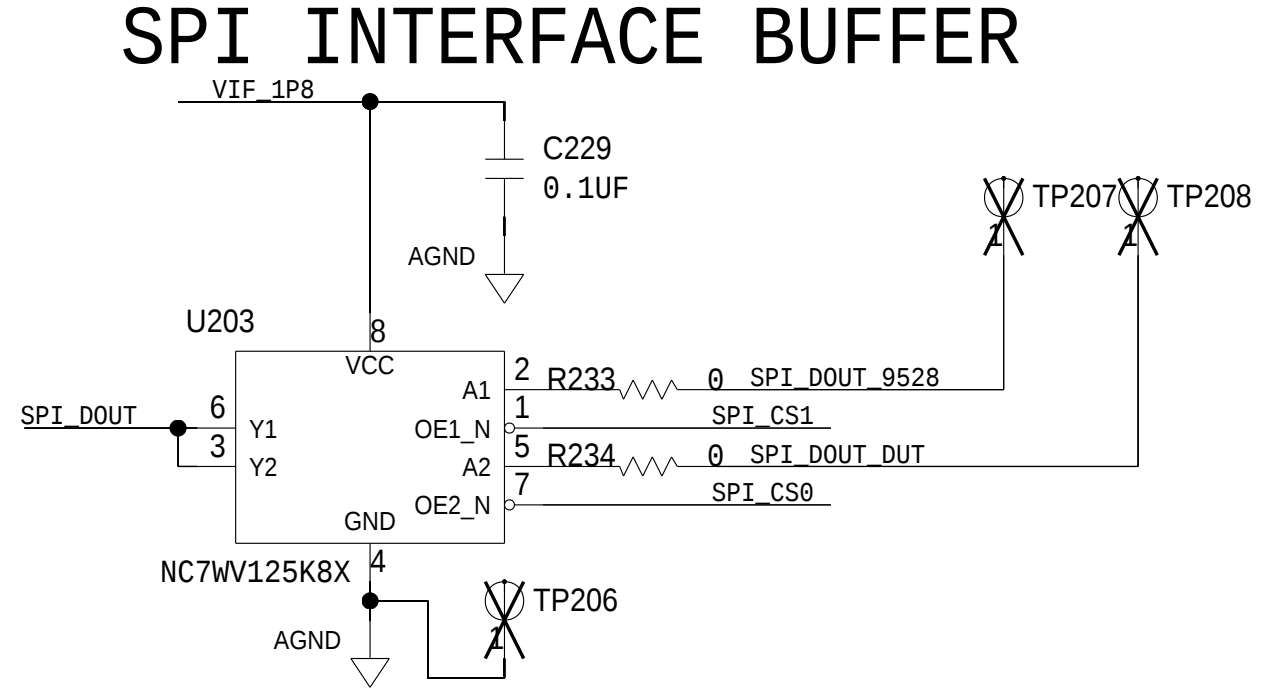
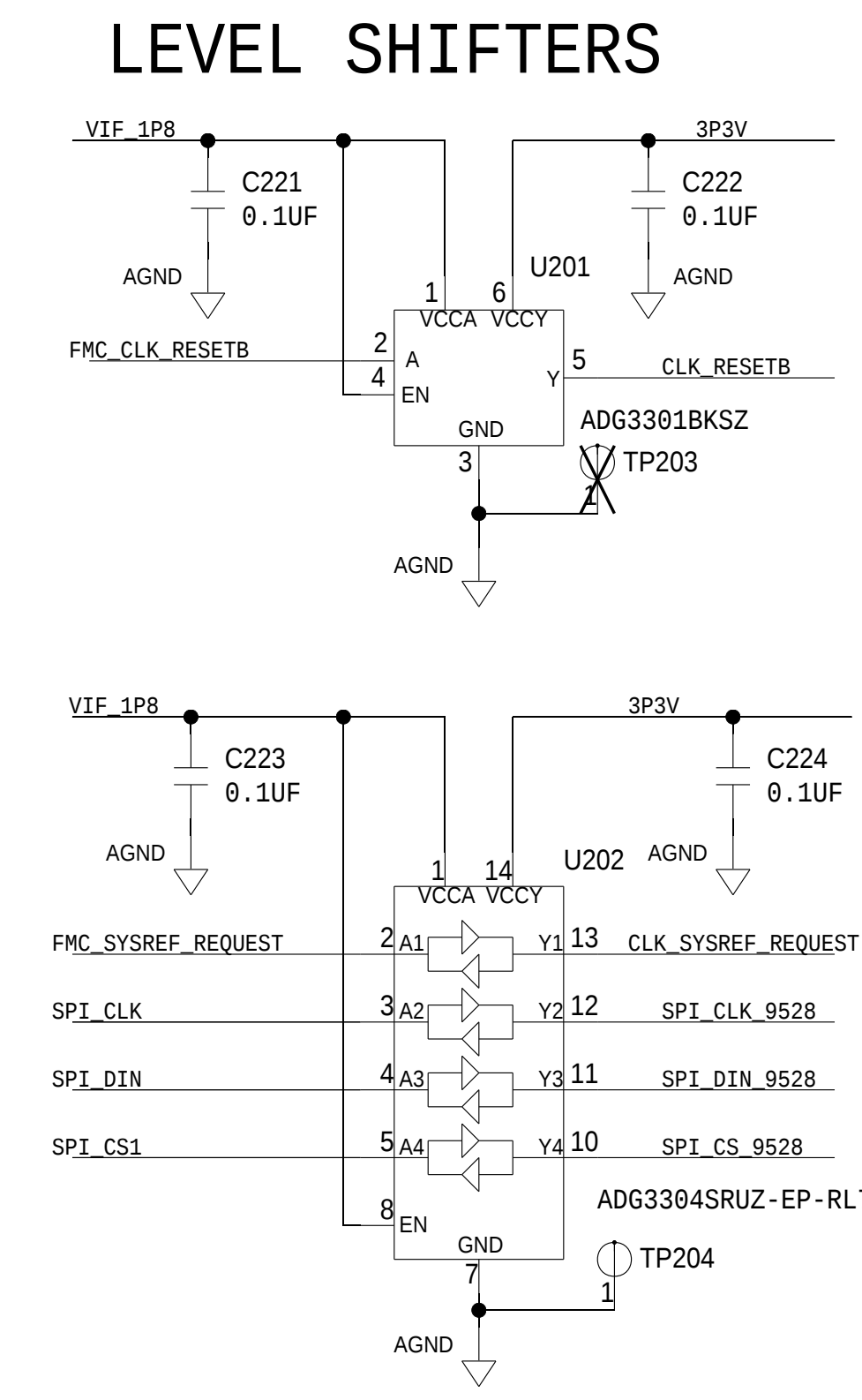
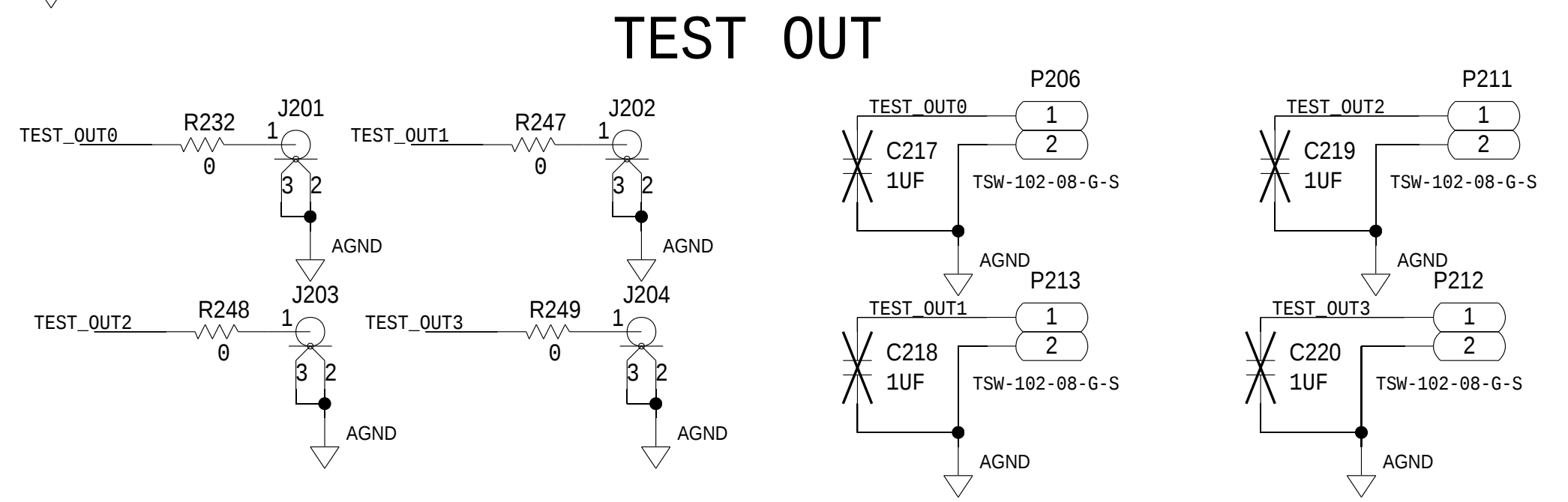
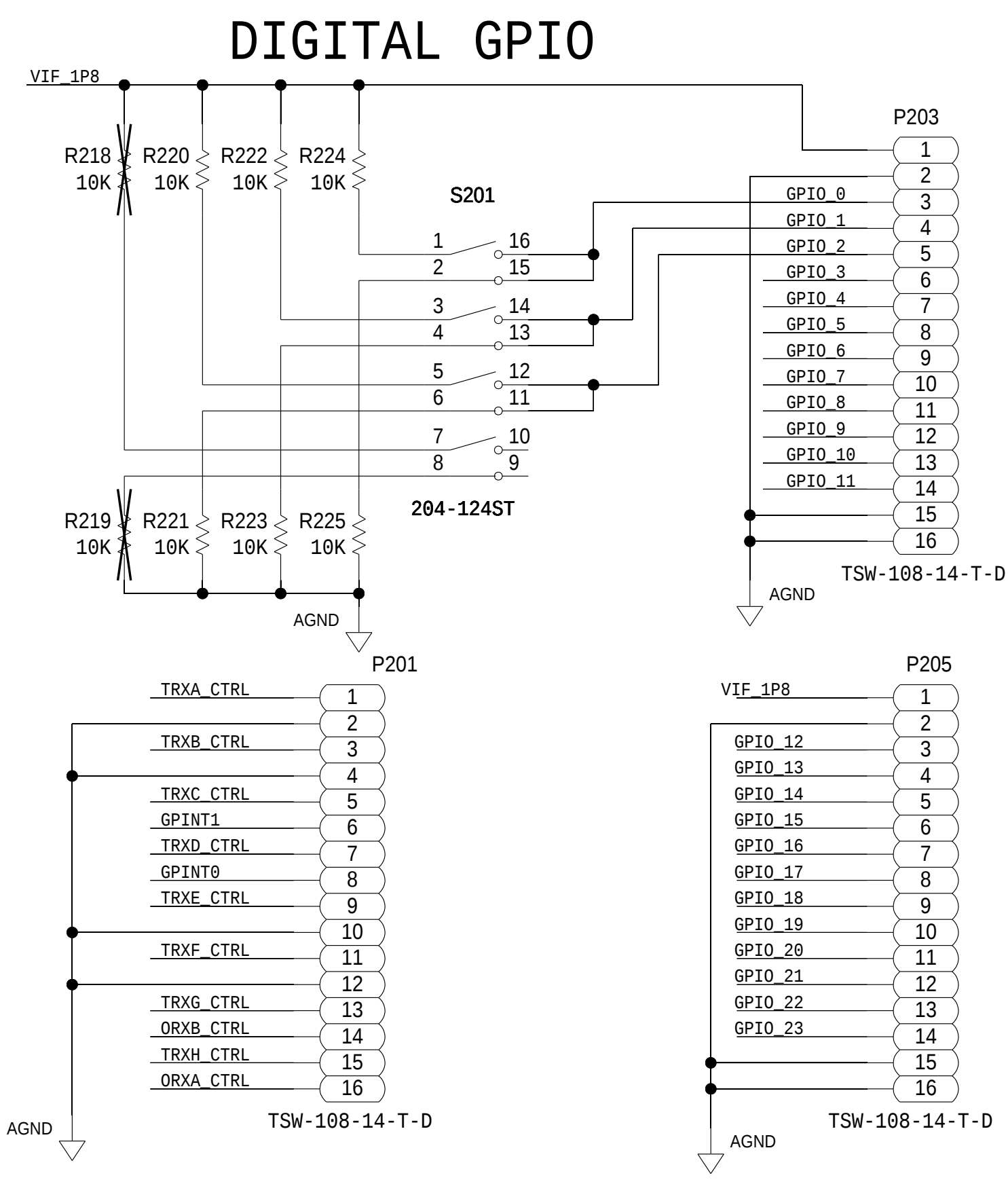
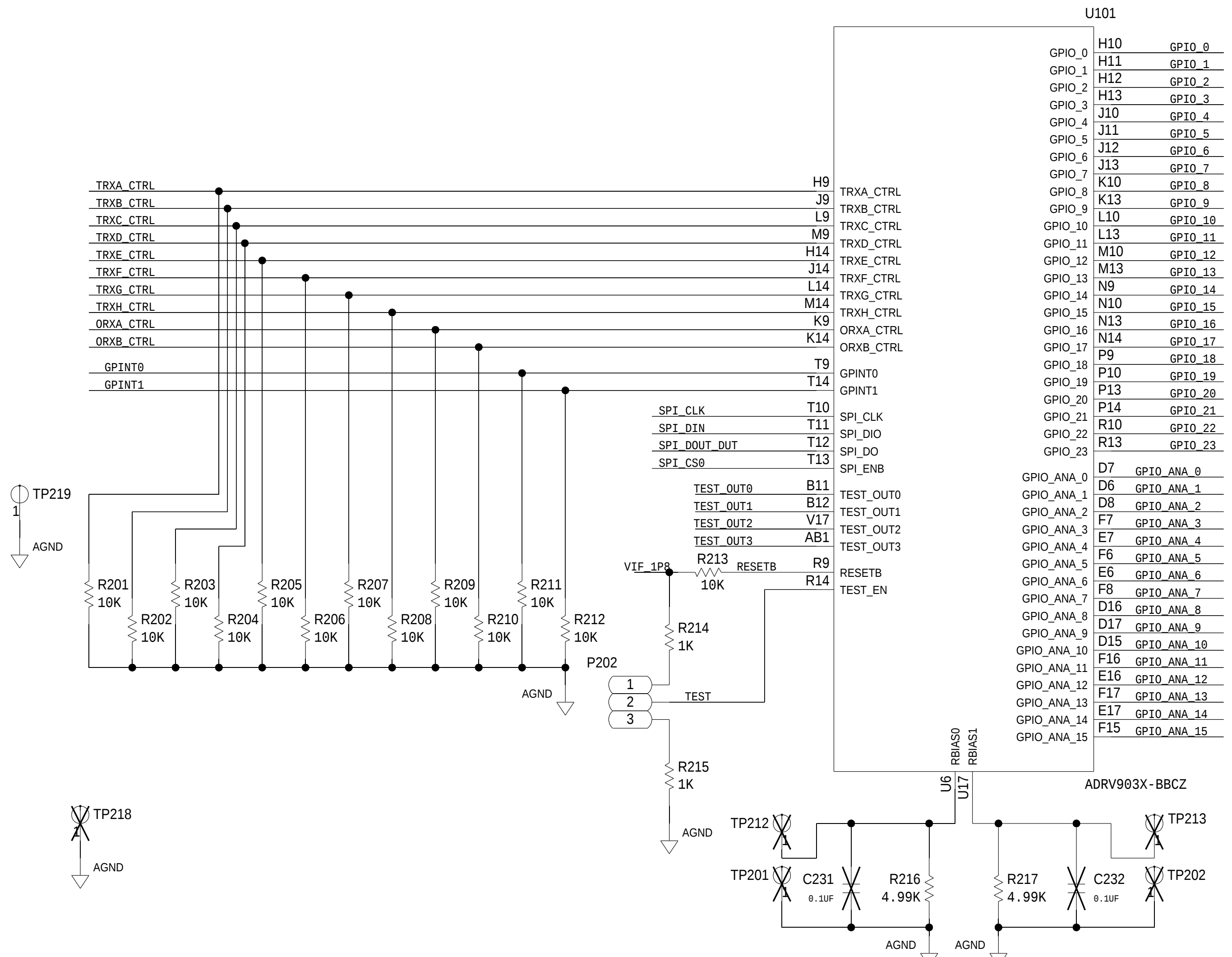


8		7		6		5		4		3		2		1																																																																																																					
D	THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.					JUMPER TABLE			REVISIONS																																																																																																										
									REV	DESCRIPTION		DATE	APPROVED																																																																																																						
	CONTROL	CODE	DEVICE	FUNCTION	CONNECTOR	JP#	ON	OFF																																																																																																											
	RELAY CONTROL CHART					1																																																																																																													
						2																																																																																																													
						3																																																																																																													
						4																																																																																																													
						5																																																																																																													
						* SEE ASSEMBLY INSTRUCTIONS																																																																																																													
C																																																																																																																			
B																																																																																																																			
A						<table><tr><td>TEMPLATE ENGINEER</td><td>DATE</td><td colspan="2" rowspan="2">SCHEMATIC</td><td rowspan="2"></td></tr><tr><td>-</td><td></td></tr><tr><td>HARDWARE SERVICES</td><td></td><td colspan="2" rowspan="5">HW TYPE : Customer Evaluation Product(s): ADRV903x : N/A PACKAGE : LeadCount-lead BodySize PackageType-family : Pitch-pitch StyleVendor Style</td><td rowspan="5"></td></tr><tr><td>-</td><td></td></tr><tr><td>HARDWARE SYSTEMS</td><td></td></tr><tr><td>-</td><td></td></tr><tr><td>TEST ENGINEER</td><td></td></tr><tr><td>-</td><td></td><td colspan="2" rowspan="3">Mid Band ADRV903X-MB/PCBZ 1800 MHz to 4800 MHz</td><td rowspan="3"></td></tr><tr><td>COMPONENT ENGINEER</td><td></td></tr><tr><td>-</td><td></td></tr><tr><td>TEST PROCESS</td><td></td><td colspan="2">MASTER PROJECT TEMPLATE</td><td>TESTER TEMPLATE</td><td colspan="2" rowspan="2">DRAWING NO. 02_064540-05</td><td rowspan="2">REV. C</td></tr><tr><td>-</td><td></td><td colspan="2">TBD</td><td colspan="2">no_template</td></tr><tr><td colspan="2">DESIGNER</td><td colspan="2" rowspan="2">UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES</td><td colspan="2" rowspan="2">TOLERANCES</td><td colspan="2" rowspan="2">SIZE D</td><td colspan="2" rowspan="2">SCALE 1:1</td><td colspan="2" rowspan="2">CODE ID NO. CodeID</td><td colspan="2" rowspan="2">SHEET 1 OF 16</td></tr><tr><td colspan="2">-</td></tr><tr><td colspan="2">PTD ENGINEER</td><td colspan="2"></td><td colspan="2"></td><td colspan="2"></td><td colspan="2"></td><td colspan="2"></td><td colspan="2"></td></tr><tr><td colspan="2">CHECKER</td><td colspan="2"></td><td colspan="2"></td><td colspan="2"></td><td colspan="2"></td><td colspan="2"></td><td colspan="2"></td></tr><tr><td colspan="2">-</td><td colspan="2"></td><td colspan="2"></td><td colspan="2"></td><td colspan="2"></td><td colspan="2"></td><td colspan="2"></td></tr></table>									TEMPLATE ENGINEER	DATE	SCHEMATIC			-		HARDWARE SERVICES		HW TYPE : Customer Evaluation Product(s): ADRV903x : N/A PACKAGE : LeadCount-lead BodySize PackageType-family : Pitch-pitch StyleVendor Style			-		HARDWARE SYSTEMS		-		TEST ENGINEER		-		Mid Band ADRV903X-MB/PCBZ 1800 MHz to 4800 MHz			COMPONENT ENGINEER		-		TEST PROCESS		MASTER PROJECT TEMPLATE		TESTER TEMPLATE	DRAWING NO. 02_064540-05		REV. C	-		TBD		no_template		DESIGNER		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		TOLERANCES		SIZE D		SCALE 1:1		CODE ID NO. CodeID		SHEET 1 OF 16		-		PTD ENGINEER														CHECKER														-													
	TEMPLATE ENGINEER	DATE	SCHEMATIC																																																																																																																
	-																																																																																																																		
	HARDWARE SERVICES		HW TYPE : Customer Evaluation Product(s): ADRV903x : N/A PACKAGE : LeadCount-lead BodySize PackageType-family : Pitch-pitch StyleVendor Style																																																																																																																
	-																																																																																																																		
	HARDWARE SYSTEMS																																																																																																																		
	-																																																																																																																		
	TEST ENGINEER																																																																																																																		
	-		Mid Band ADRV903X-MB/PCBZ 1800 MHz to 4800 MHz																																																																																																																
	COMPONENT ENGINEER																																																																																																																		
-																																																																																																																			
TEST PROCESS		MASTER PROJECT TEMPLATE		TESTER TEMPLATE	DRAWING NO. 02_064540-05		REV. C																																																																																																												
-		TBD		no_template																																																																																																															
DESIGNER		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		TOLERANCES		SIZE D		SCALE 1:1		CODE ID NO. CodeID		SHEET 1 OF 16																																																																																																							
-																																																																																																																			
PTD ENGINEER																																																																																																																			
CHECKER																																																																																																																			
-																																																																																																																			
P.O SPEC.	BK/BD SPEC.	SOCKET OEM	OEM PART#	HANDLER																																																																																																															
8	7	6	5	4	3	2	1																																																																																																												

DIGITAL/ANALOG I/O

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

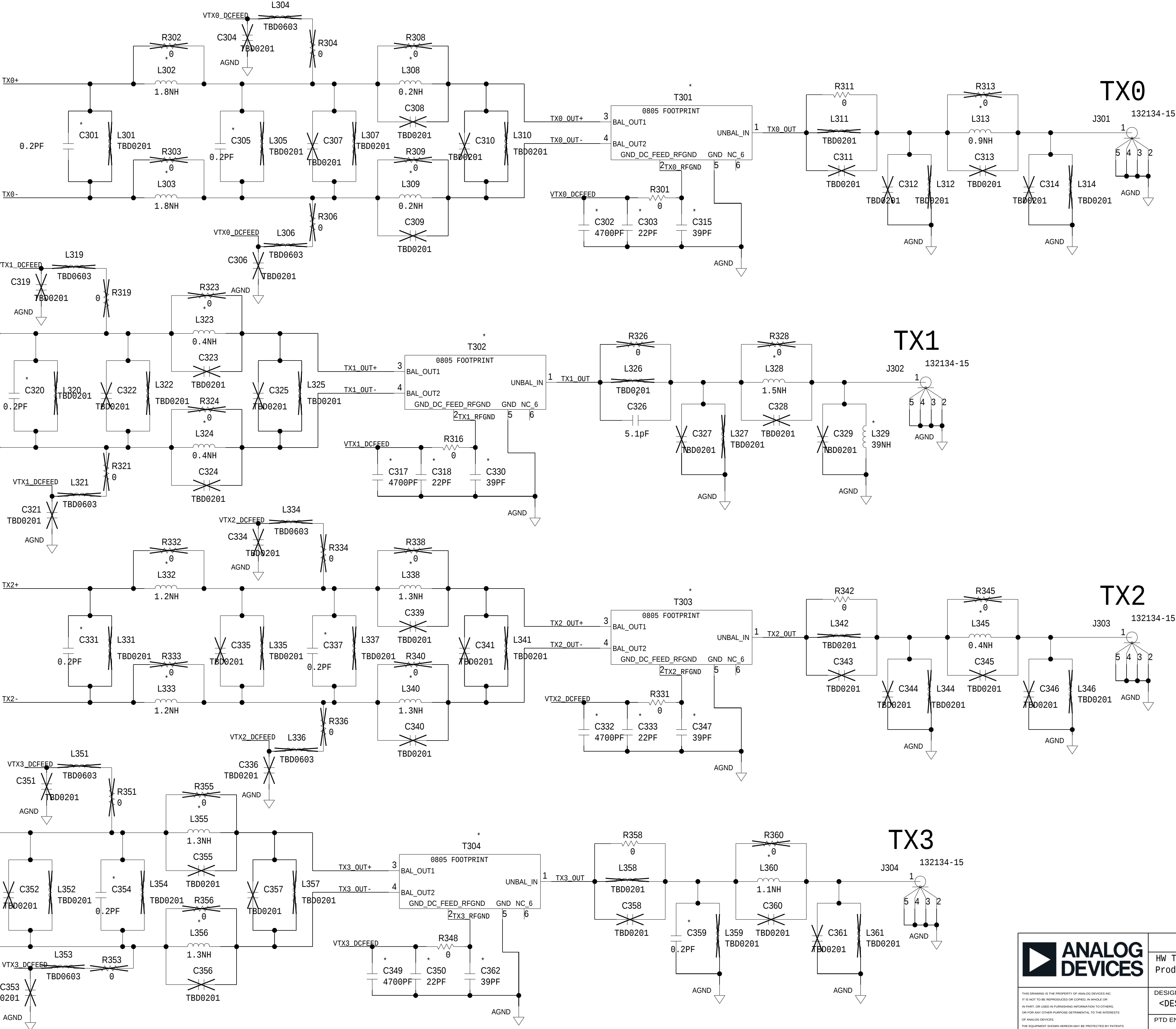


SCHEMATIC			
HW TYPE : Customer Evaluation Product(s) : ADRV903x : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064540-05	REV C	
PTD ENGINEER -	SIZE D	SCALE 1:1	SHEET 2 OF 16

TRANSMITTER RF OUTPUT

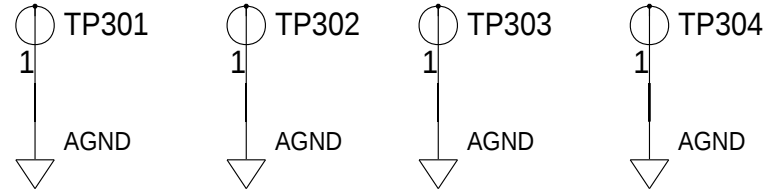
EVB MATCH HAS BEEN DESIGNED TO SUPPORT THE WIDEST MATCH POSSIBLE;
MATCHING TOPOLOGY COULD BE REDUCED/SIMPLIFIED DEPENDING ON THE SYSTEM REQUIREMENTS.

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

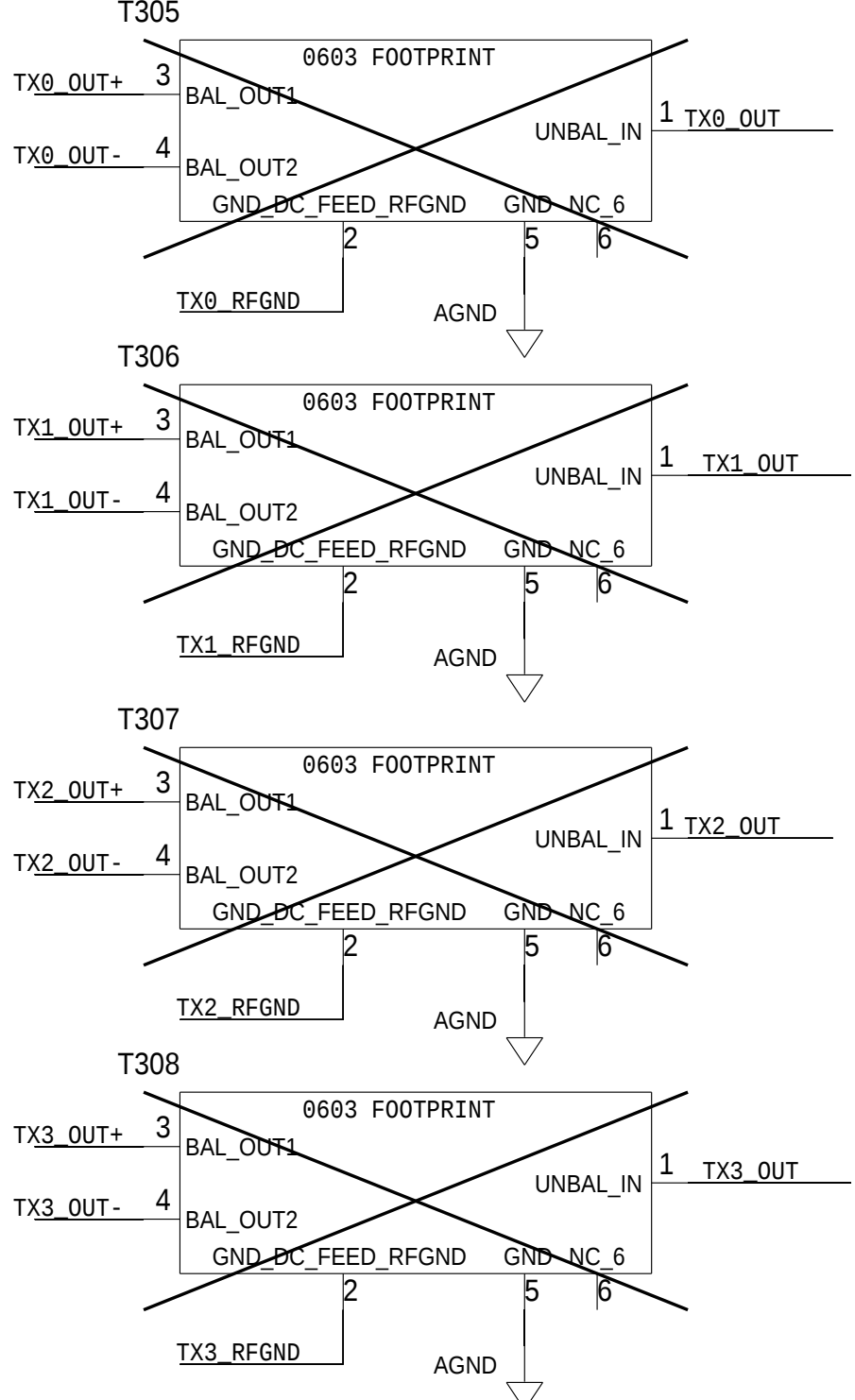


U101	
TX0P	C1 TX0+
TX0N	B1 TX0-
TX1P	G1 TX1+
TX1N	F1 TX1-
TX2P	R1 TX2+
TX2N	P1 TX2-
TX3P	W1 TX3+
TX3N	B22 TX4+
TX4P	C22 TX4-
TX4N	F22 TX5+
TX5P	G22 TX5-
TX5N	P22 TX6+
TX6P	R22 TX6-
TX7P	V22 TX7+
TX7N	W22 TX7-

ADRV903X-BBCZ



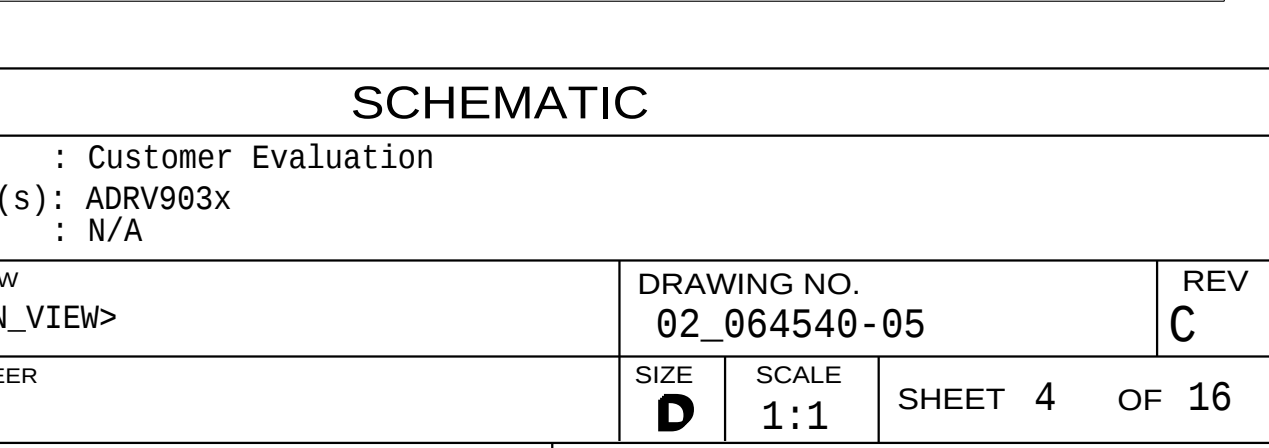
OVERLAP TX BALUNS



THIS DOCUMENT IS THE PROPERTY OF ANALOG DEVICES INC.
IT IS NOT TO BE REPRODUCED OR COPIED IN WHOLE OR
IN PART, OR USED IN ANY MANNER FOR THE PURPOSES OF
OR ANALOG DEVICES.
THE EQUIPMENT SHOWN HEREIN MAY BE PROTECTED BY PATENTS
OWNED OR CONTROLLED BY ANALOG DEVICES.

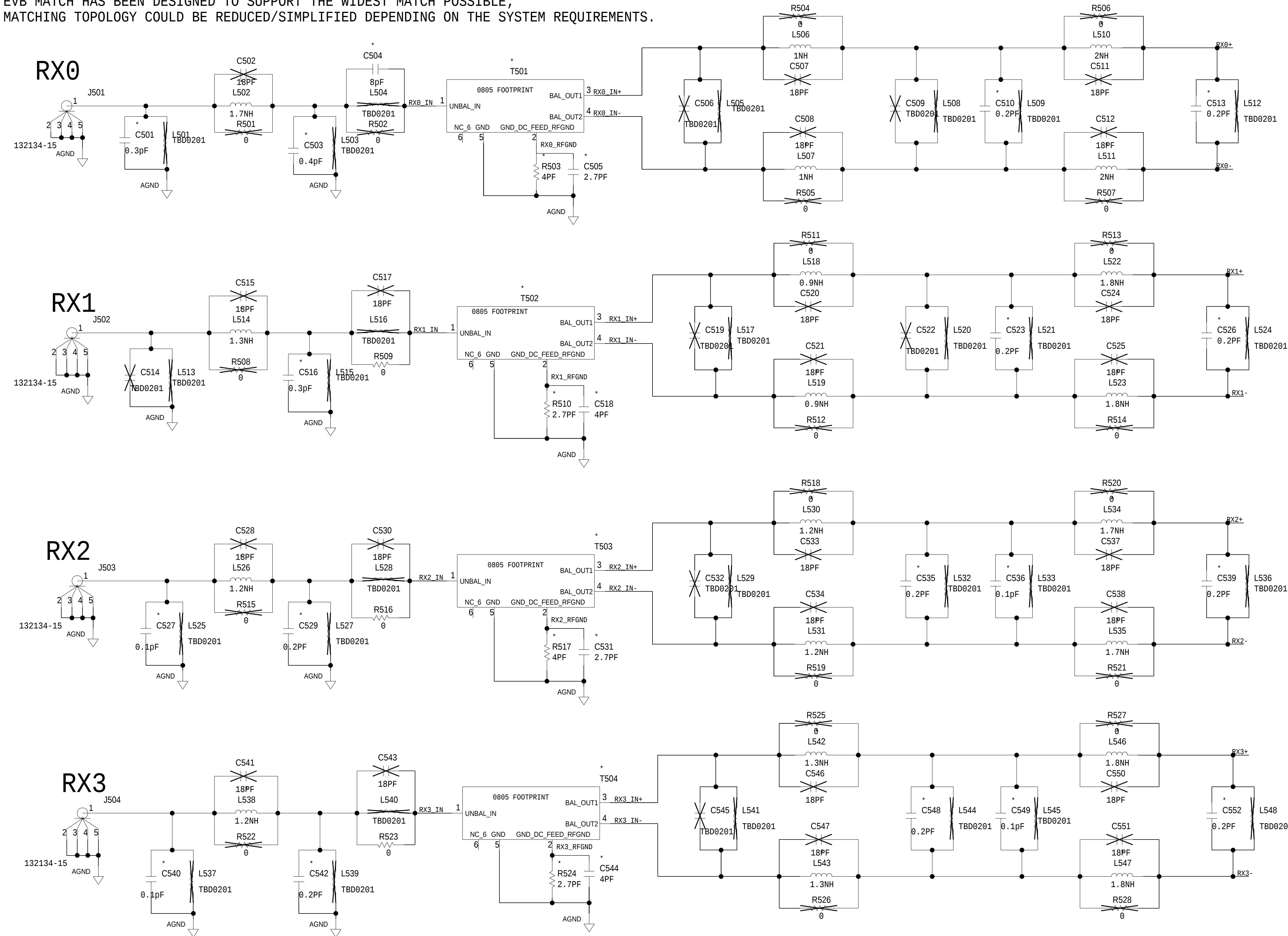
SCHEMATIC			
HW TYPE : Customer Evaluation Product(s) : ADRV903x : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064540-05	REV C	
PTD ENGINEER -	SIZE D	SCALE 1:1	SHEET 3 OF 16

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

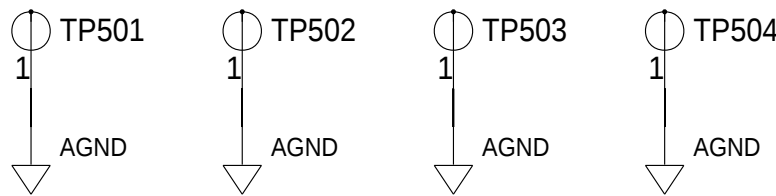
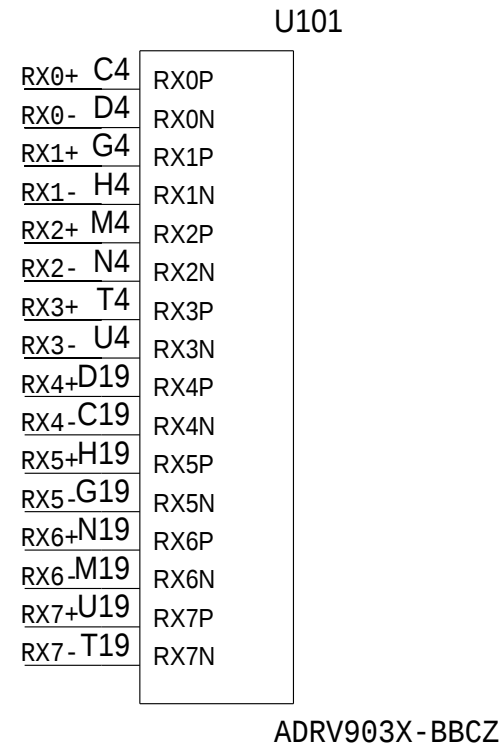
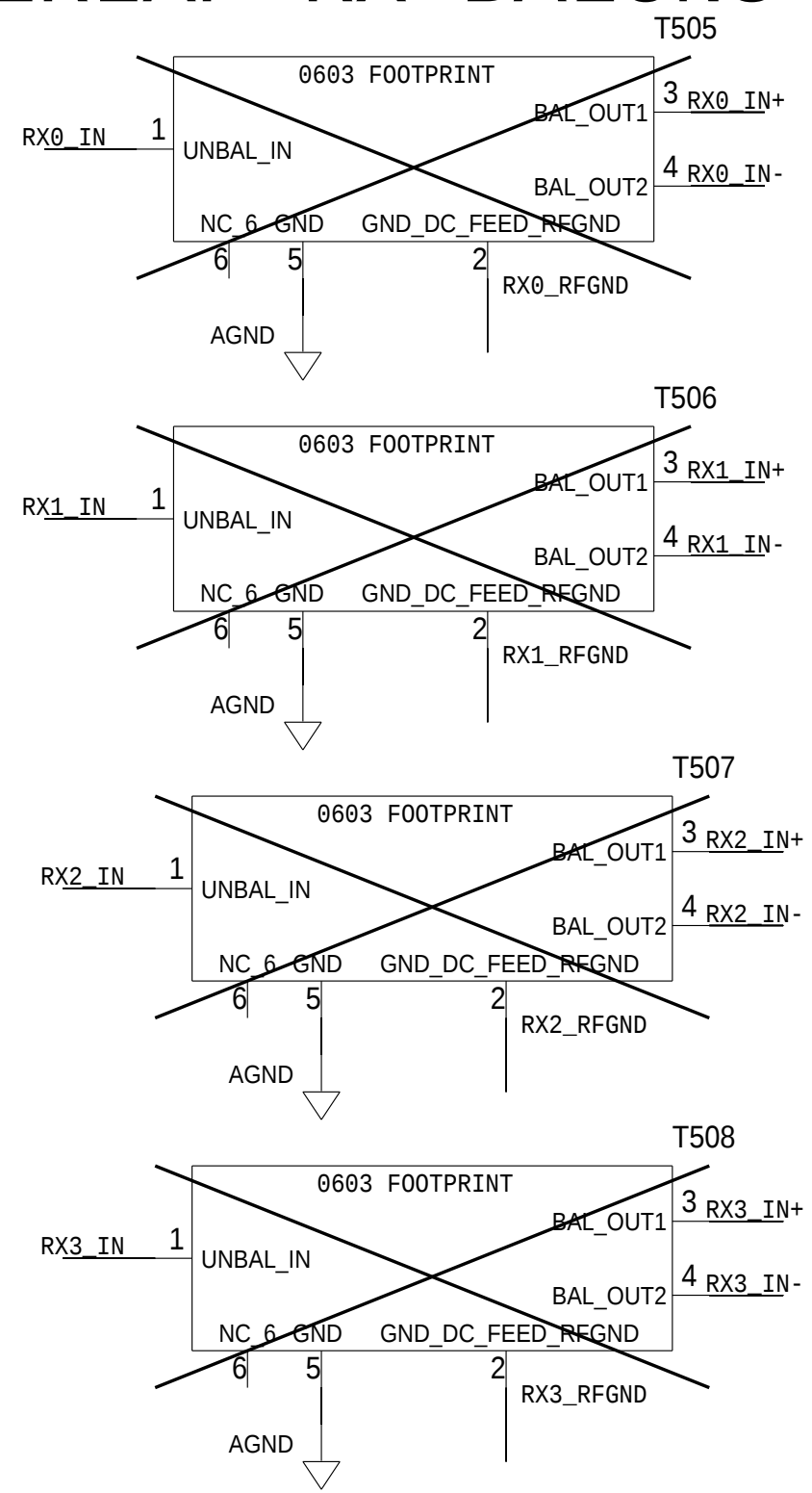


RECEIVER RF INPUT

EVb MATCH HAS BEEN DESIGNED TO SUPPORT THE WIDEST MATCH POSSIBLE;
MATCHING TOPOLOGY COULD BE REDUCED/SIMPLIFIED DEPENDING ON THE SYSTEM REQUIREMENTS.



OVERLAP RX BALUNS

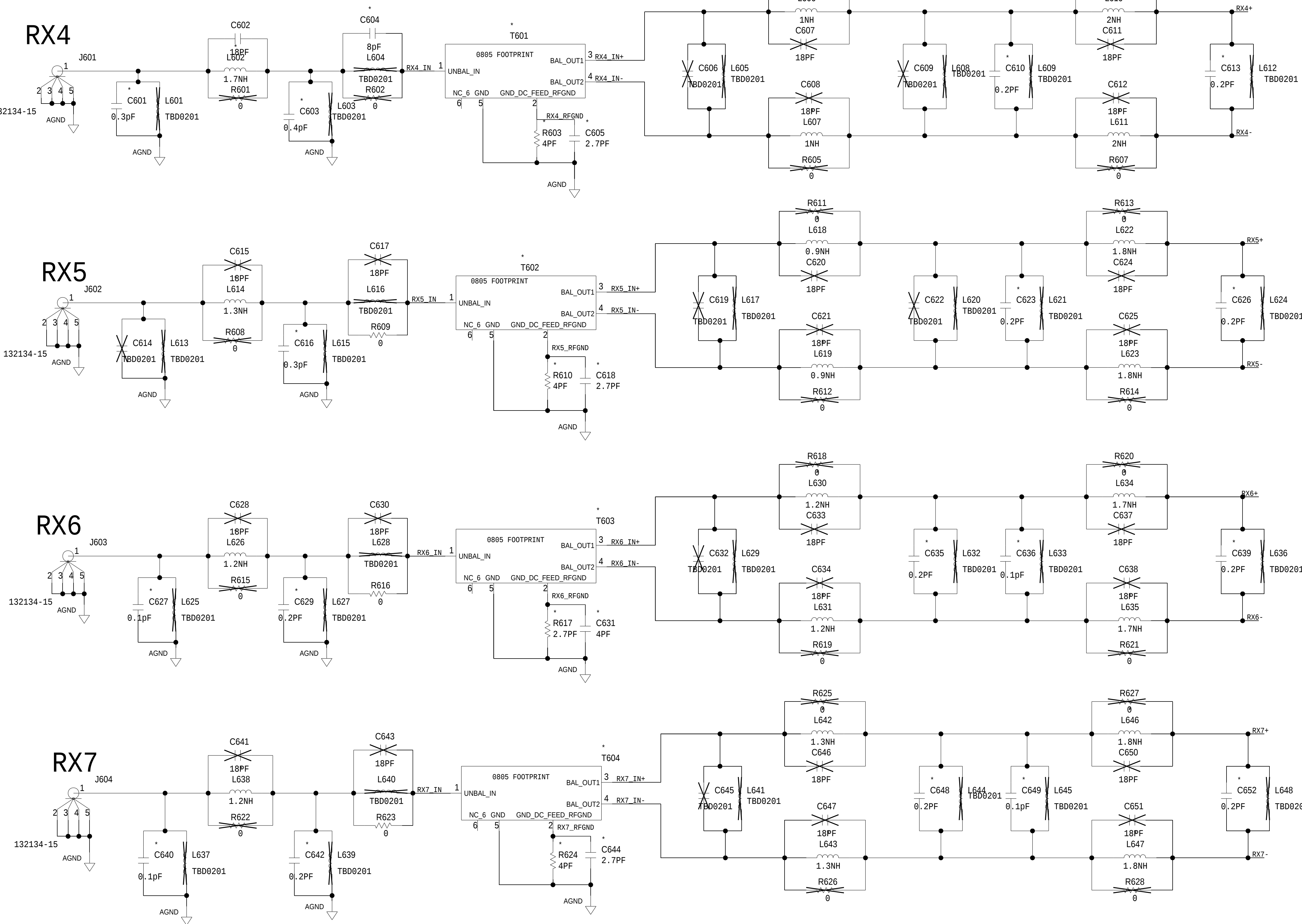


THIS DOCUMENT IS THE PROPERTY OF ANALOG DEVICES INC.
IT IS NOT TO BE REPRODUCED OR COPIED IN WHOLE OR
IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS,
OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS
OF ANALOG DEVICES.
THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS
OWNED OR CONTROLLED BY OWNED ANALOG DEVICES.

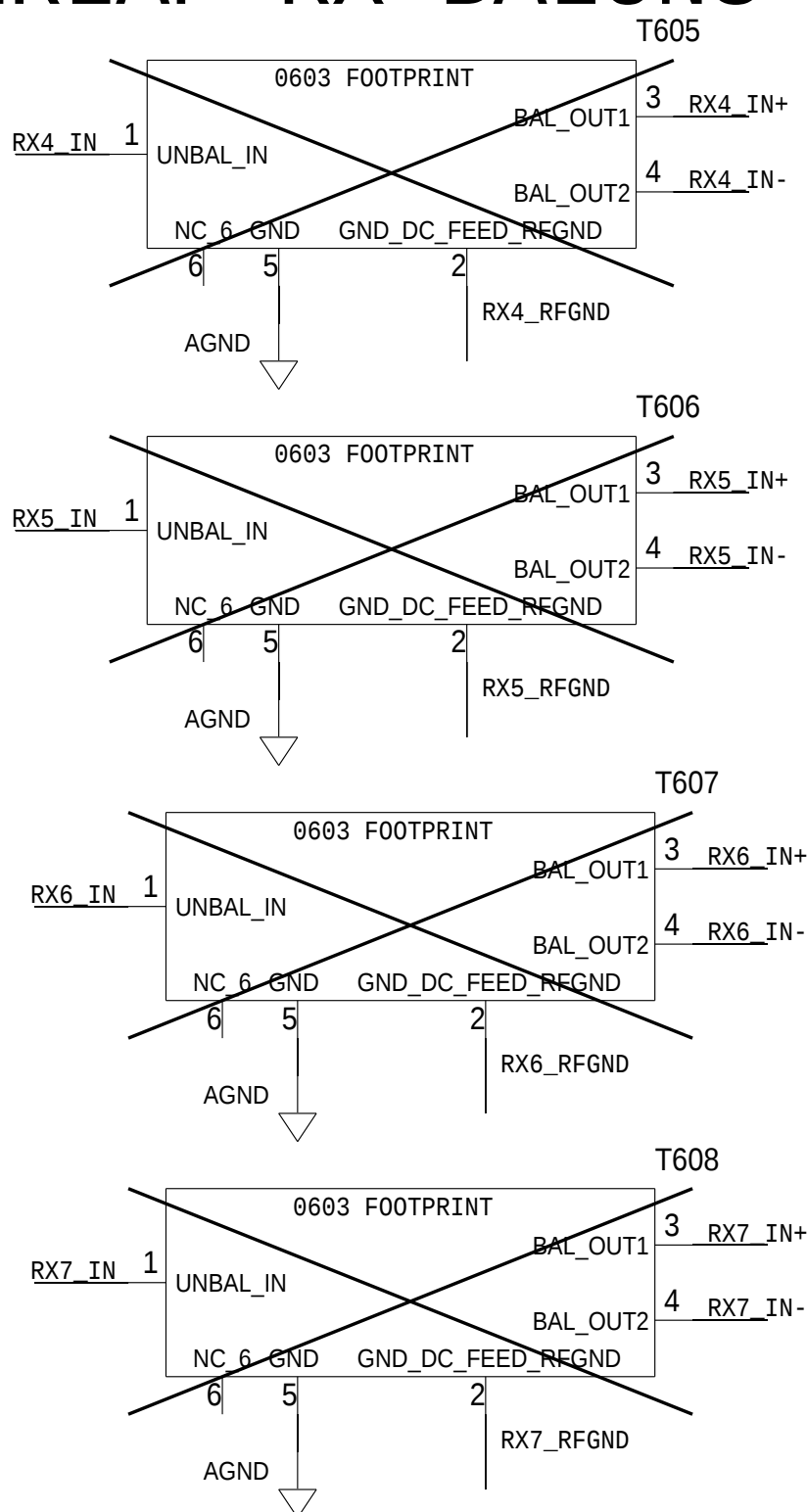
SCHEMATIC			
HW TYPE : Customer Evaluation Product(s): ADRV903x : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064540-05	REV C	
PTD ENGINEER -	SIZE D	SCALE 1:1	SHEET 5 OF 16

RECEIVER RF INPUT

EVb MATCH HAS BEEN DESIGNED TO SUPPORT THE WIDEST MATCH POSSIBLE;
MATCHING TOPOLOGY COULD BE REDUCED/SIMPLIFIED DEPENDING ON THE SYSTEM REQUIREMENTS.



OVERLAP RX BALUNS



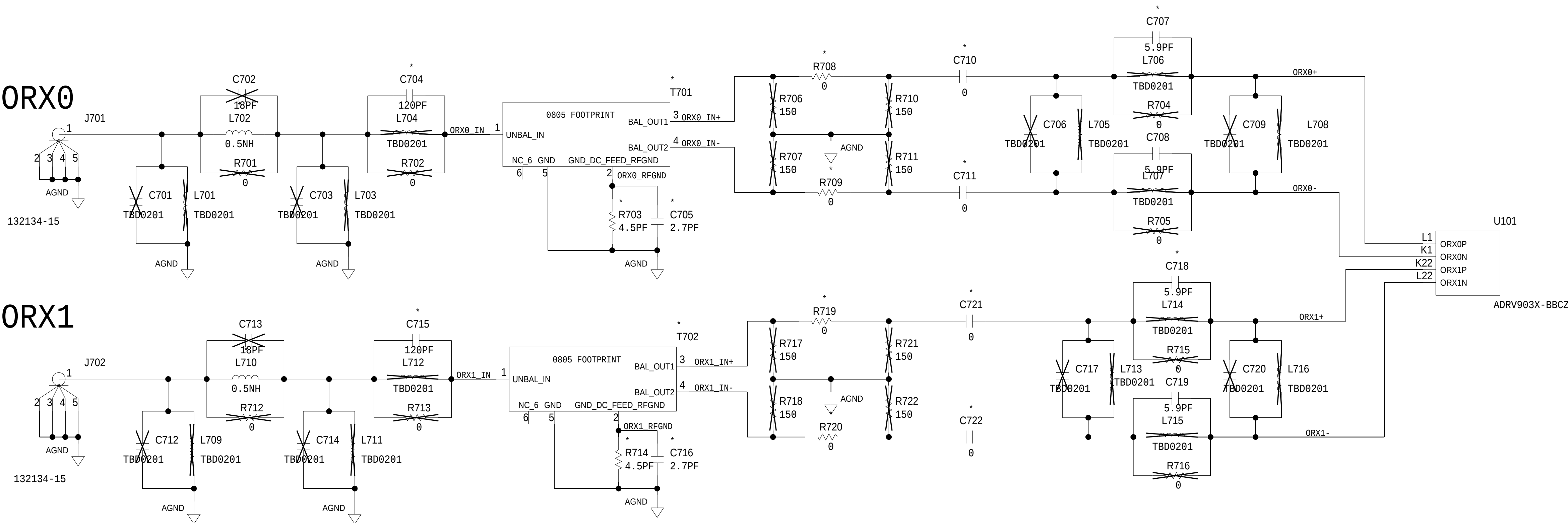
THIS DOCUMENT IS THE PROPERTY OF ANALOG DEVICES INC.
IT IS NOT TO BE REPRODUCED OR COPIED IN WHOLE OR
IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS,
OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS
OF ANALOG DEVICES.
THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS
OWNED OR CONTROLLED BY ANALOG DEVICES.

SCHEMATIC			
HW TYPE : Customer Evaluation Product(s): ADRV903x : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064540-05	REV C	
PTD ENGINEER -	SIZE D	SCALE 1:1	SHEET 6 OF 16

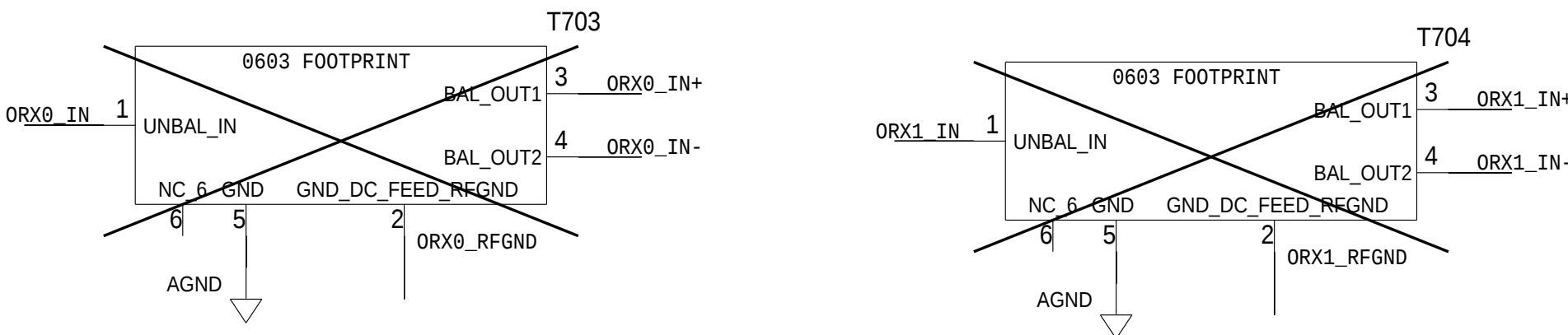
OBSERVATION RX RF INPUT

EVb MATCH HAS BEEN DESIGNED TO SUPPORT THE WIDEST MATCH POSSIBLE;
MATCHING TOPOLOGY COULD BE REDUCED/SIMPLIFIED DEPENDING ON THE SYSTEM REQUIREMENTS.

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



OVERLAP ORX BALUNS



THIS DOCUMENT IS THE PROPERTY OF ANALOG DEVICES INC.
IT IS NOT TO BE REPRODUCED OR COPIED IN WHOLE OR
IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS,
OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS
OF ANALOG DEVICES.
THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS
OWNED OR CONTROLLED BY OWNED ANALOG DEVICES.

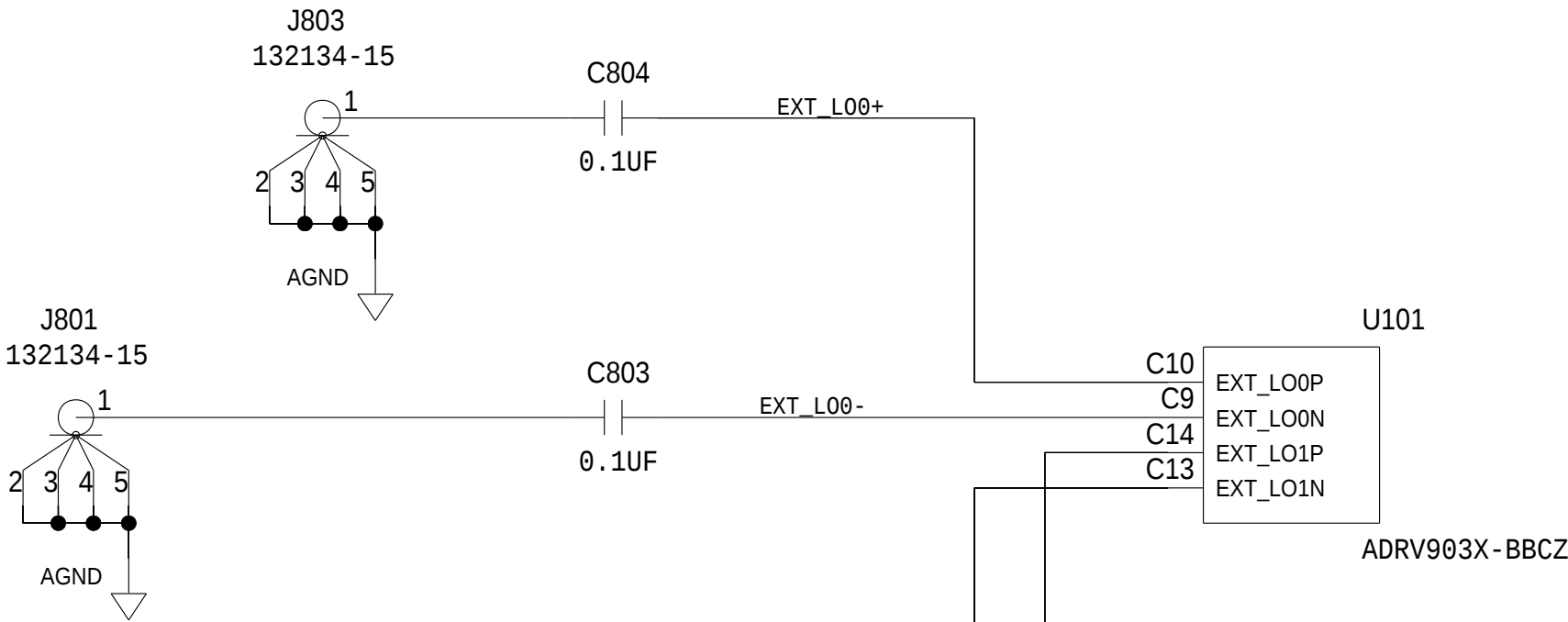
SCHEMATIC			
HW TYPE : Customer Evaluation Product(s): ADRV903x : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064540-05	REV C	
PTD ENGINEER -	SIZE D	SCALE 1:1	SHEET 7 OF 16

EXTERNAL L0 IN/OUT

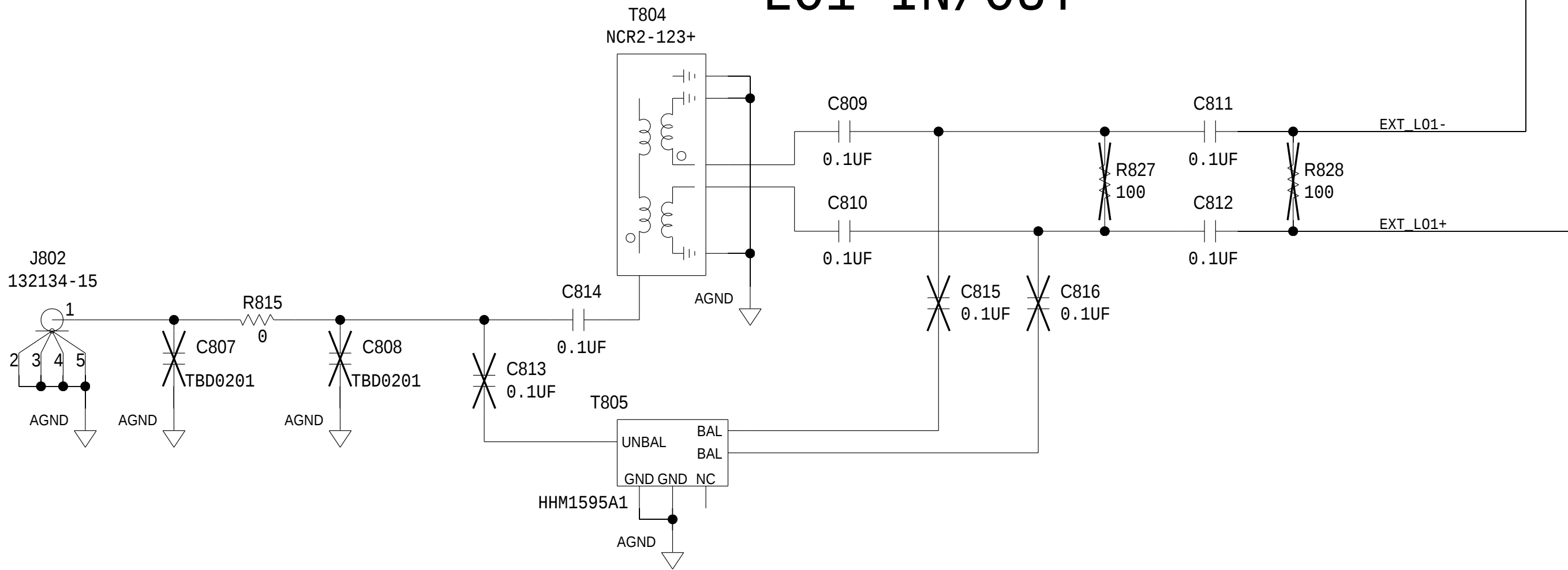
EVb MATCH HAS BEEN DESIGNED TO SUPPORT THE WIDEST MATCH POSSIBLE;
MATCHING TOPOLOGY COULD BE REDUCED/SIMPLIFIED DEPENDING ON THE SYSTEM REQUIREMENTS.

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

L00 IN/OUT



L01 IN/OUT

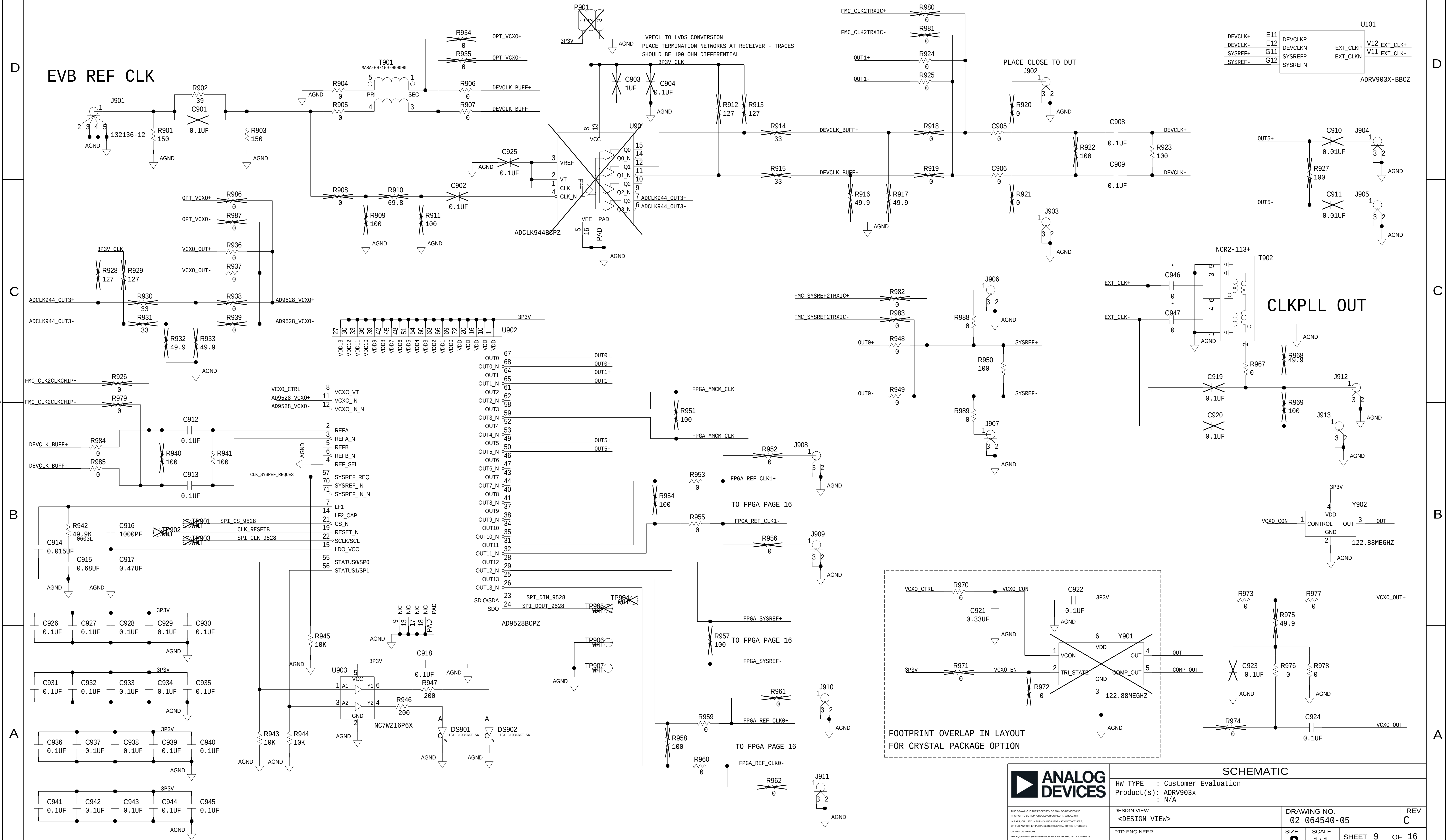


THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC.
IT IS NOT TO BE REPRODUCED OR COPIED IN WHOLE OR
IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS,
OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS
OF ANALOG DEVICES.
THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS
OWNED OR CONTROLLED BY OWNED ANALOG DEVICES.

SCHEMATIC					
HW TYPE : Customer Evaluation Product(s): ADRV903x : N/A					
DESIGN VIEW <DESIGN_VIEW>			DRAWING NO. 02_064540-05		REV C
PTD ENGINEER -			SIZE D	SCALE 1:1	SHEET 8 OF 16

CLOCK GENERATION/DISTRIBUTION

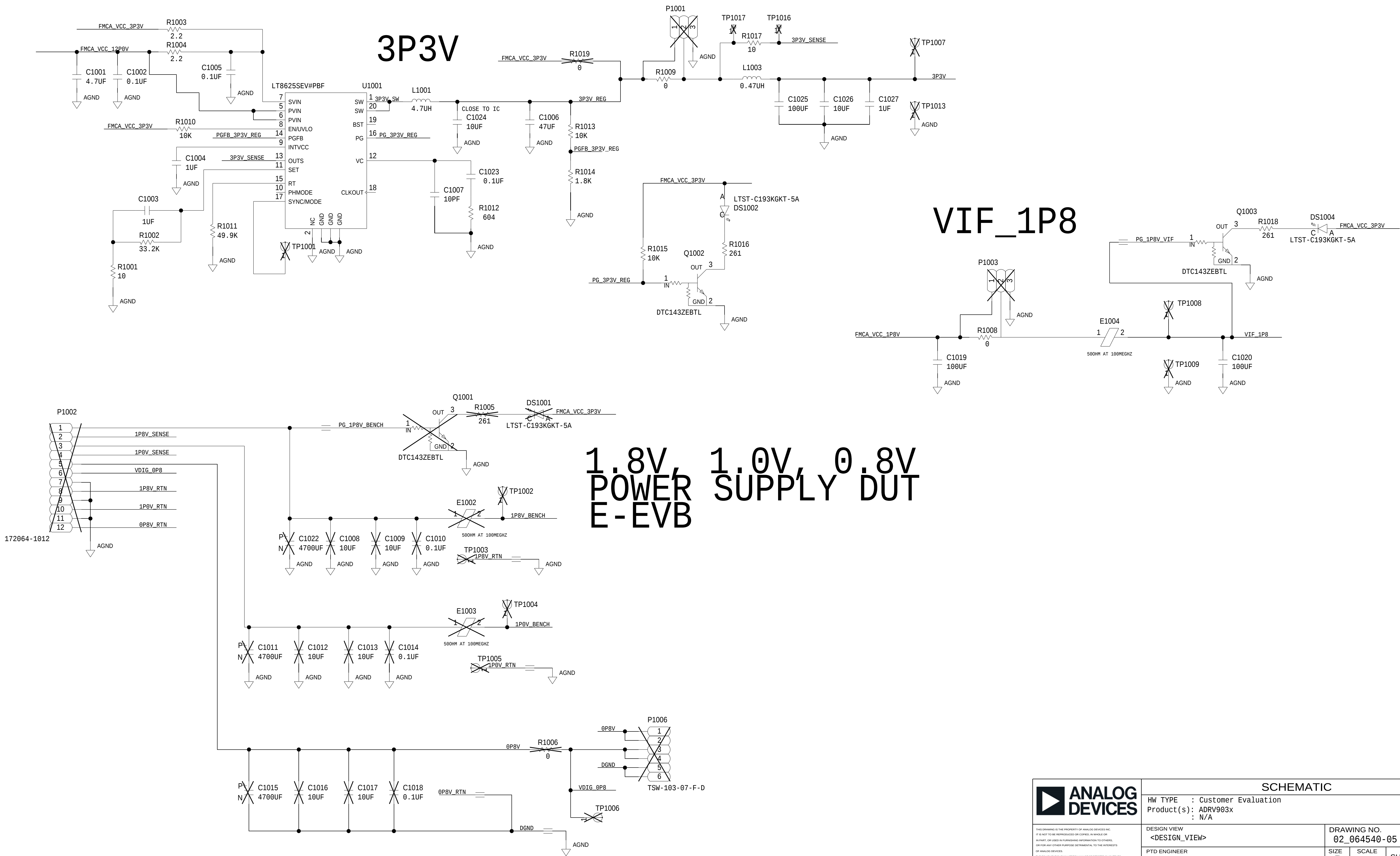
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



	SCHEMATIC				
	HW TYPE : Customer Evaluation Product(s) : ADRV903x : N/A				
THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN ANY MANNER WITHOUT THE APPROVAL OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY OWNED ANALOG DEVICES.	DESIGN VIEW <u><DESIGN_VIEW></u>		DRAWING NO. 02_064540-05		REV C
	PTD ENGINEER *	D	SCALE 1:1	SHEET 9	OF 16

POWER SUPPLY

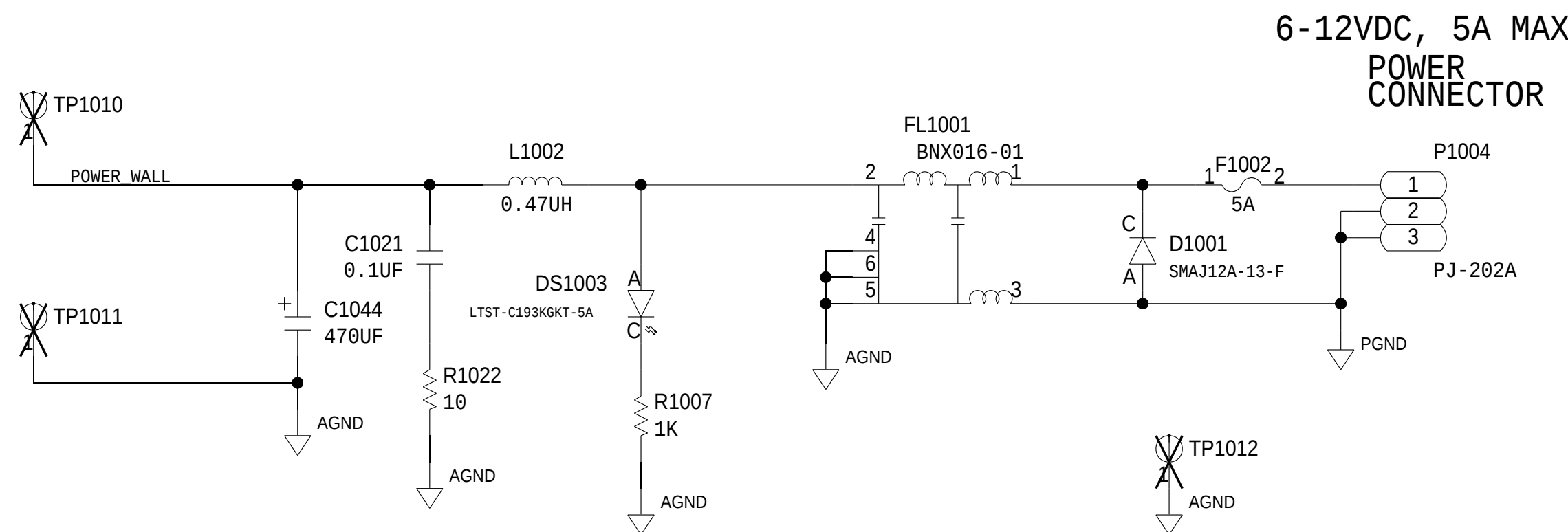
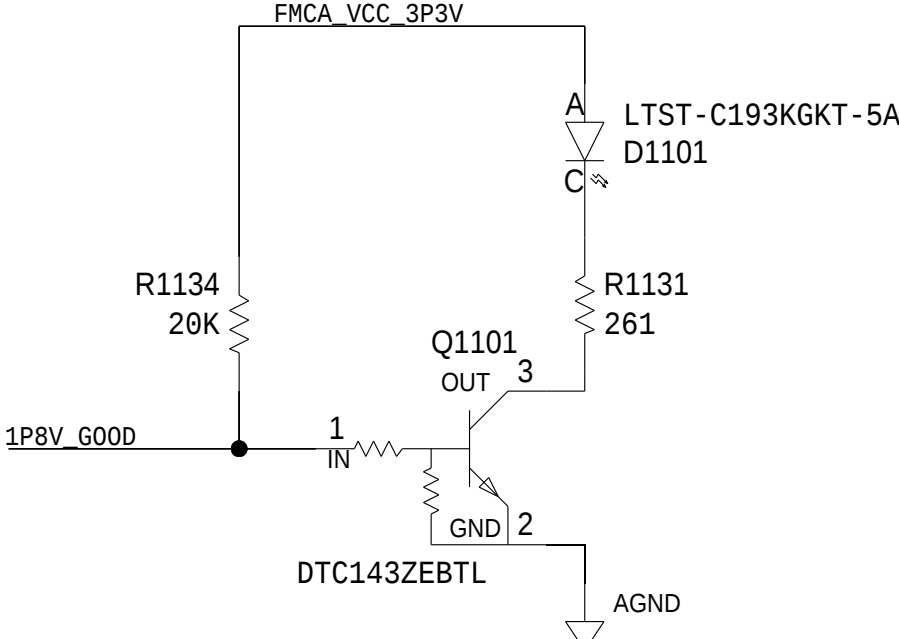
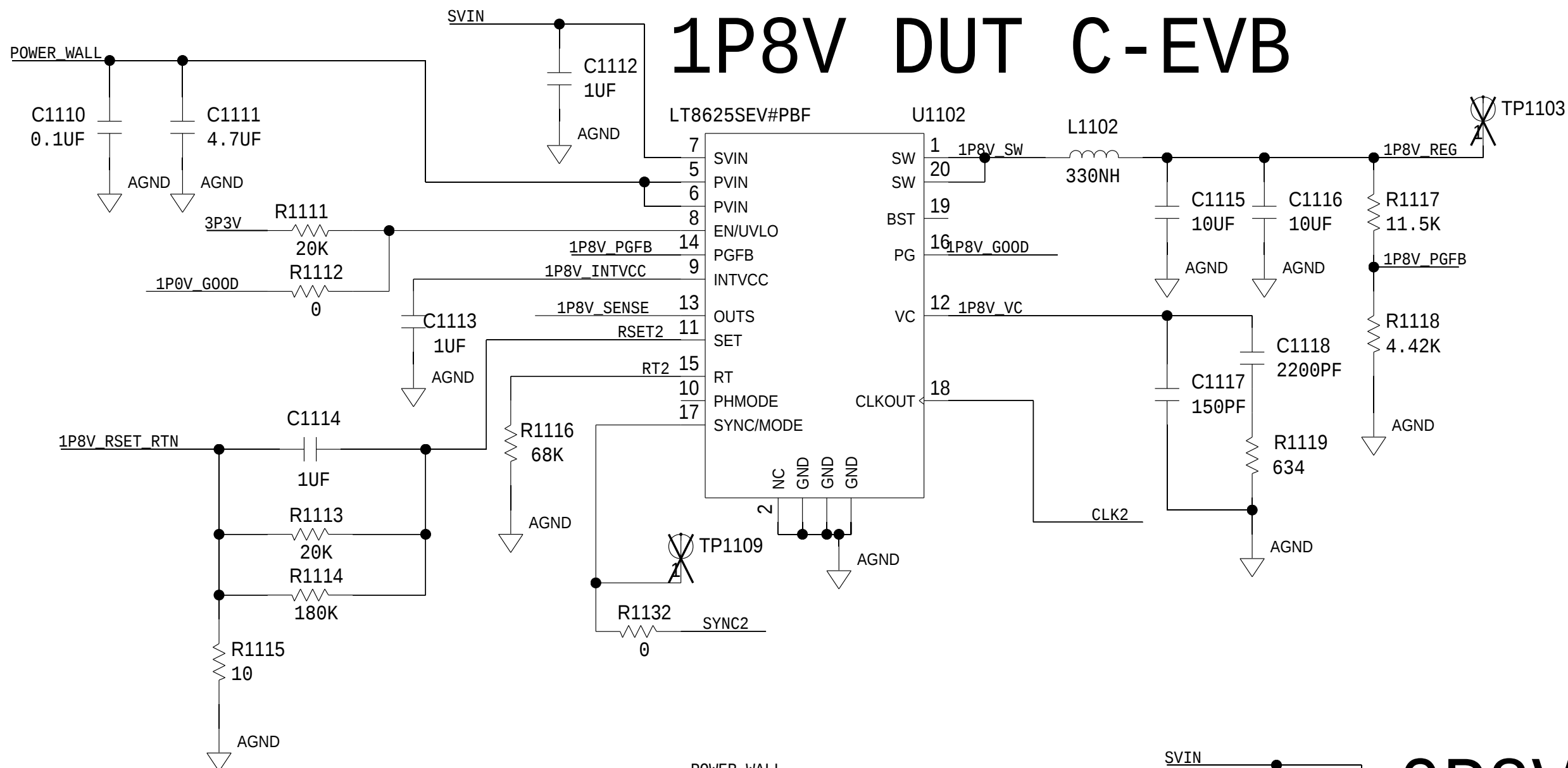
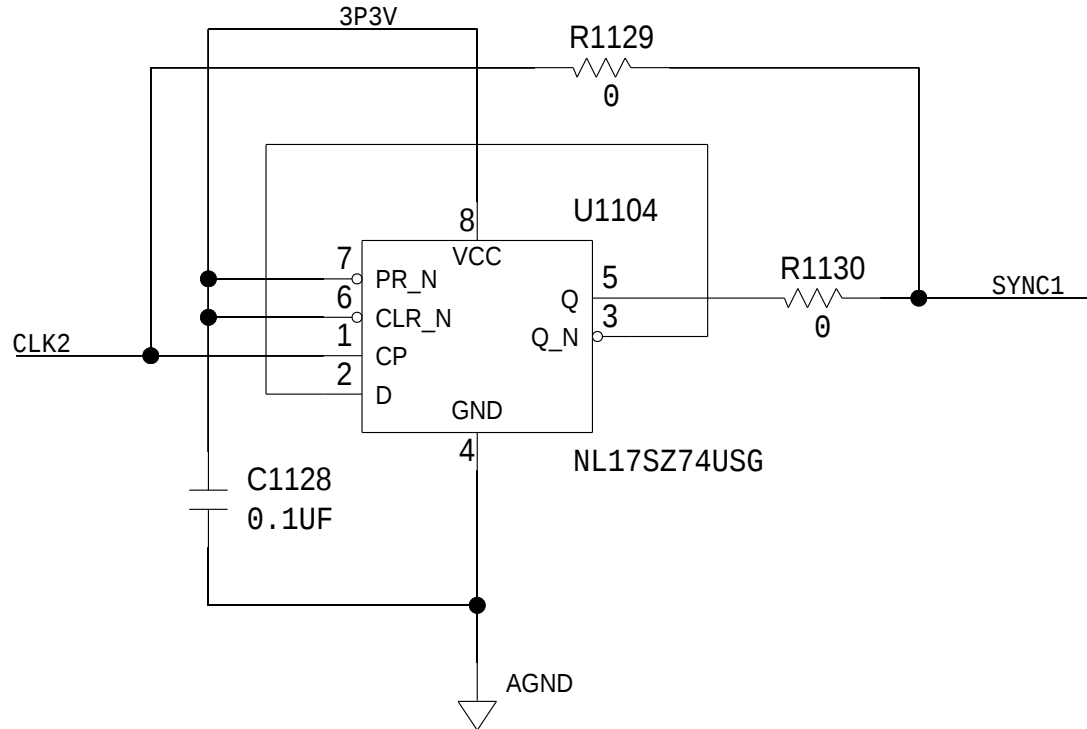
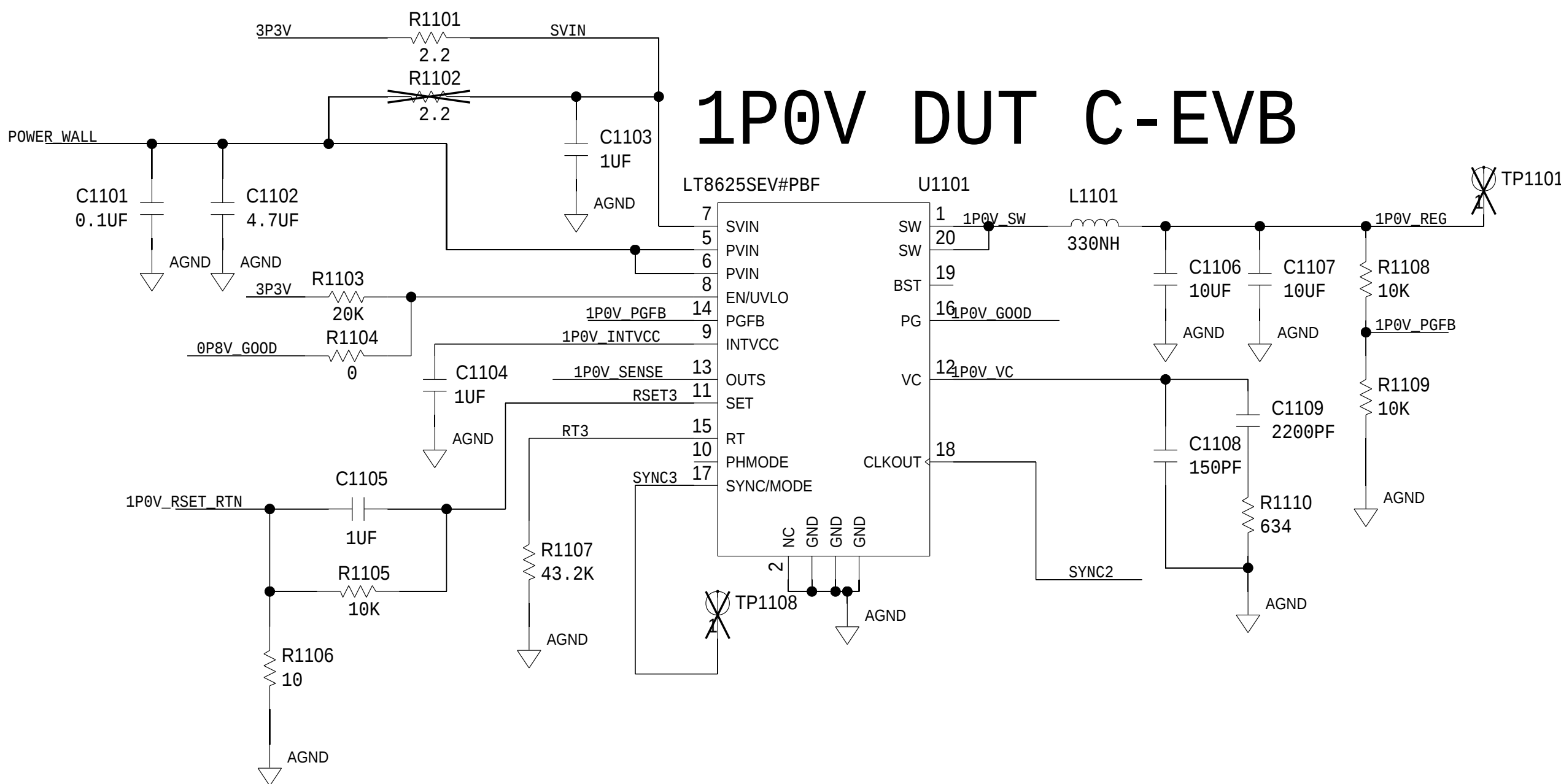
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



SCHEMATIC			
HW TYPE : Customer Evaluation Product(s): ADRV903x : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064540-05	REV C	
PTD ENGINEER -	SIZE D	SCALE 1:1	SHEET 10 OF 16

POWER SILENT SWITCHER

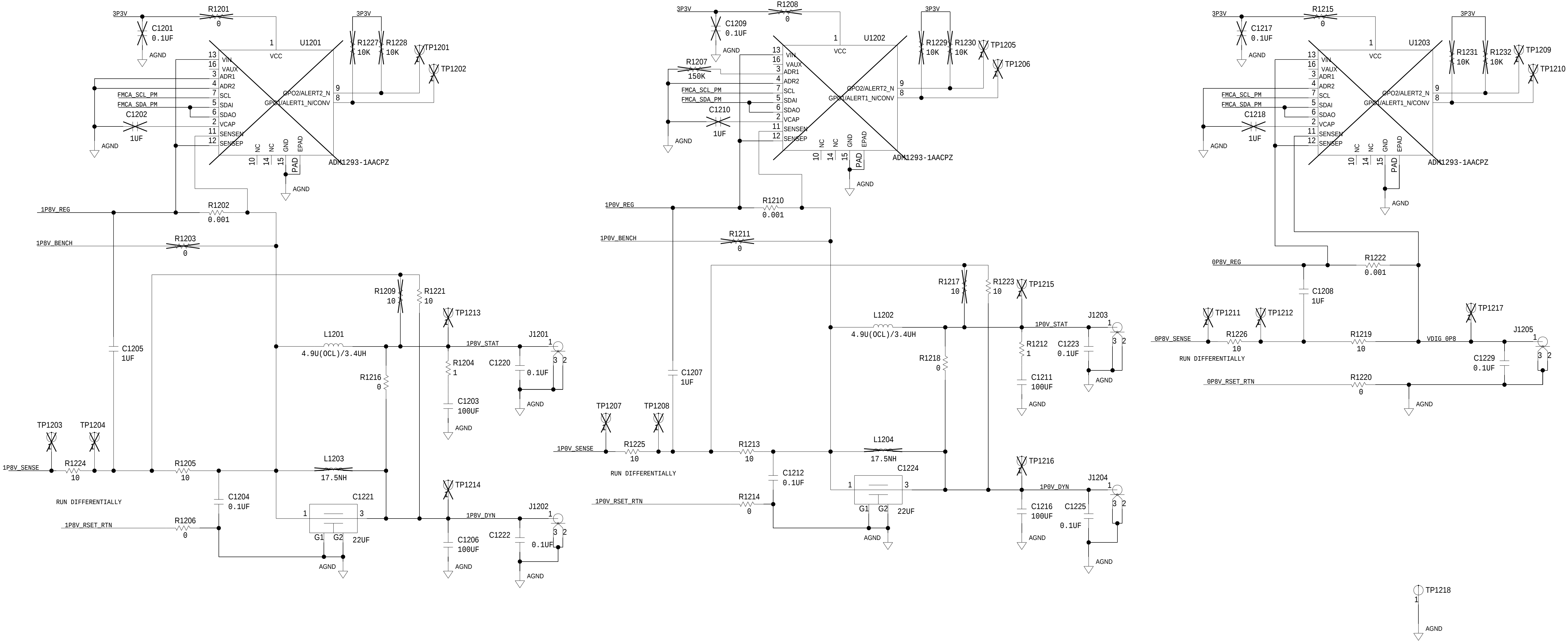
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



SCHEMATIC			
HW TYPE : Customer Evaluation Product(s): ADRV903x : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064540-05	REV C	
PTD ENGINEER -	SIZE D	SCALE 1:1	SHEET 11 OF 16

POWER MEASUREMENT & FILTERING

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

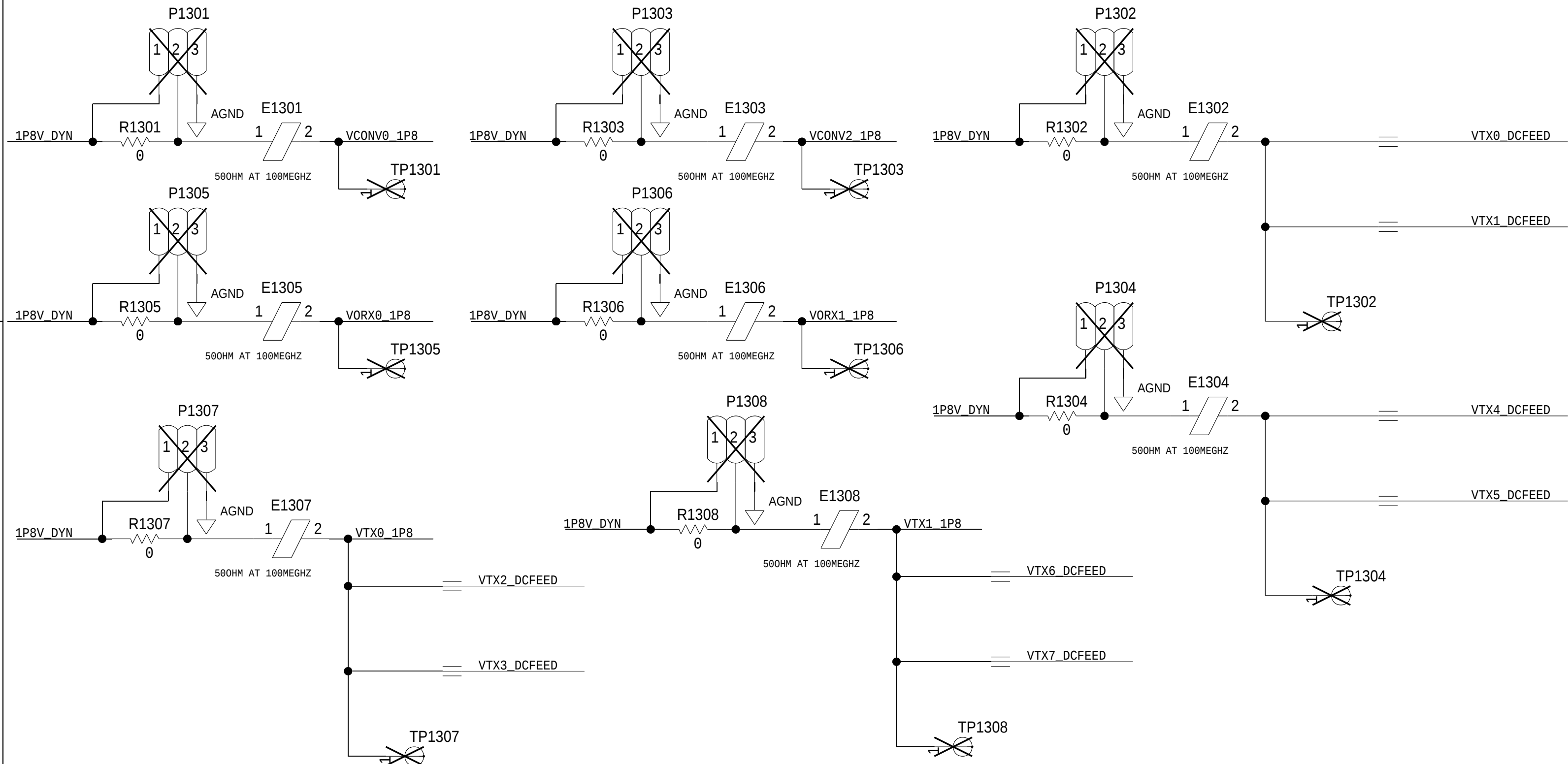


THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC.
IT IS NOT TO BE REPRODUCED OR COPIED IN WHOLE OR
IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS,
OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS
OF ANALOG DEVICES.
THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS
OWNED OR CONTROLLED BY ANALOG DEVICES.

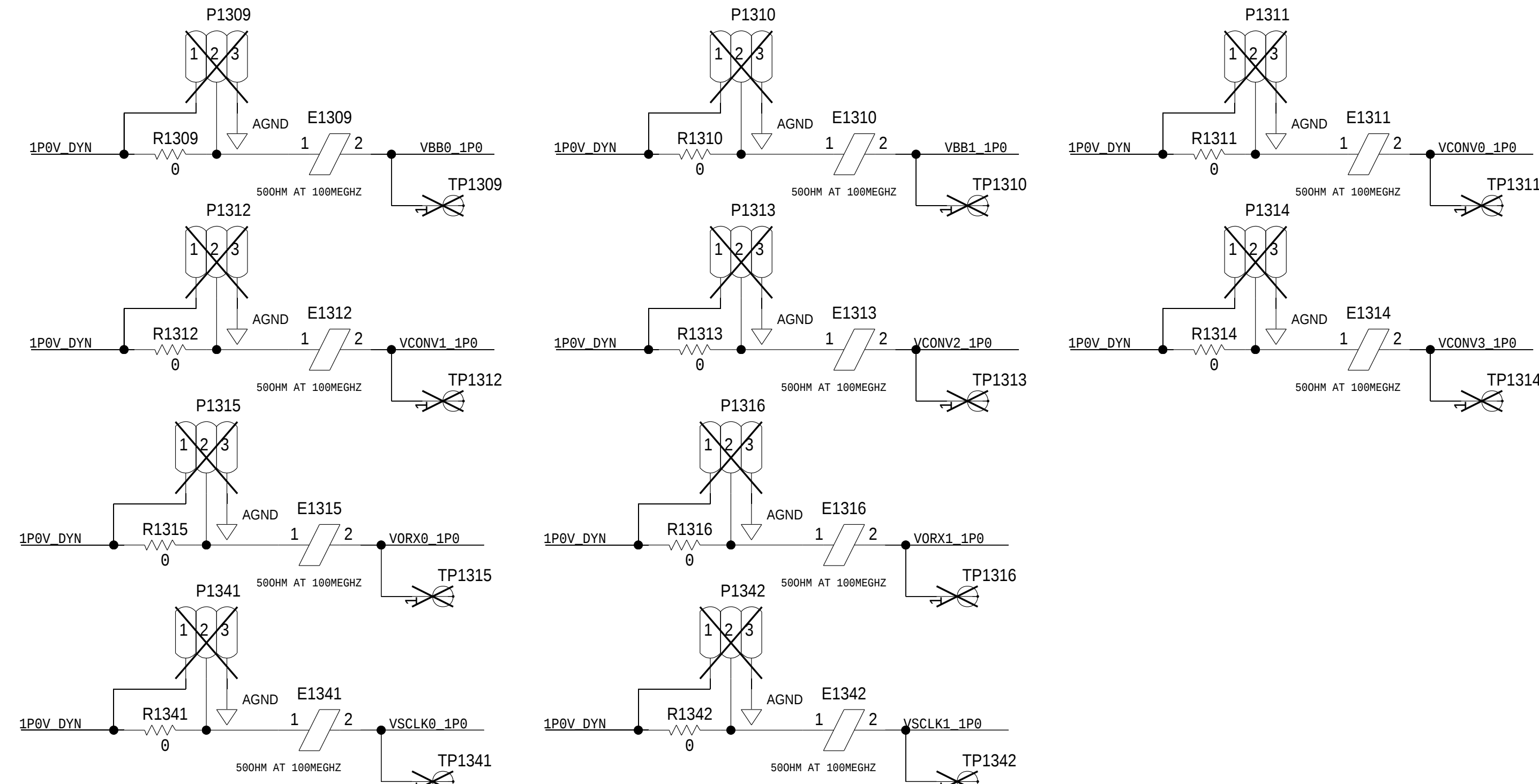
SCHEMATIC			
HW TYPE : Customer Evaluation Product(s): ADRV903x : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064540-05	REV C	
PTD ENGINEER -	SIZE D	SCALE 1:1	SHEET 12 OF 16

POWER DISTRIBUTION

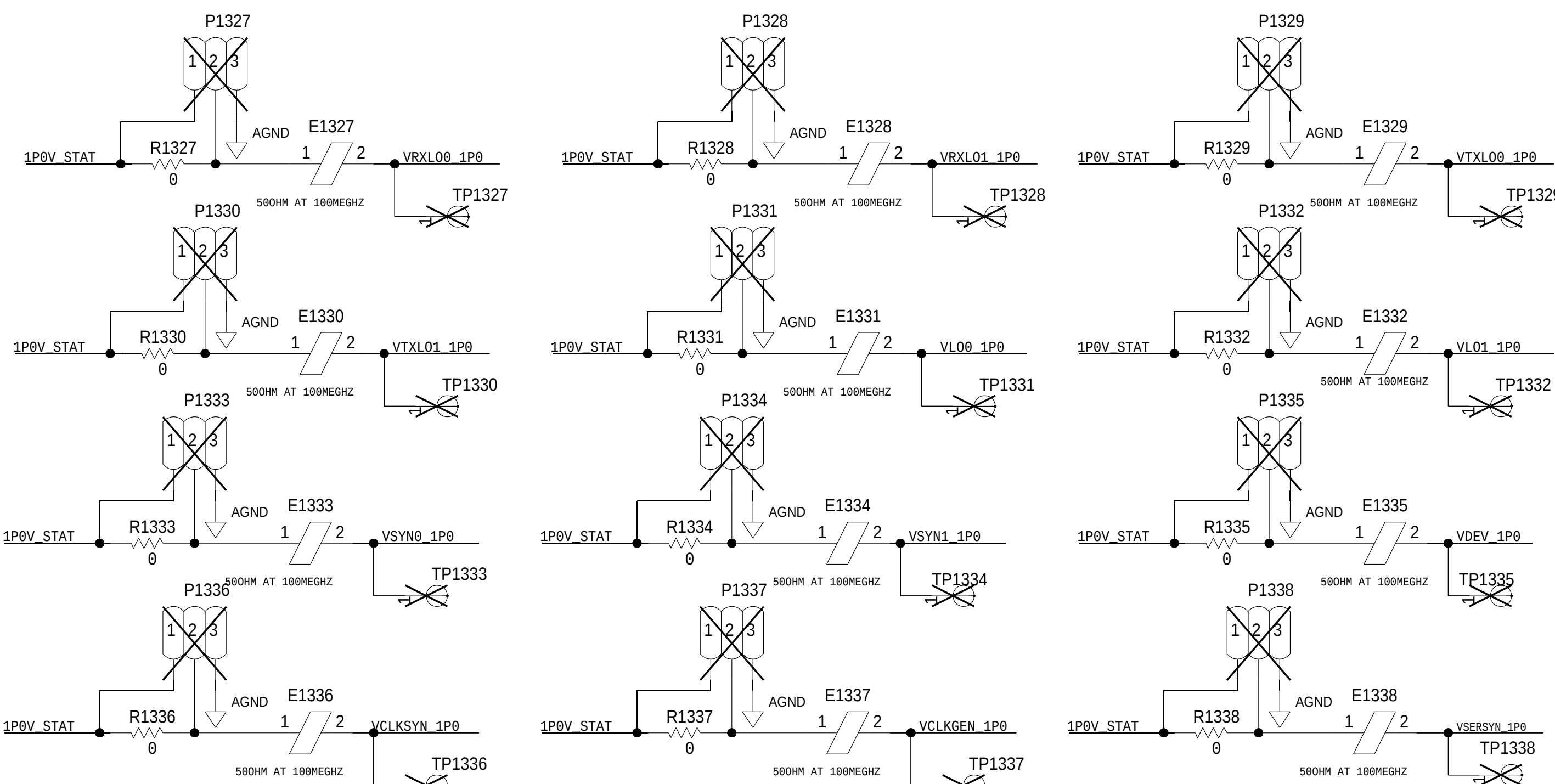
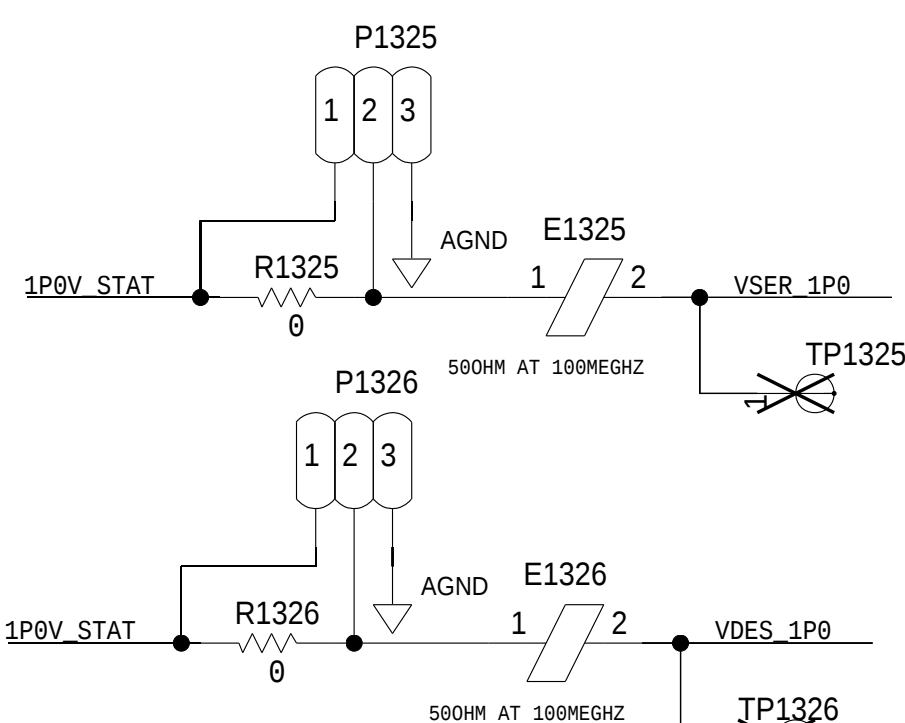
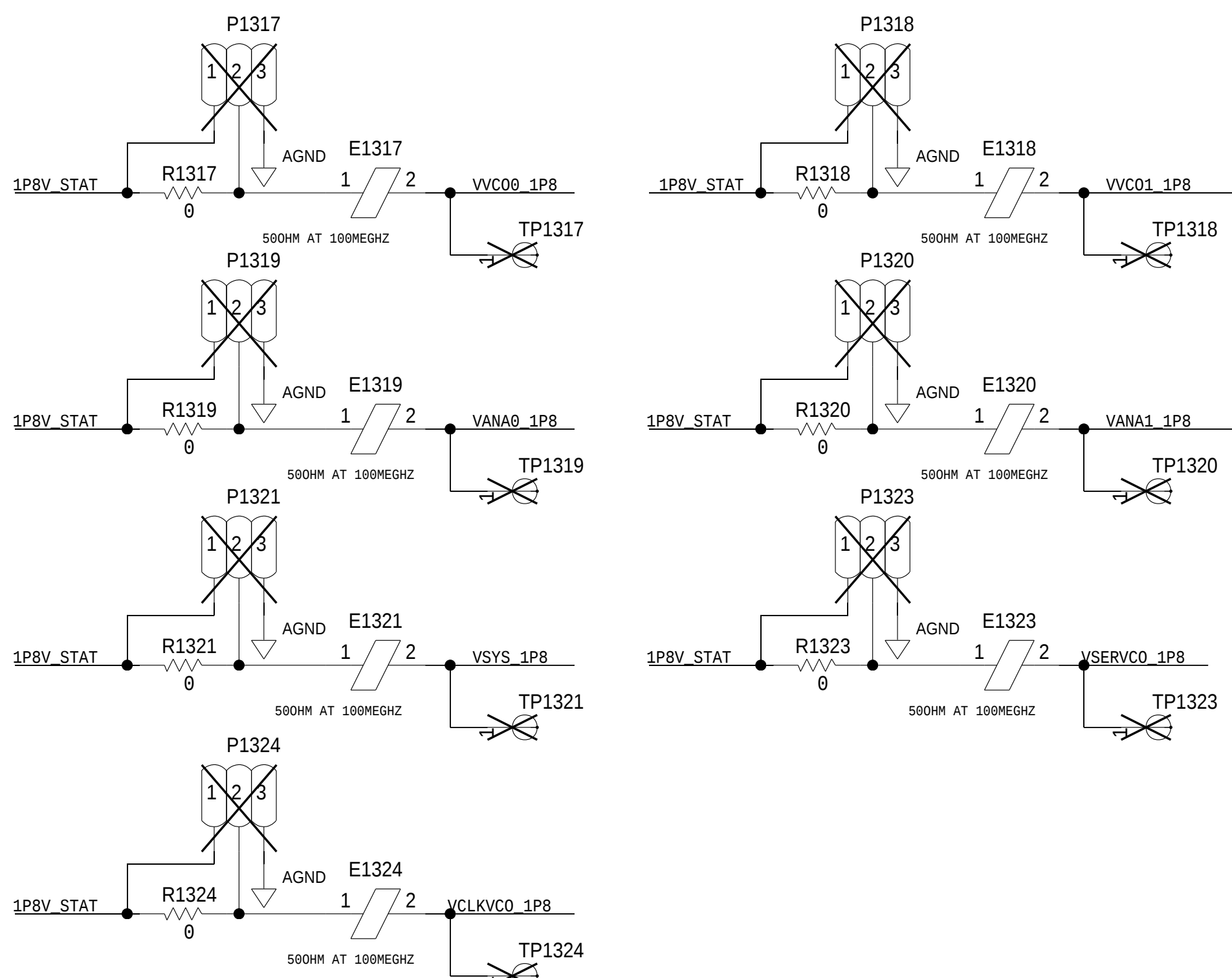
1.8V DYNAMIC SUPPLIES



1.0V DYNAMIC SUPPLIES



1.8V STATIC SUPPLIES



THIS DOCUMENT IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREIN MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.

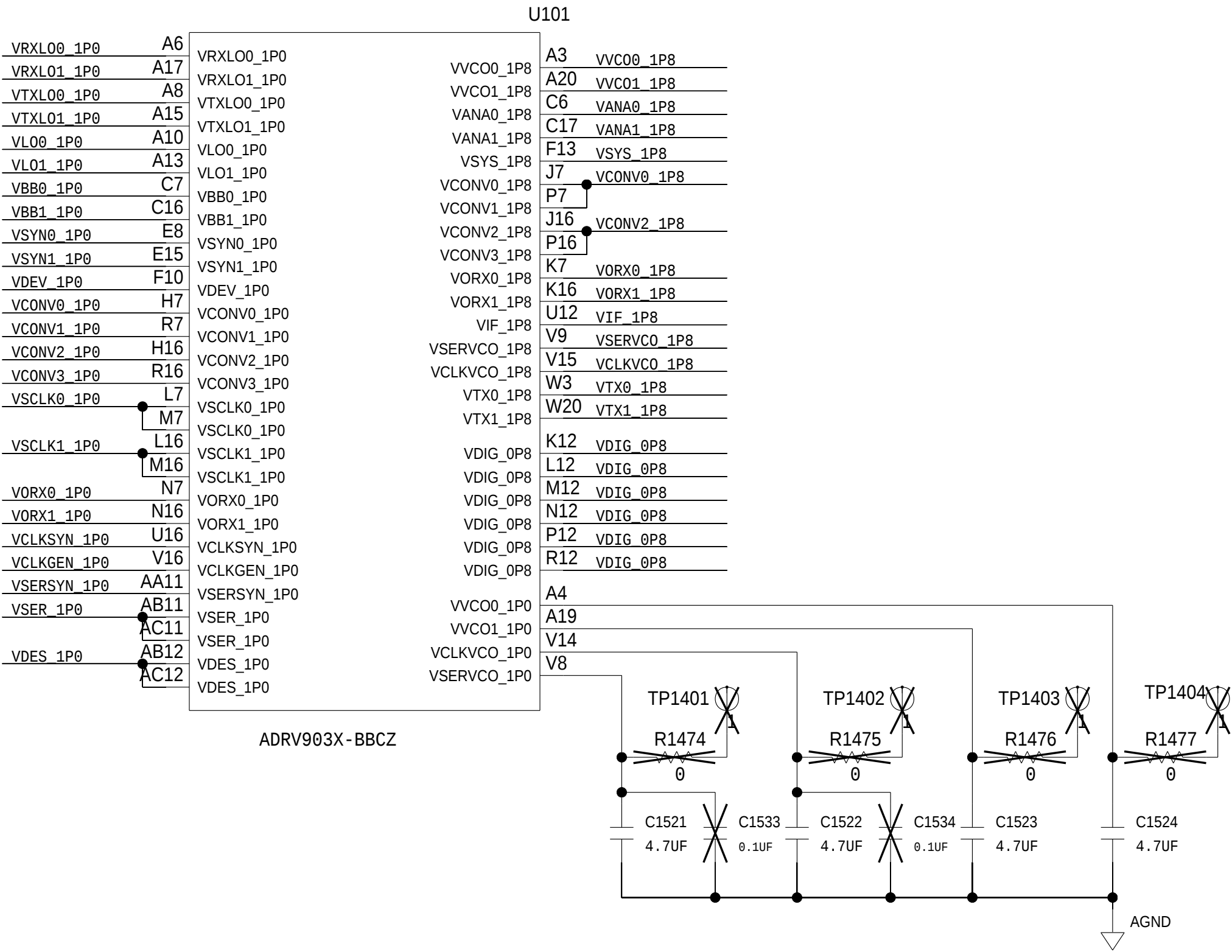
SCHEMATIC			
HW TYPE : Customer Evaluation Product(s): ADRV903x : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064540-05	REV C	
PTD ENGINEER -	SIZE D	SCALE 1:1	SHEET 13 OF 16

POWER INPUT

1.8V CAPS

0.8V CAPS

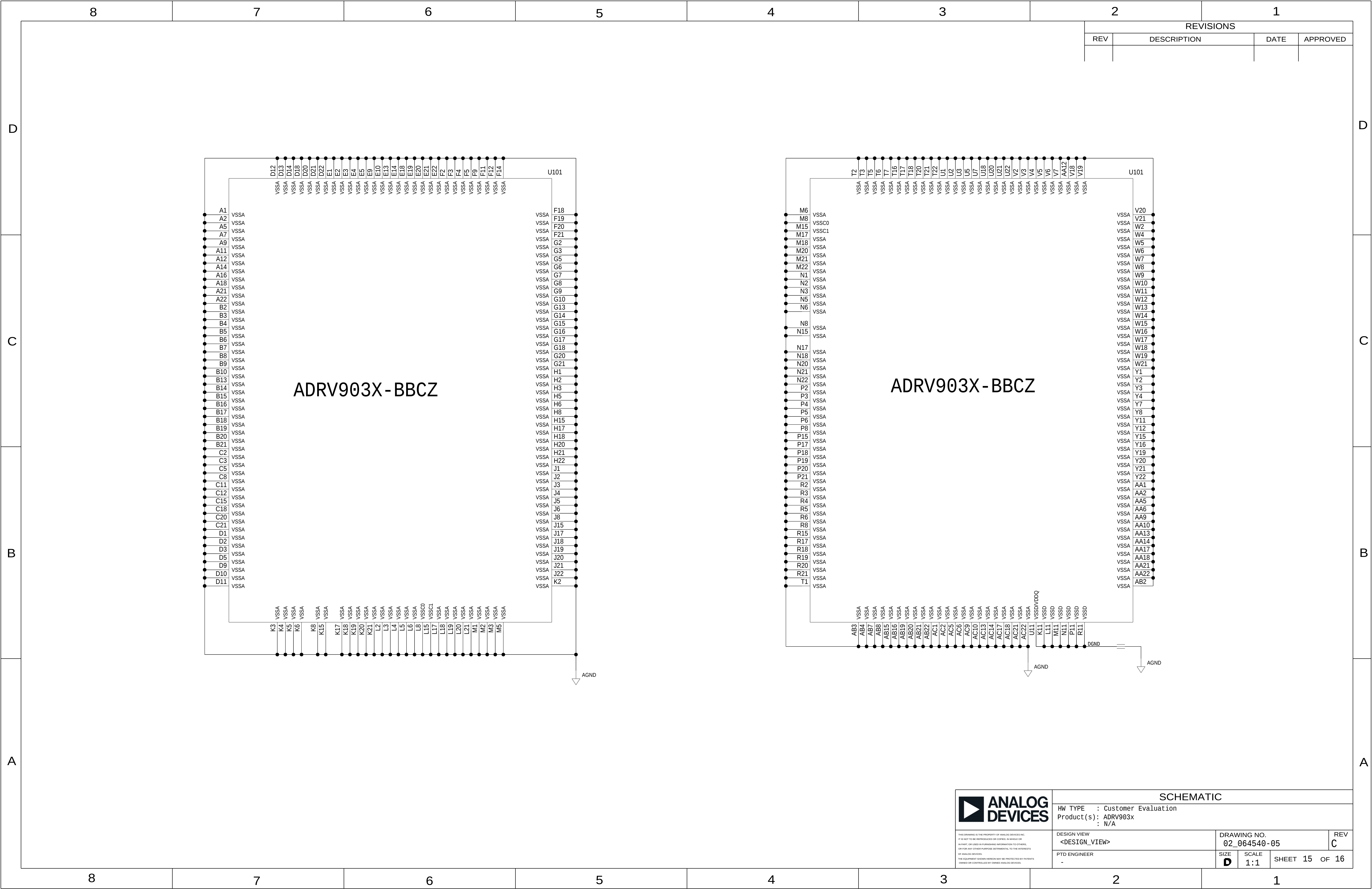
1.0V CAPS





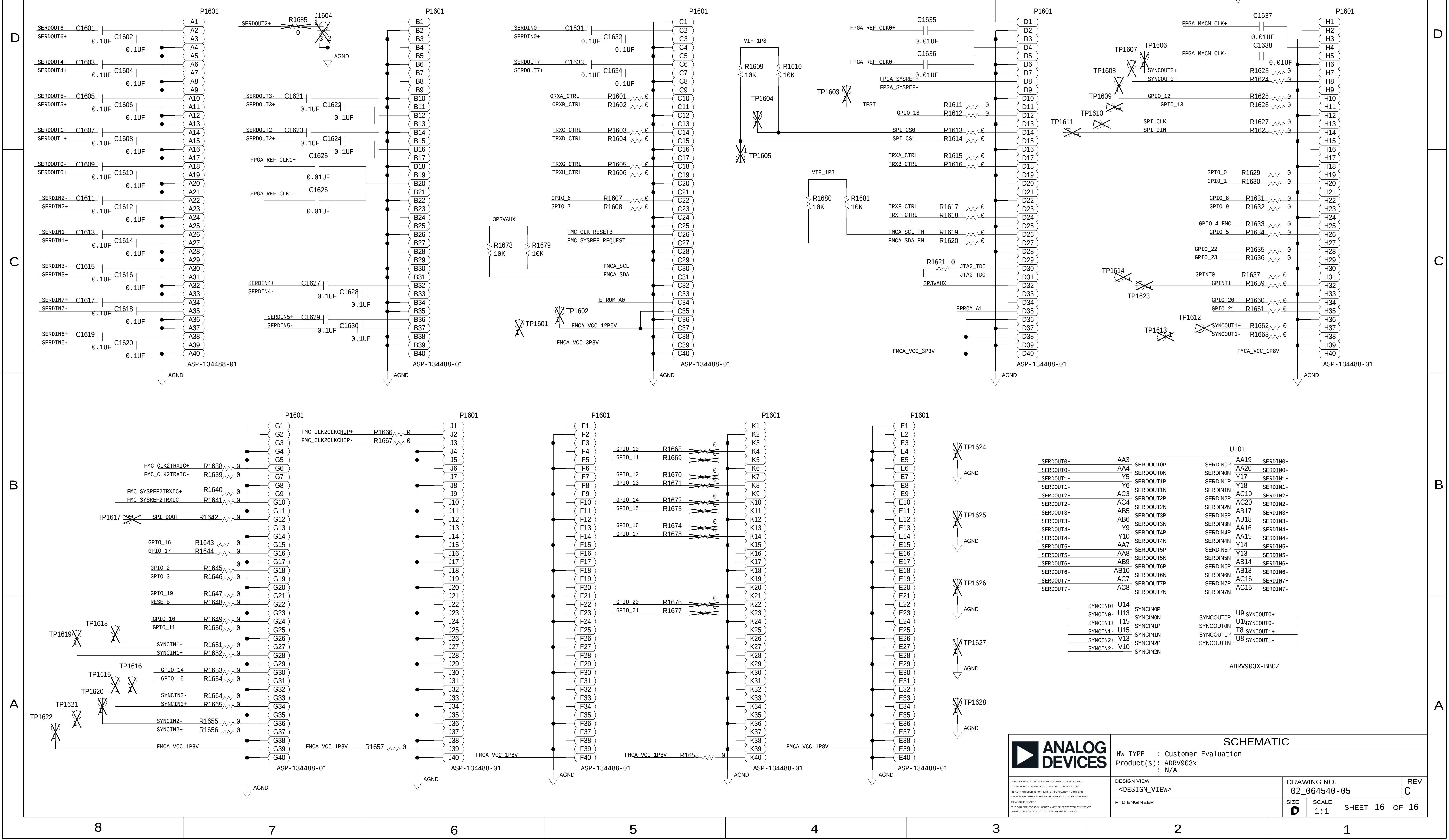
THIS DOCUMENT IS THE PROPERTY OF ANALOG DEVICES INC.
IT IS NOT TO BE REPRODUCED OR COPIED IN WHOLE OR
IN PART, OR USED IN ANY MANNER THAT IS DETRIMENTAL TO THE INTERESTS
OF ANALOG DEVICES.
THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS
OWNED OR CONTROLLED BY ANALOG DEVICES.

SCHEMATIC			
HW TYPE : Customer Evaluation Product(s) : ADRV903x : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064540-05	REV C	
PTD ENGINEER -	SIZE D	SCALE 1:1	SHEET 14 OF 16



FPGA CONNECTIONS

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



SCHEMATIC			
HW TYPE : Customer Evaluation Product(s) : ADRV903x : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064540-05	REV C	
PTD ENGINEER -	SIZE D	SCALE 1:1	SHEET 16 OF 16