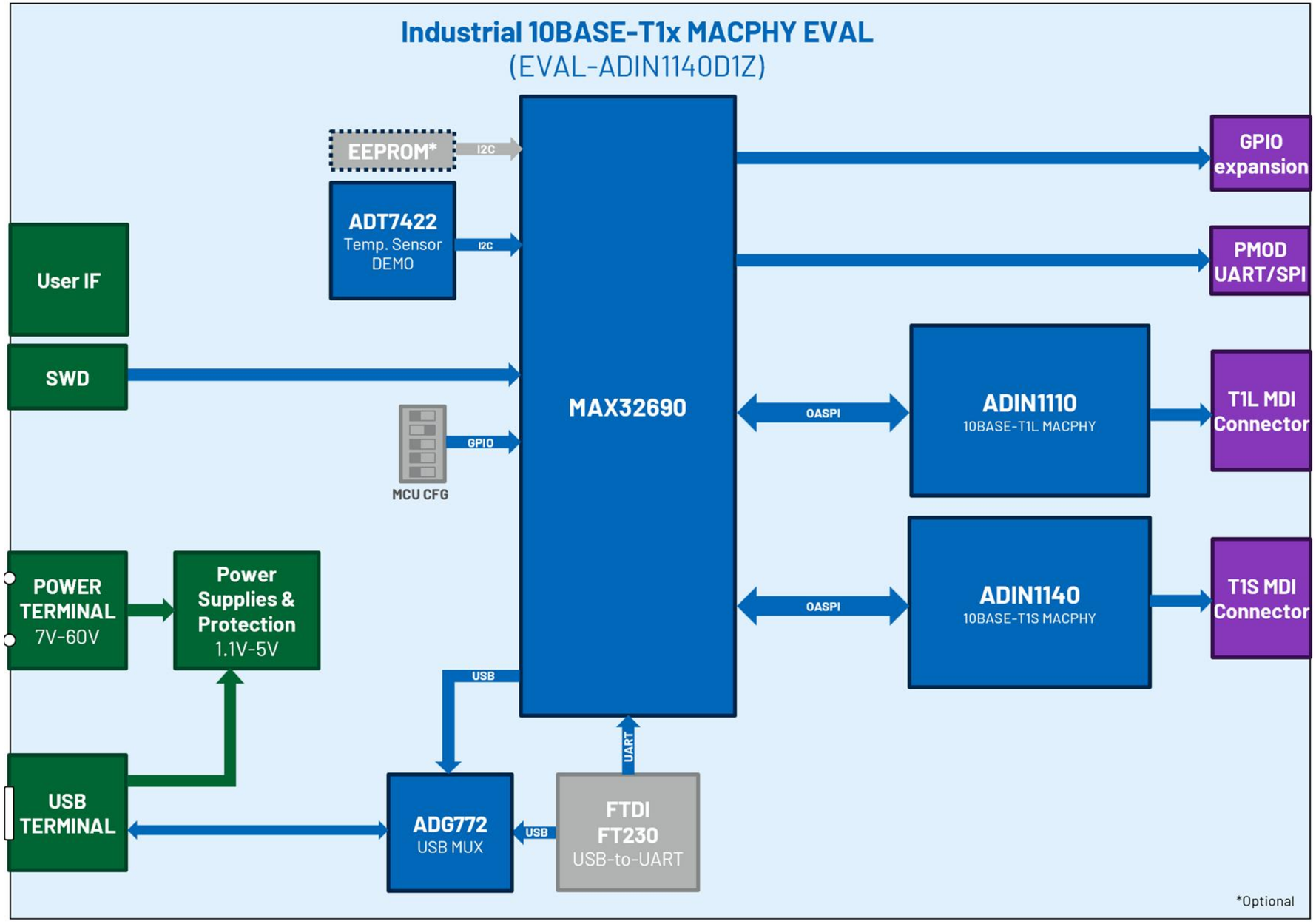
10BASE-T1S ADIN1140 EVAL Platform

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



ANALOG DEVICES

SCHEMATIC

HW TYPE : Customer Evaluation
Product(s): ADIN1140,ADIN1100,MAX32690

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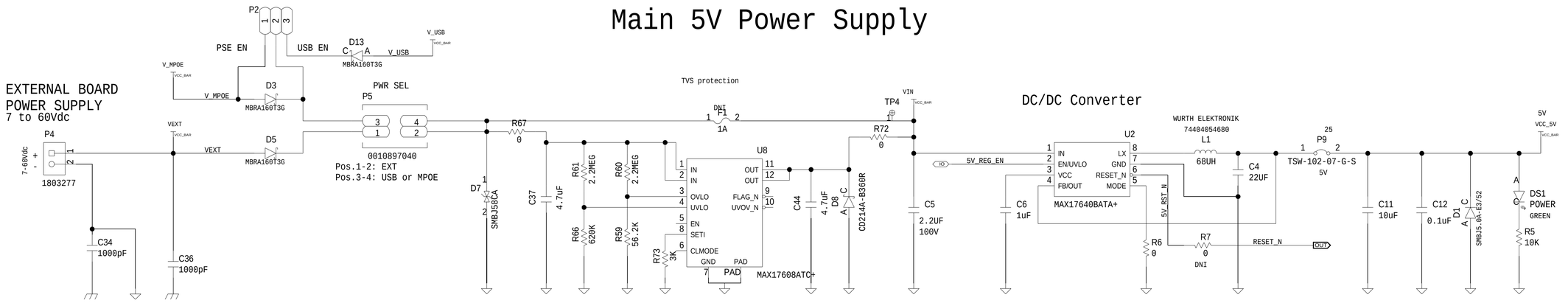
DESIGN VIEW	DRAWING NO.	REV
<DESIGN_VIEW>	02_083876	D

PTD ENGINEER	SIZE	SCALE	SHEET
-	D	1:1	1 OF 7

POWER

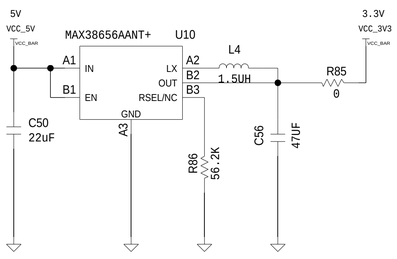
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

Main 5V Power Supply

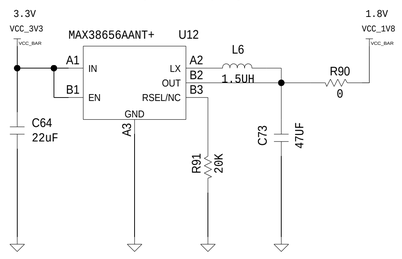


Digital Power Supplies (1.1V, 1.2V, 1.8V & 3.3V)

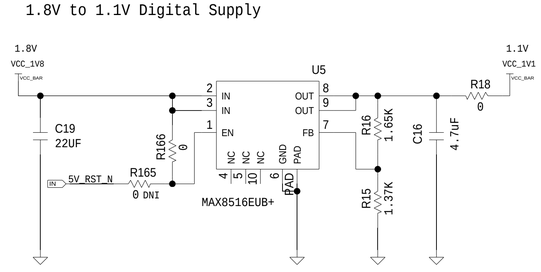
5V to 3.3V Digital Supply



3.3V to 1.8V Digital Supply

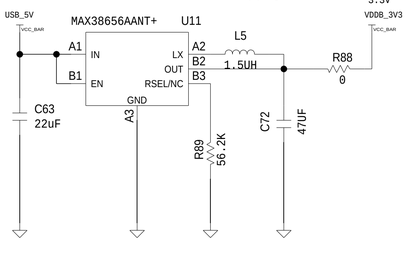


1.8V to 1.1V Digital Supply



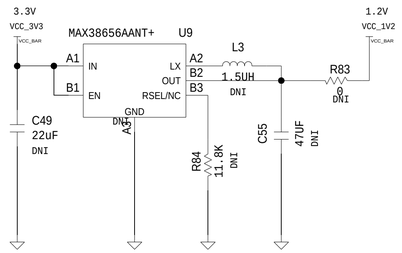
MAX32690 requires Vcore to be supplied in a sequence after VDDIO

VUSB 5V to MCU VDDB 3.3V Supply

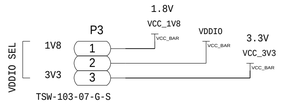


Voltage should be applied to the VDDB pin only while the VBUS signal is present and removed whenever the VBUS signal is not present

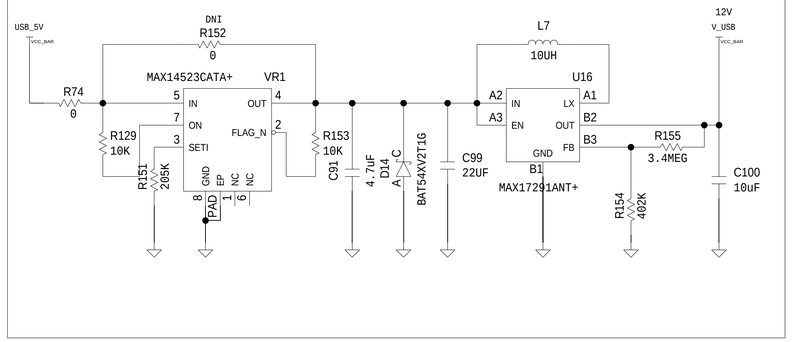
3.3V to 1.2V Digital Supply



Used when External Clock is used

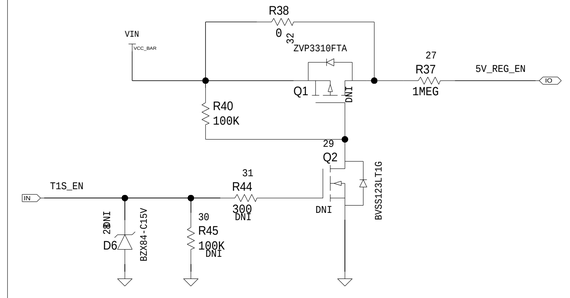


USB Supply Step-up



5V REG WAKE UP EN by T1S

(Optional)

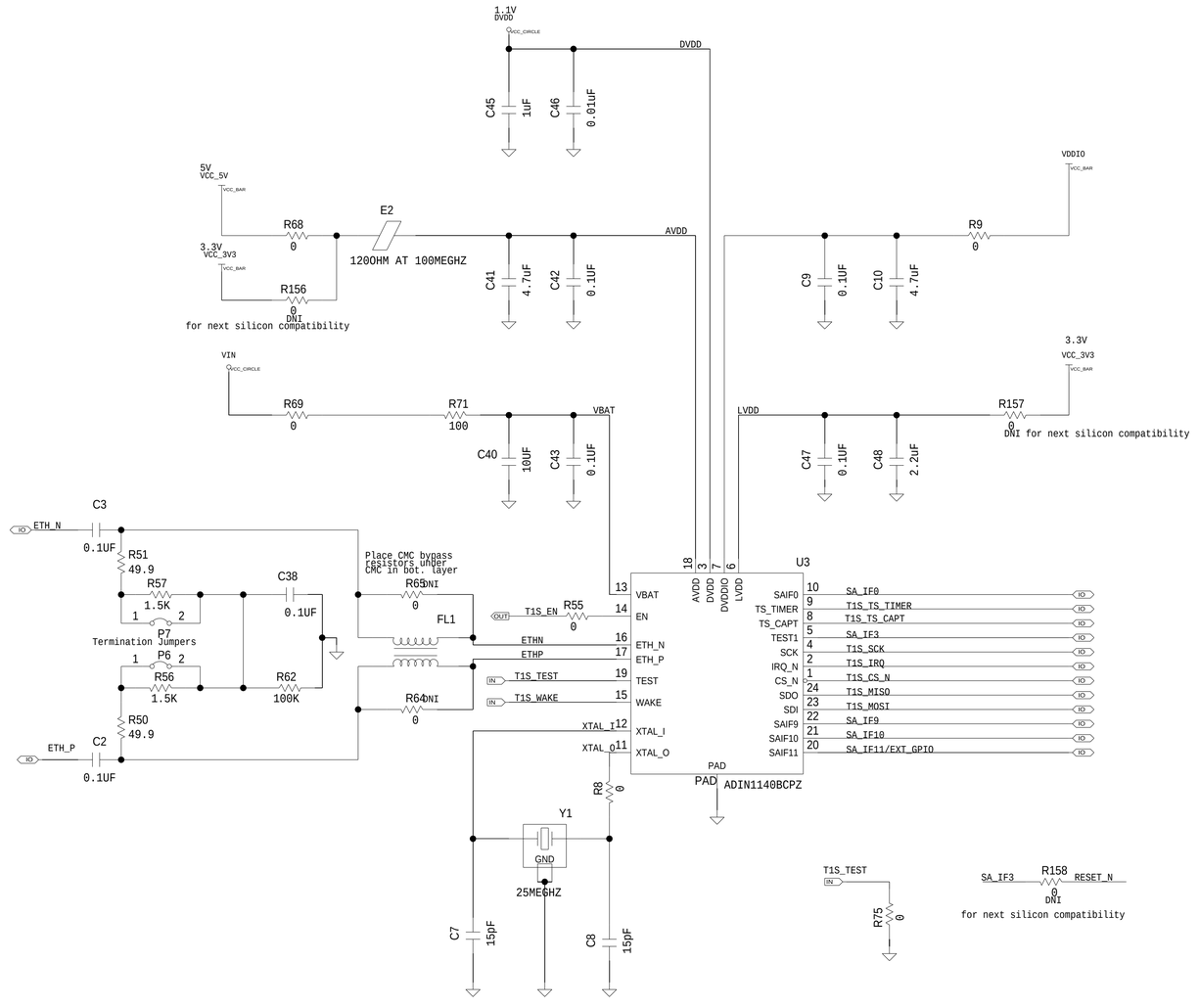


ANALOG DEVICES	SCHEMATIC			
	HW TYPE : Customer Evaluation Product(s): ADIN1140,ADIN1100,MAX32690			
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	PTD ENGINEER -	SIZE D	SCALE 1:1	SHEET 2 OF 7

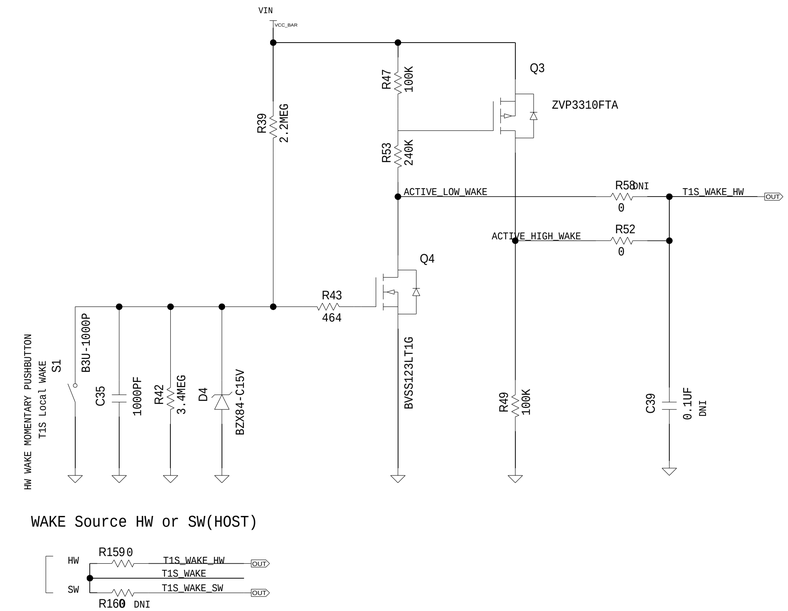
ADIN1140 CIRCUIT

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

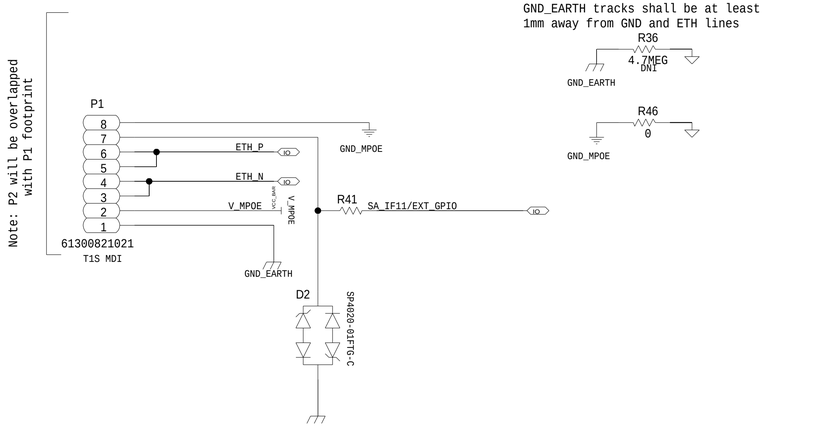
Decoupling Capacitors to belplaced close to the pin.
Capacitors order from the pin out starts with the lowest value
LENGTH MATCH ETH_P & ETH_N
USE SILVER PLATED CONTACTS FOR -40 TO +170 DEGREESC CONNECTOR RATING
DO NOT USE TIN PLATED CONTACTS WHICH ARE ONLY RATED TO +105 DEGREEC



ADIN1140 LOCAL WAKE



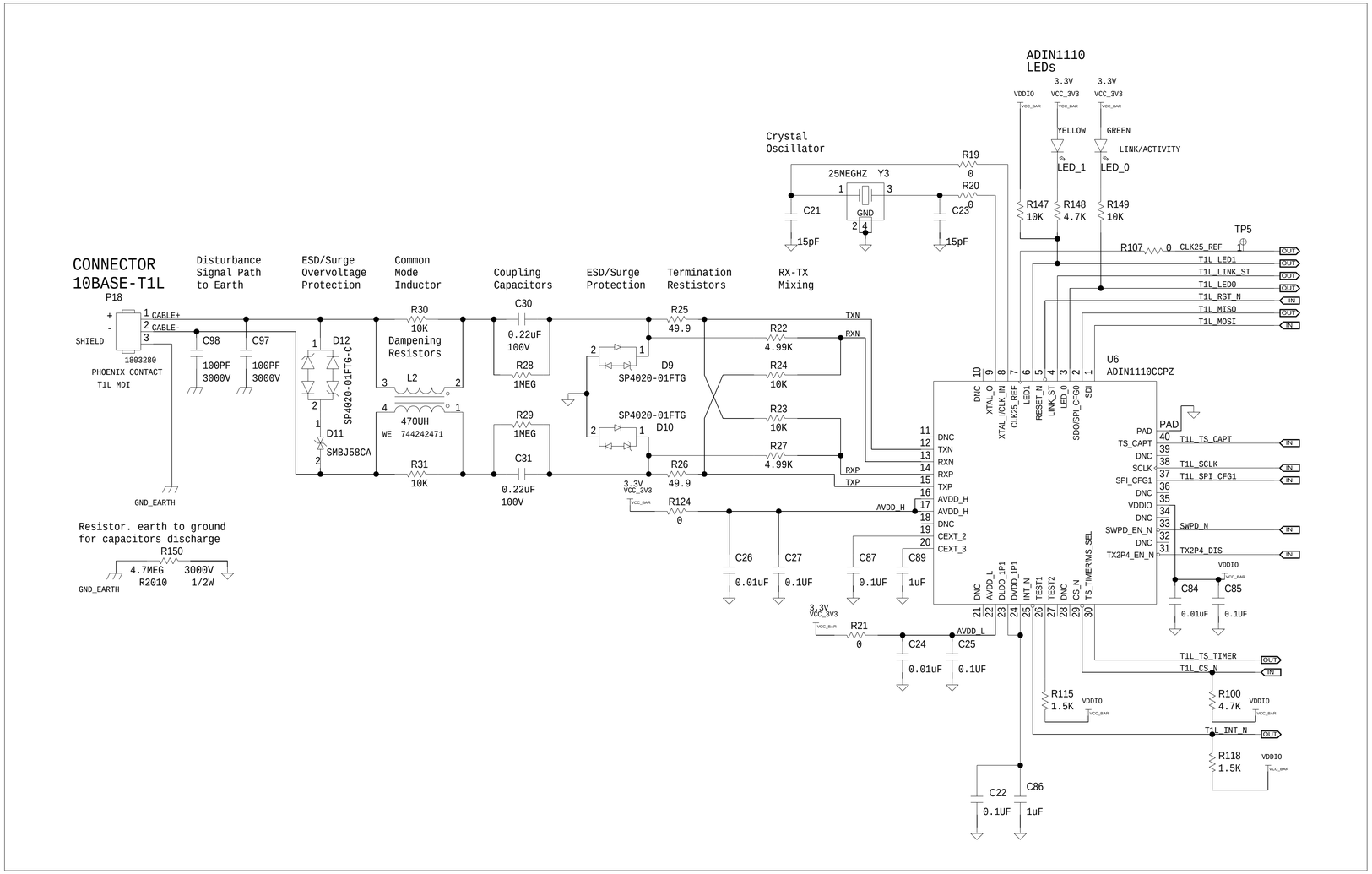
T1S MDI Connector



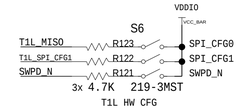
ANALOG DEVICES	SCHEMATIC	
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DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_083876	REV D
PTD ENGINEER -	SIZE D	SCALE 1:1
	SHEET 3 OF 7	

ADIN1110 CIRCUIT

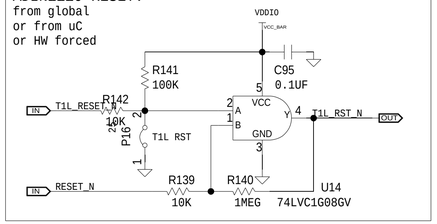
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



ADIN1110
HARDWARE CONFIGURATION



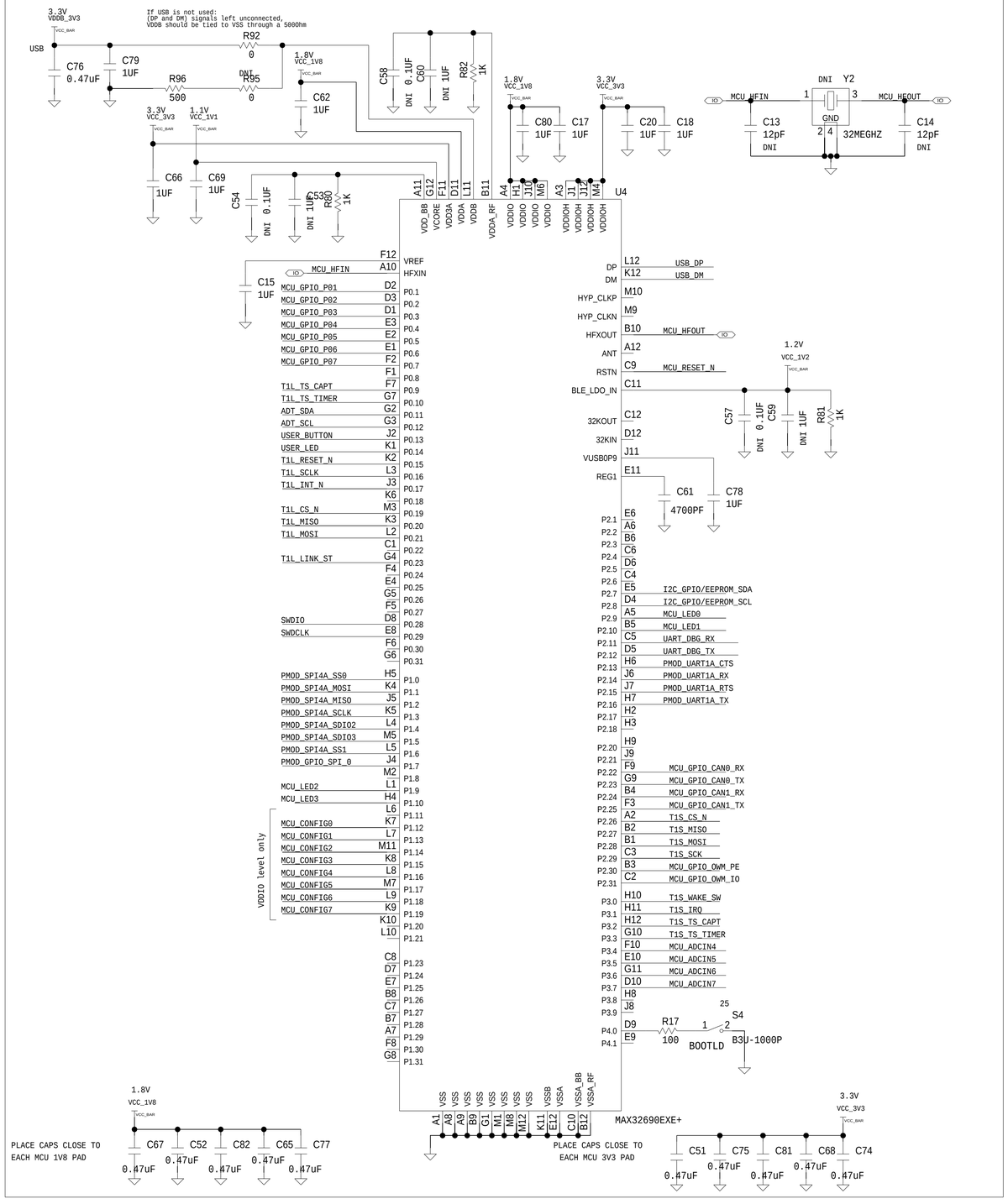
ADIN1110 RESET:
from global
or from uC
or HW forced



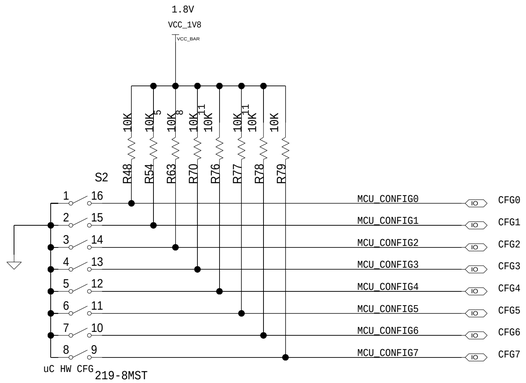
ANALOG DEVICES	SCHEMATIC		
	HW TYPE : Customer Evaluation Product(s): ADIN1140,ADIN1100,MAX32690		
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	PTD ENGINEER -	SIZE D	SCALE 1:1
		SHEET 4 OF 7	

MCU (MAX32690)

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

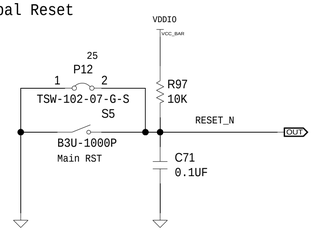


MCU MODE CONFIGURATION

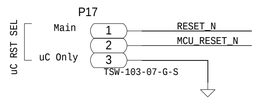


RESETS

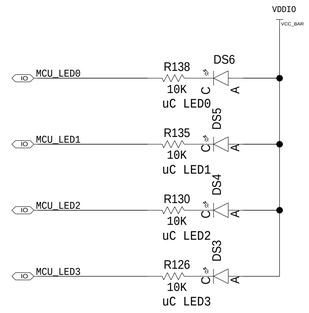
Global Reset



MCU Reset Select

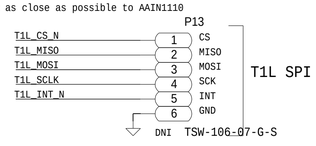


MCU LEDs

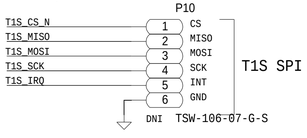


SPI DEBUG

Place as close as possible to AAIN1110



Place as close as possible to ADIN1140



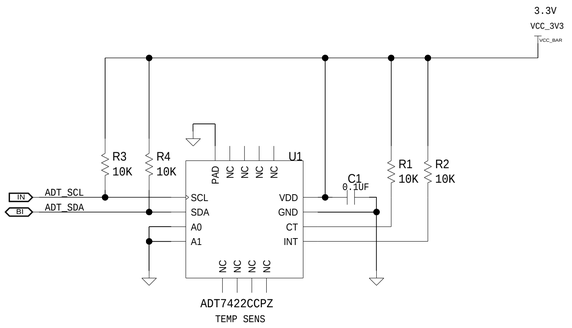
ANALOG DEVICES	SCHEMATIC		
	HW TYPE : Customer Evaluation Product(s): ADIN1140,ADIN1100,MAX32690		
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	PTD ENGINEER -	SIZE D	SCALE 1:1
		SHEET 5 OF 7	

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

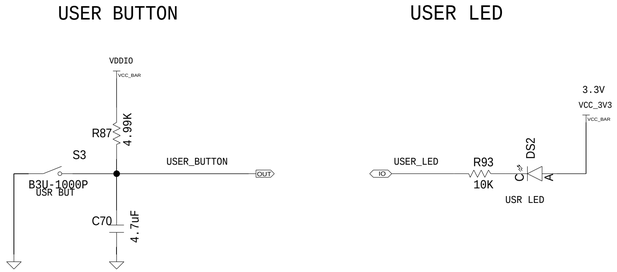
USER INTERFACE

TEMPERATURE SENSOR

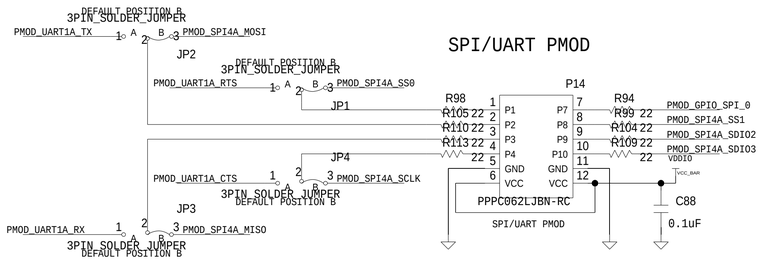
0.1 Deg.C Accuracy



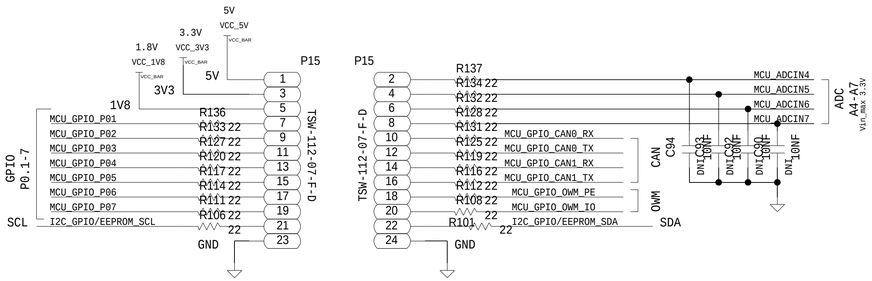
USER LED/BUTTON



PMOD



GPIOs



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SCHEMATIC

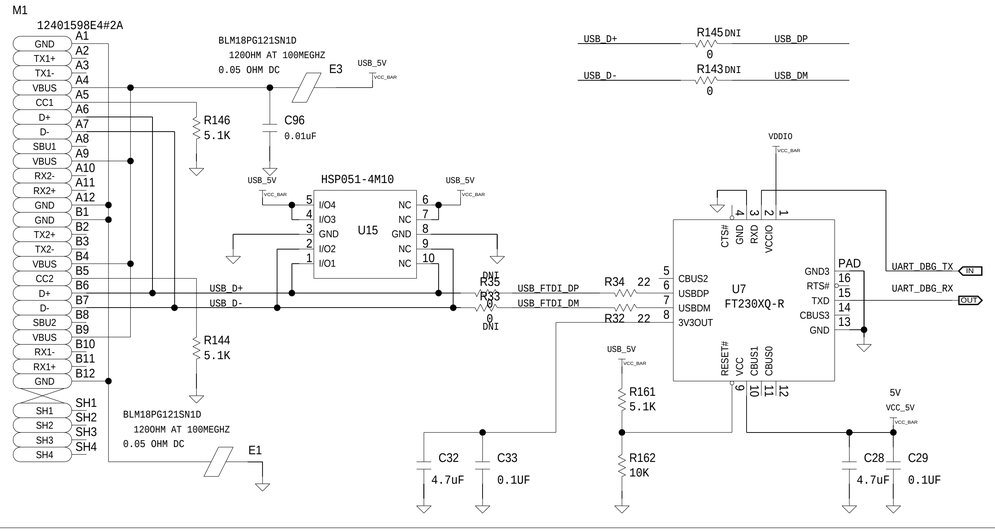
HW TYPE : Customer Evaluation
Product(s): ADIN1140,ADIN1100,MAX32690

DESIGN VIEW	DRAWING NO.	REV
<DESIGN_VIEW>	02_083876	D
PTD ENGINEER	SIZE / SCALE	SHEET 6 OF 7
-	D / 1:1	

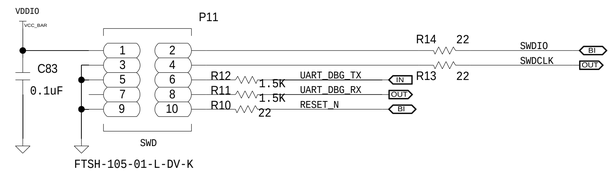
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

USB,DEBUG,MEMORY

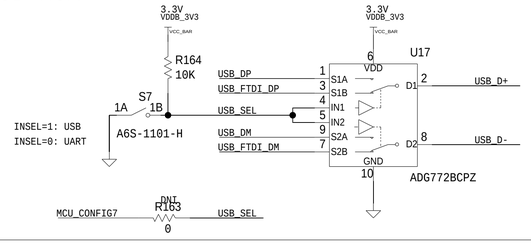
USB to UART



JTAG / SWD

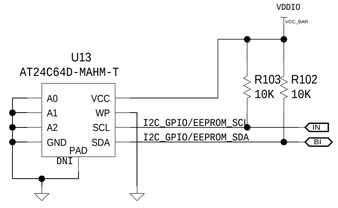


USB MUX



MEMORY (Optional)

EEPROM 64 Kb memory



Mounting holes 3.2mm
exposed contact ring

X1 X2 X3 X4

GND_EARTH GND_EARTH

PLace close to board edges

42 SILK=GND TP1 1 BLK
42 SILK=GND TP2 1 BLK
42 SILK=GND TP3 1 BLK

ANALOG DEVICES

SCHEMATIC

HW TYPE : Customer Evaluation
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DESIGN VIEW	DRAWING NO.	REV
<DESIGN_VIEW>	02_083876	D
PTD ENGINEER: -	SIZE: D / SCALE: 1:1	SHEET 7 OF 7