

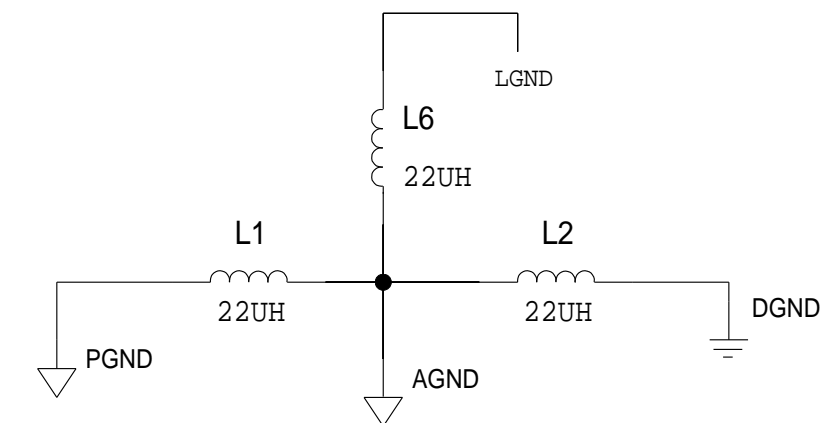
8		7		6		5		4		3		2		1															
D	THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.					JUMPER TABLE			REVISIONS																				
									REV	DESCRIPTION		DATE	APPROVED																
	A	INITIAL RELEASE		11/15/18	B.P.																								
	B	CHANGE PER ECR-085275		12FEB19	B.P.																								
	C	CHANGE PER ECR-086688		11APR19	B.P.																								
	RELAY CONTROL CHART																												
	CONTROL	CODE	DEVICE	FUNCTION	CONNECTOR																								
C																													
B																													
A																													
8		7		6		5		4		3		2		1															
P.O SPEC.		BK/BD SPEC.		SOCKET OEM		OEM PART#		HANDLER		TEMPLATE ENGINEER -		DATE		SCHEMATIC															
										HARDWARE SERVICES -				HW TYPE : Customer Evaluation Product(s): ADA4355 : ADA4355 PACKAGE : 84-lead 12X6_MM BGA-family : Pitch-pitch StyleVendor Style <User Define> <User Define> <User Define>															
										HARDWARE SYSTEMS -																			
										TEST ENGINEER -																			
										COMPONENT ENGINEER -																			
										TEST PROCESS -																			
										HARDWARE RELEASE -																			
												DESIGNER David Corbin		MASTER PROJECT TEMPLATE TBD		TESTER TEMPLATE no_template		DRAWING NO. 02_052057		REV. E									
												PTD ENGINEER Bruce Petipas		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES															
												CHECKER -		TOLERANCES		DECIMALS X.XX +0.010 X.XXX +-0.005		FRACTIONS +1/32		ANGLES +2		SIZE D		SCALE 1:1		CODE ID NO. CodeID		SHEET 1 OF 7	

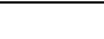
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

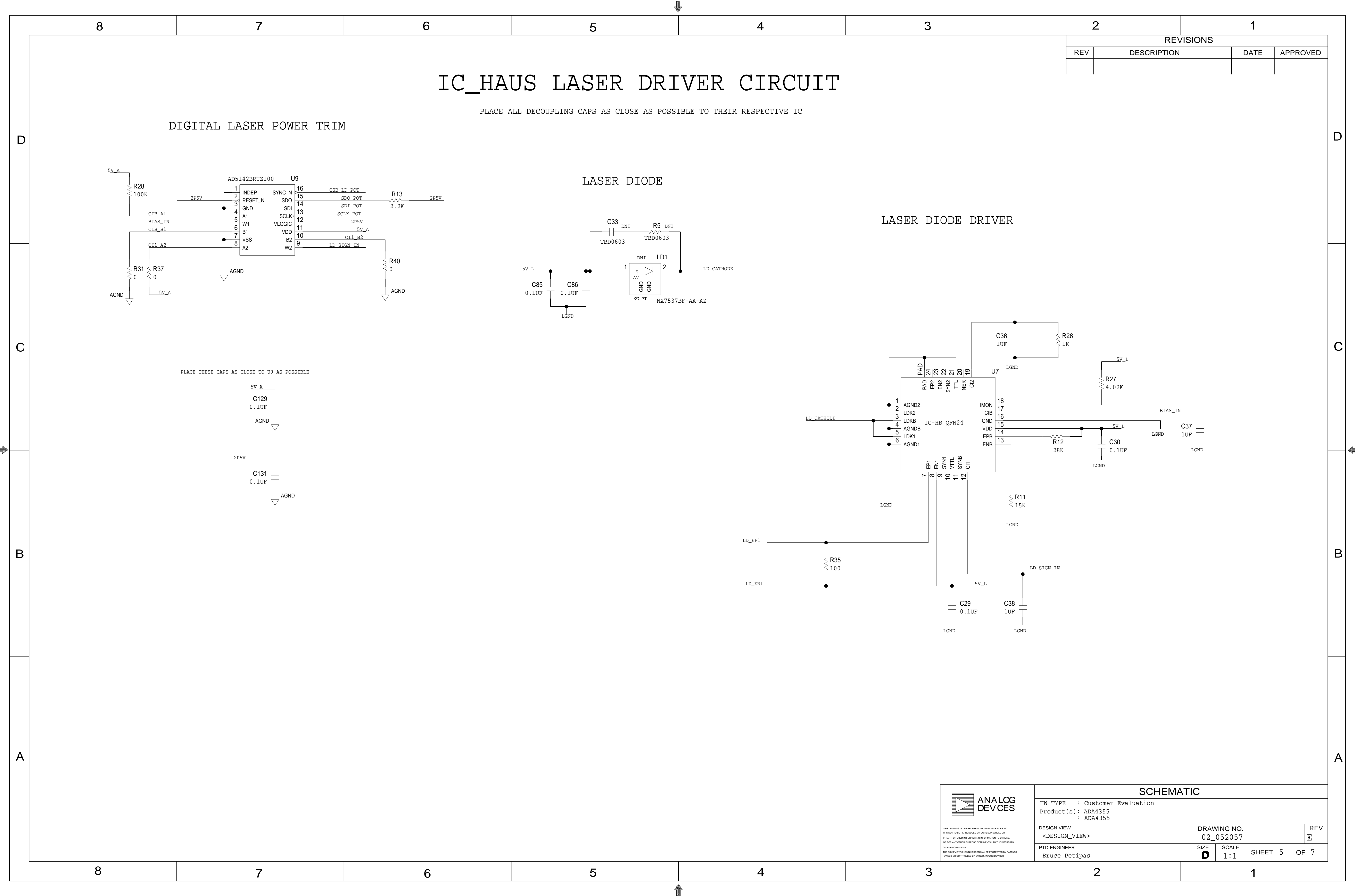
[illegible]

Schematic diagram of the VAPD pin connection for the TSW-102-07-G-S component. The VAPD pin (pin 1) is connected to a 10µF capacitor (C82) which is grounded to AGND. Pin 2 is also connected to AGND.

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

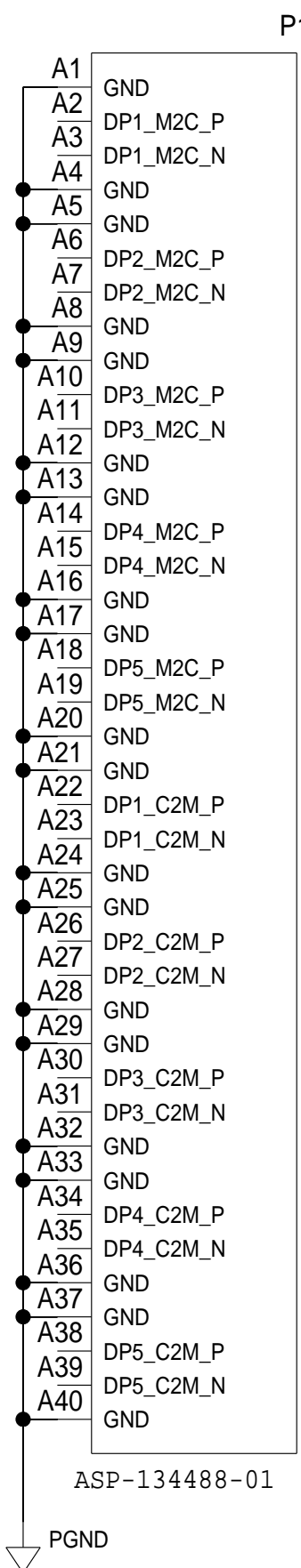
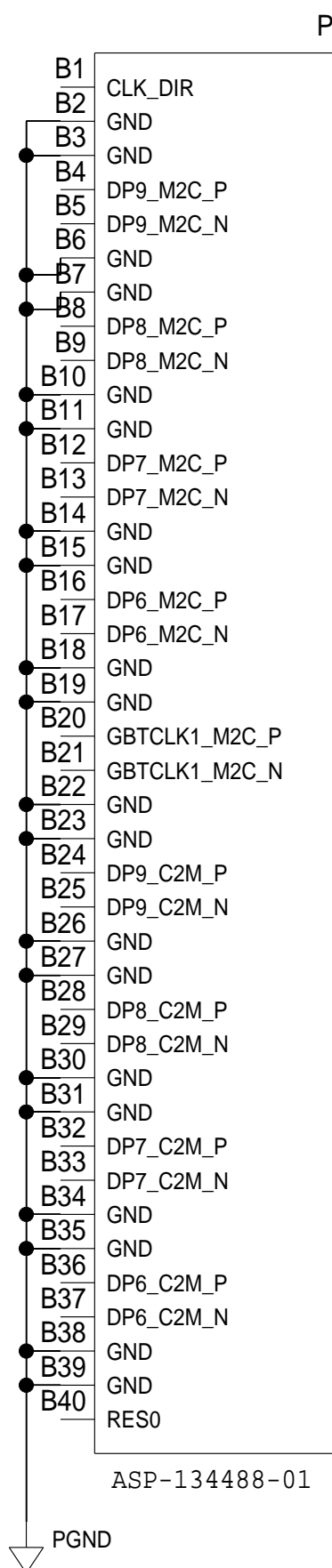
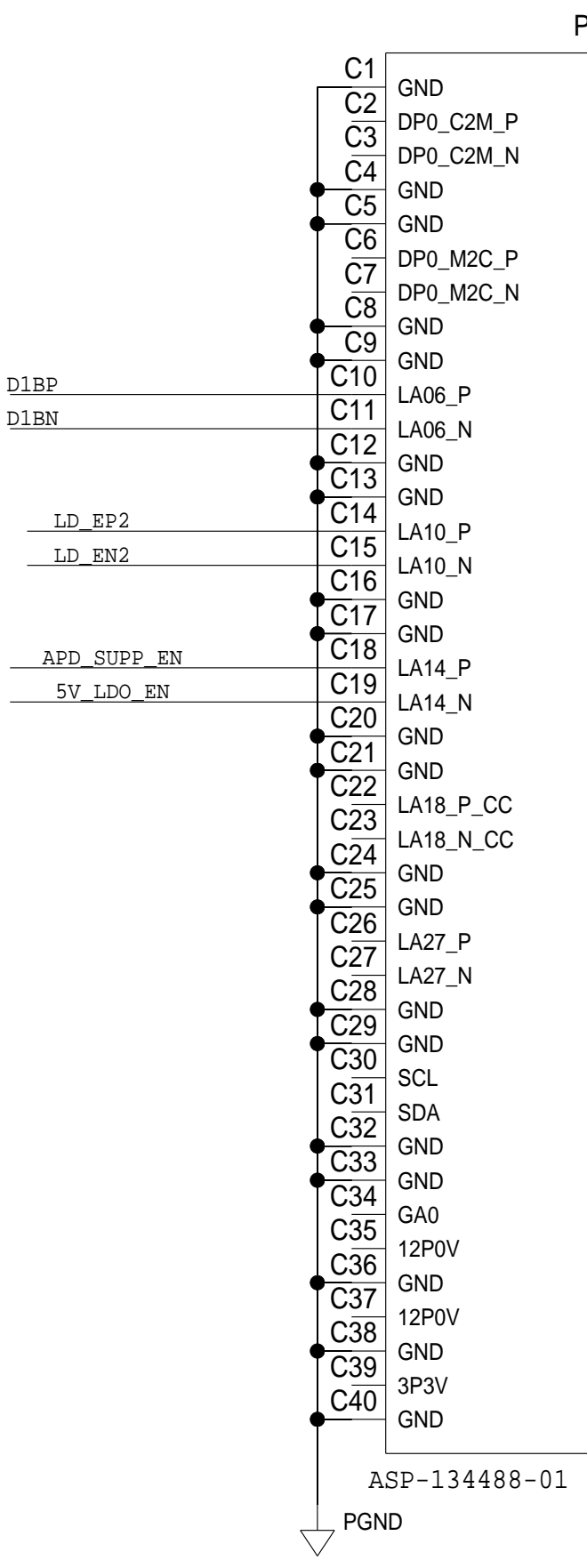
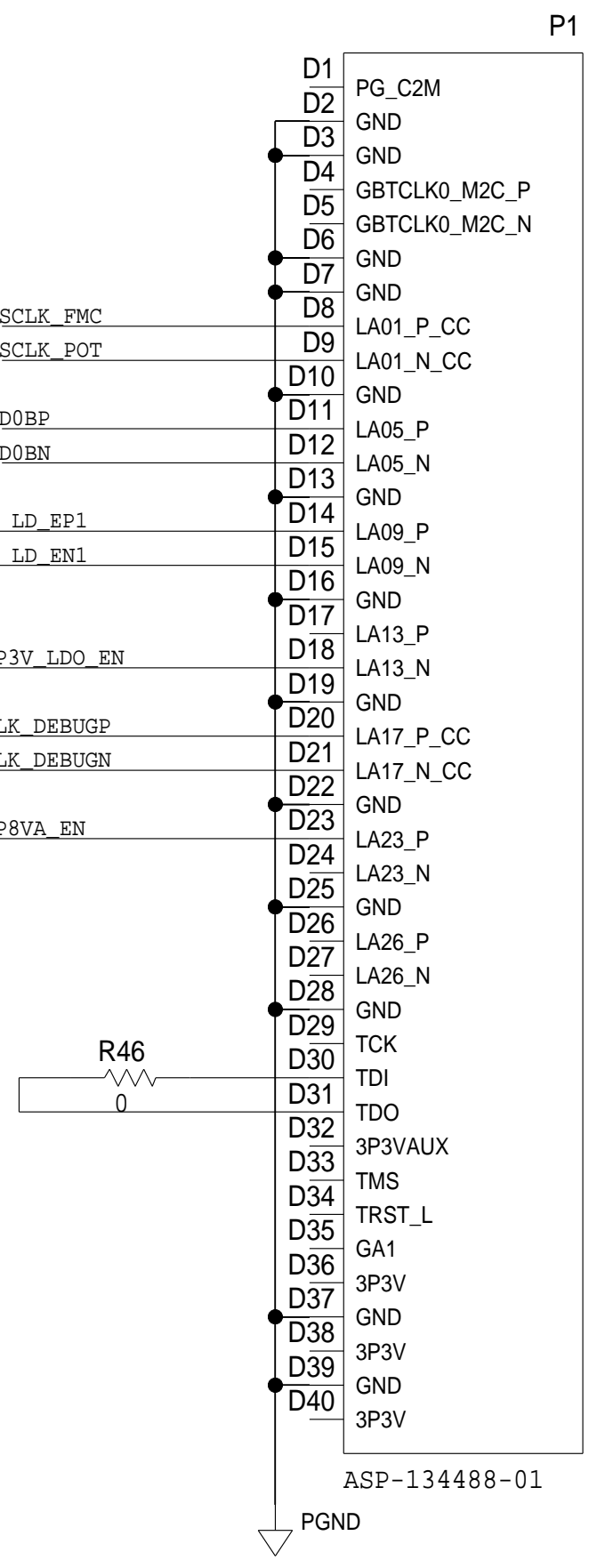
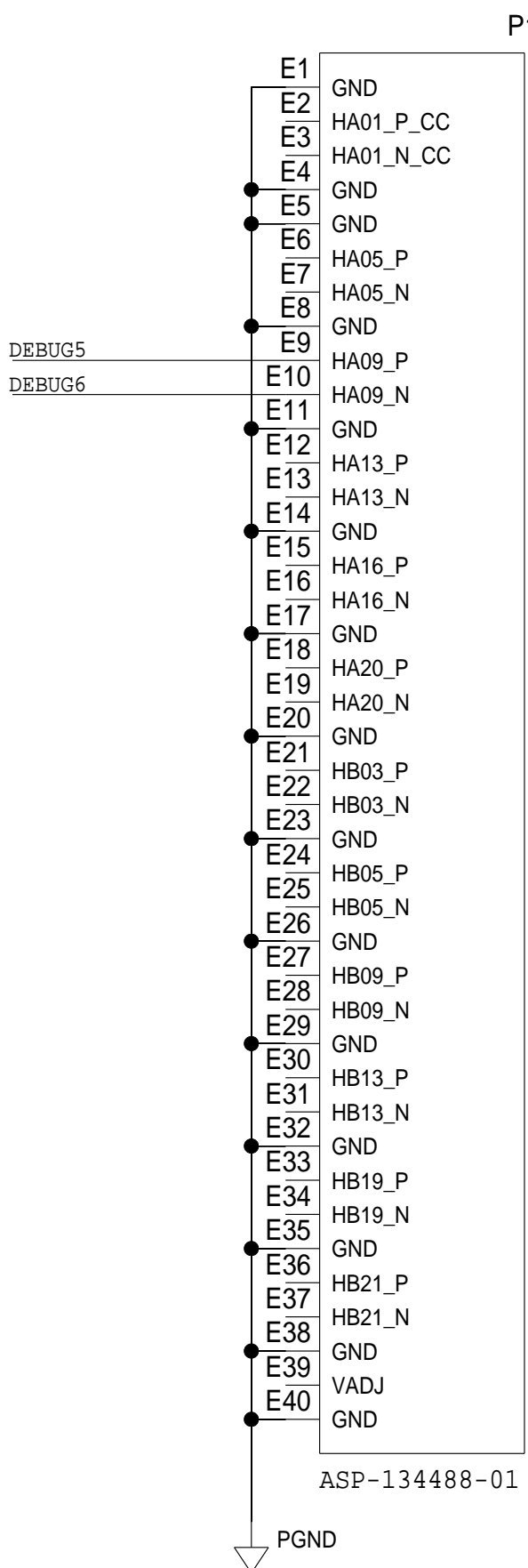
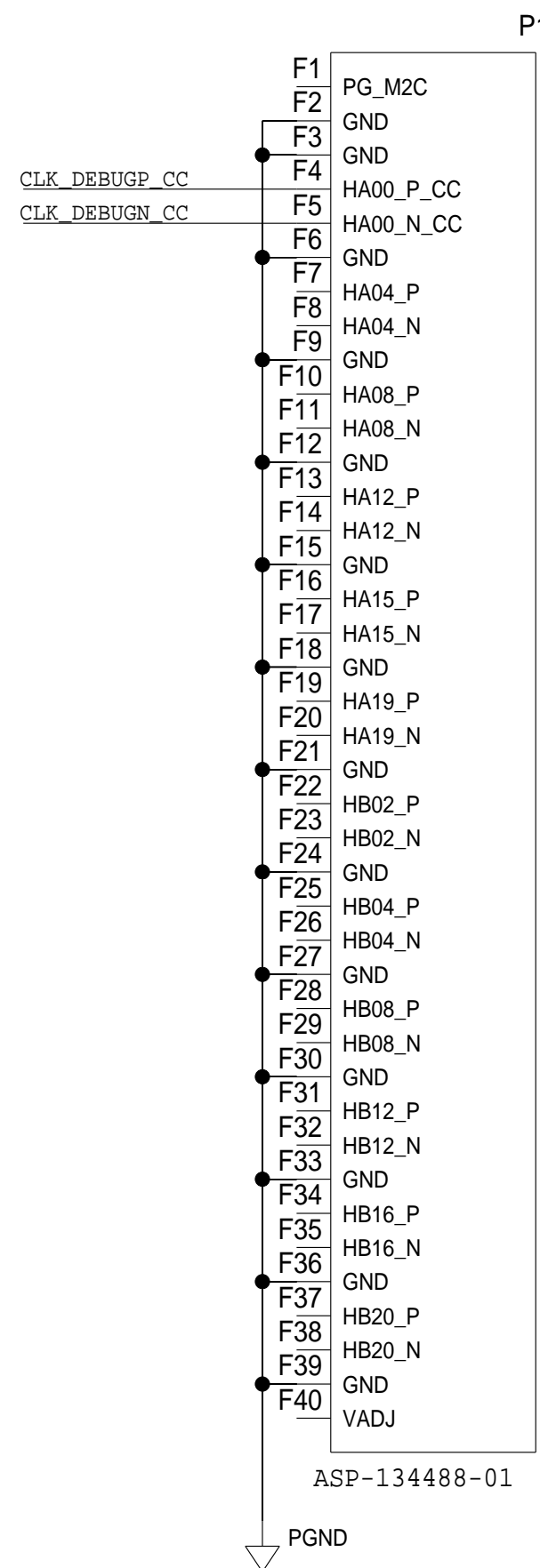
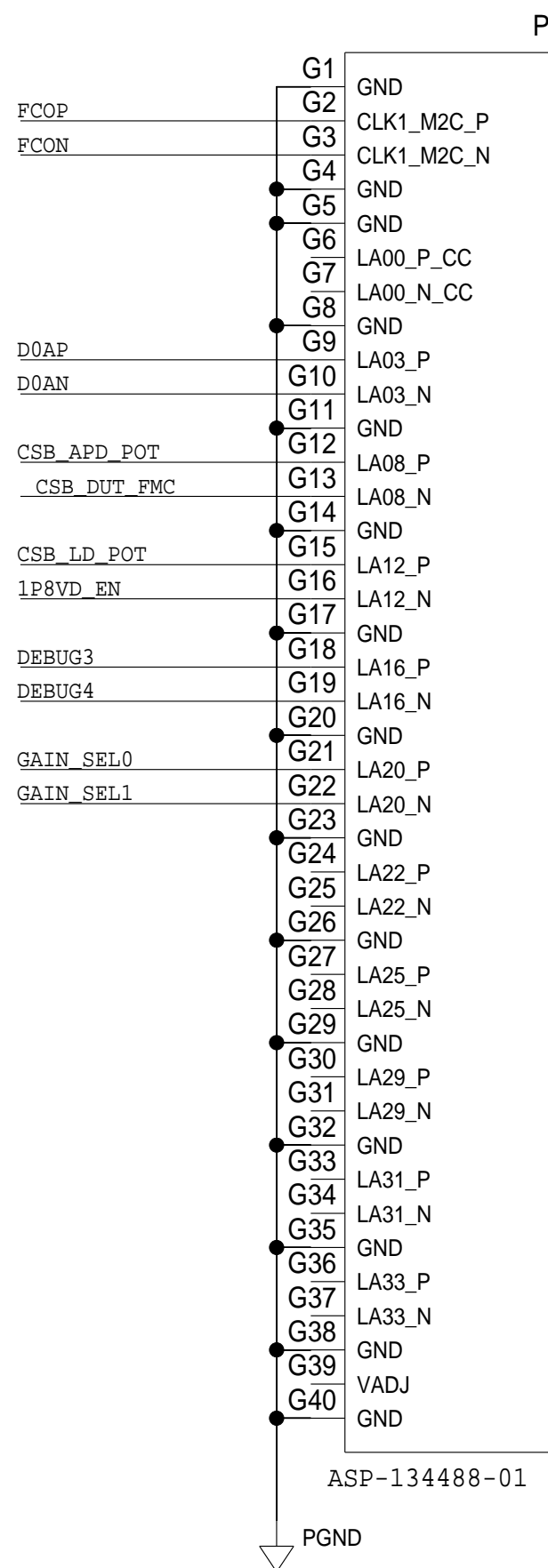
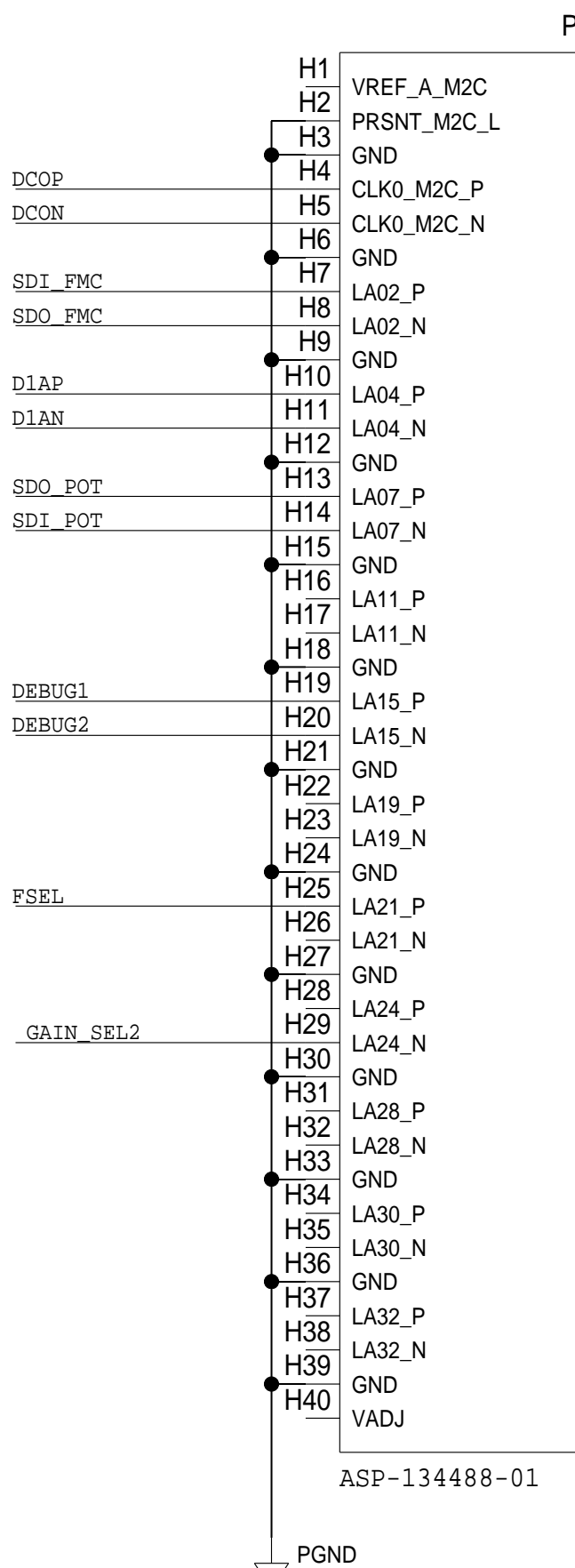
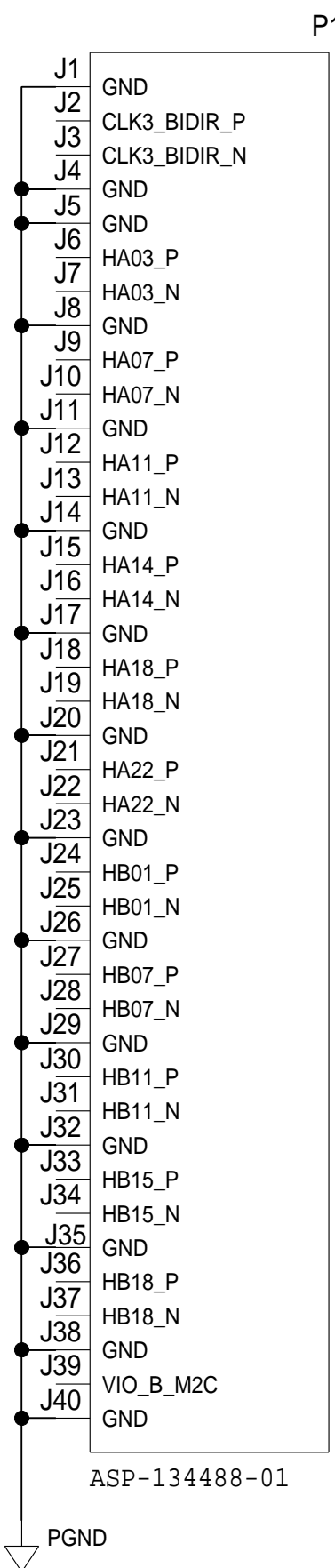
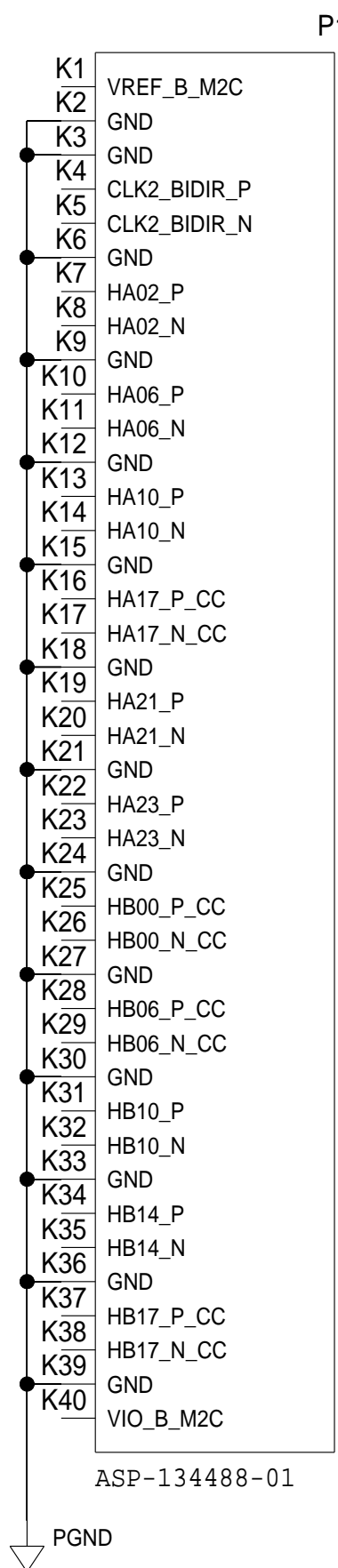


 ANALOG DEVICES	<h1 style="text-align: center;">SCHEMATIC</h1>			
	<p>HW TYPE : Customer Evaluation</p> <p>Product(s) : ADA4355</p> <p style="padding-left: 100px;">: ADA4355</p>			
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		<p>SIZE</p> <p>D</p>	<p>SCALE</p> <p>1:1</p>	<p>SHEET 4 OF 7</p>



FMC CONNECTIONS

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



SCHEMATIC			
HW TYPE : Customer Evaluation Product(s) : ADA4355 : ADA4355		DESIGN VIEW <DESIGN_VIEW>	REV E
PTD ENGINEER Bruce Petipas		DRAWING NO. 02_052057	SIZE D
SCALE 1:1		SHEET 7	OF 7