

87654321

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REVISIONS

REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	07SEP23	M. CEE
A	CHANGES PER ECR-124234	10JAN25	M. CEE

EVAL-AD9954-ARDZ SCHEMATIC REV (A)

PAGE 2 - POWER CIRCUIT & ARDUINO HEADER

PAGE 3 - AD9954 CIRCUIT

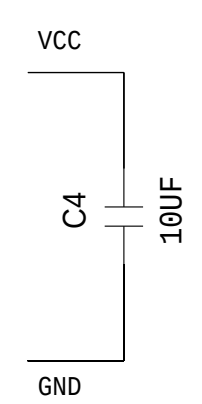
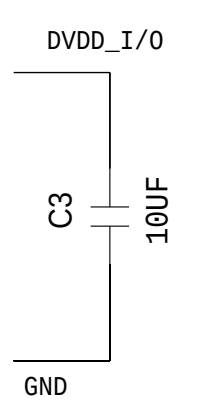
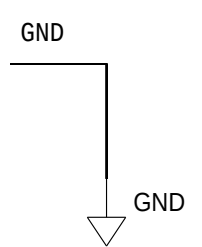
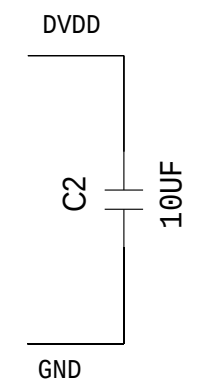
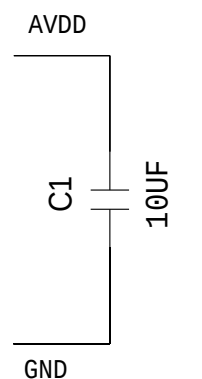
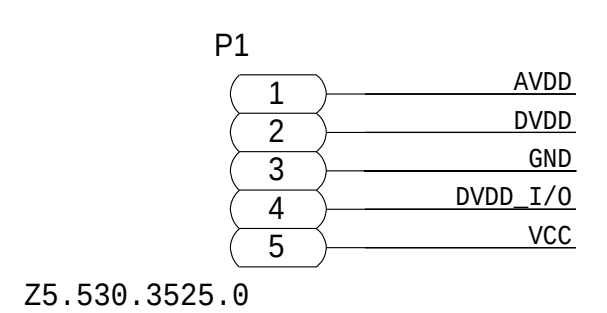
P.O SPEC.	BK/BD SPEC.	SOCKET OEM	OEM PART#	HANDLER

TEMPLATE ENGINEER	DATE	SCHEMATIC				
HARDWARE SERVICES	07SEP23					
M. VALE						
HARDWARE SYSTEMS						
TEST ENGINEER		HW TYPE : Customer Evaluation Z Product(s): AD9954 : AD9953, AD9952, AD9951, AD9859 PACKAGE : LeadCount-lead 7X7_MM TQFP_EP-family : Pitch-pitch StyleVendor Style <User Define> <User Define> <User Define>				
COMPONENT ENGINEER	07SEP23					
N. CATALAN						
TEST PROCESS						
HARDWARE RELEASE	07SEP23	MASTER PROJECT TEMPLATE		TESTER TEMPLATE	DRAWING NO.	REV.
K. JABATAN		TBD	064489_c	02_076302		
DESIGNER	07SEP23	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES				
E. DELOS REYES		TOLERANCES				
PTD ENGINEER		DECIMALS	FRACTIONS	ANGLES		
M. CEE		X.XX +-0.010	+1/32	+2		
CHECKER		X.XXX +-0.005				
-						

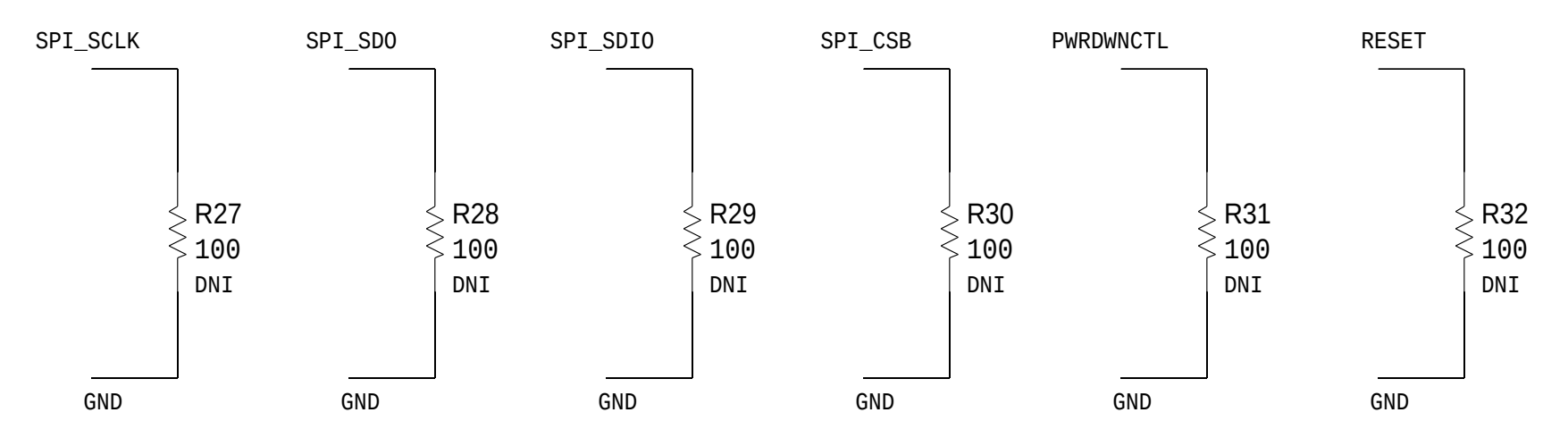
SIZE 1:1
SCALE 1:1
CODE ID NO. CodeID
SHEET 1 OF 3

87654321

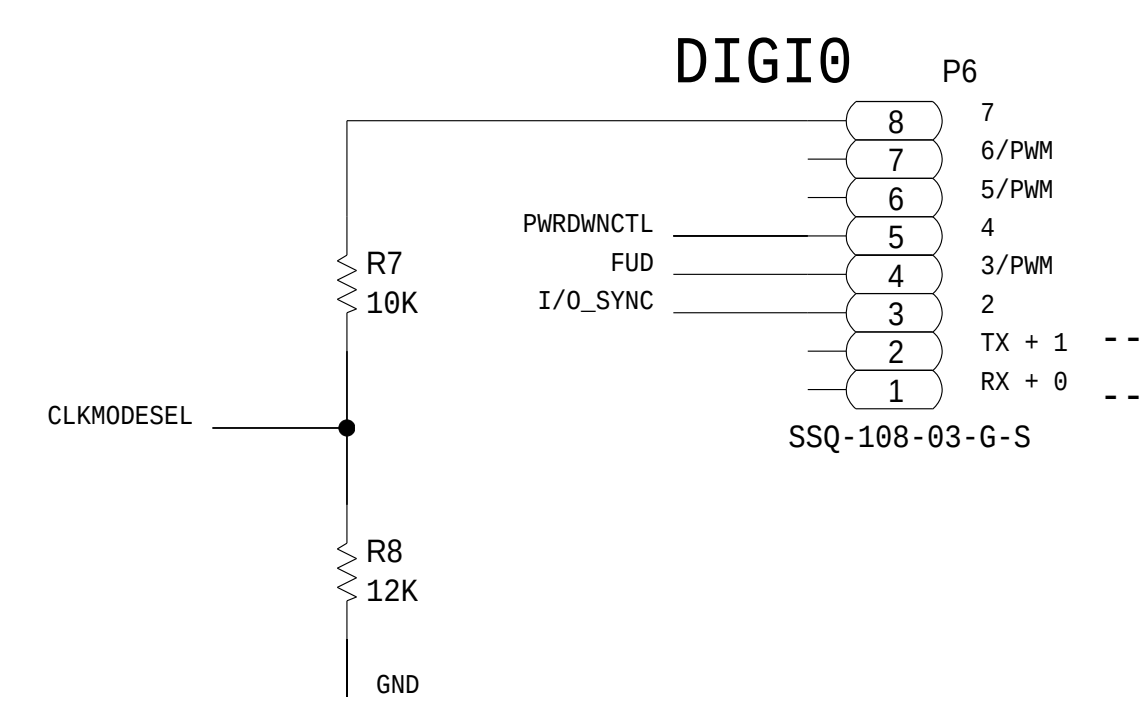
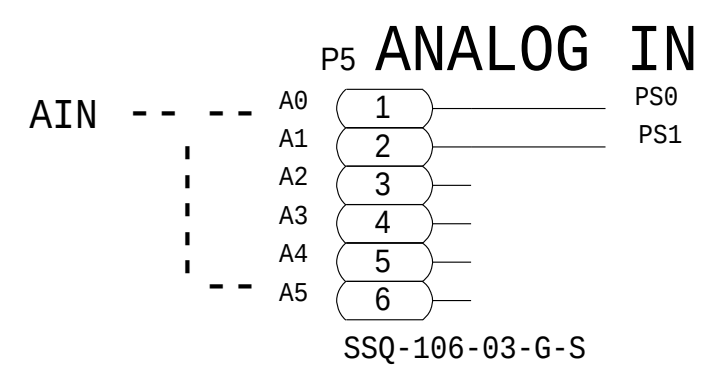
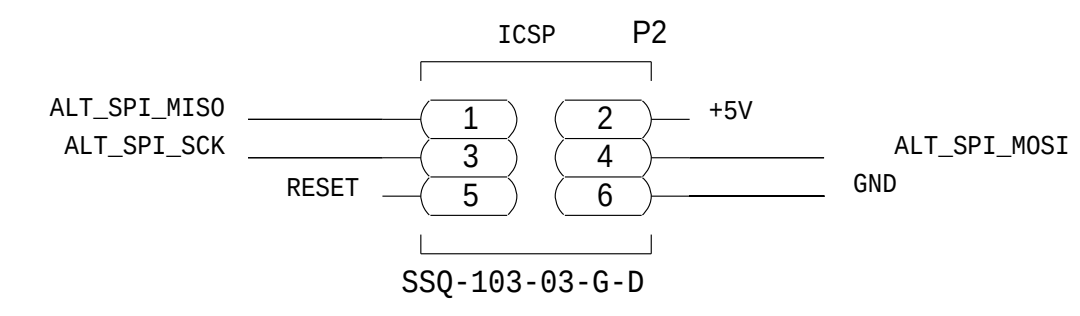
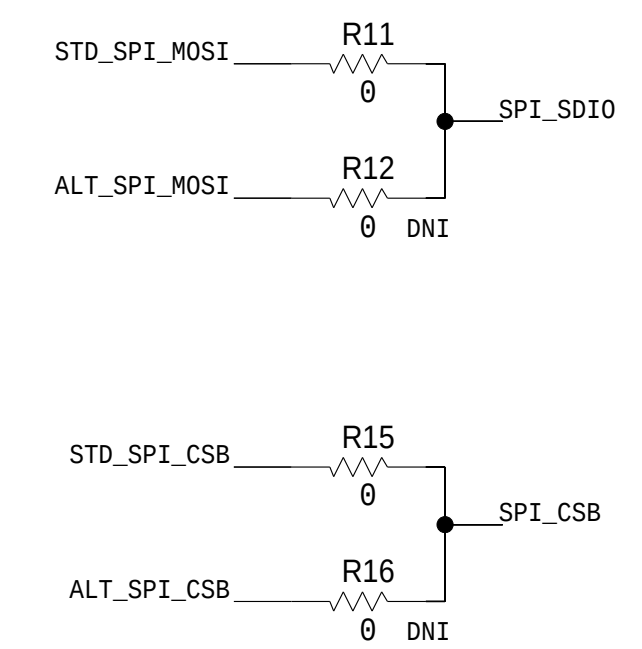
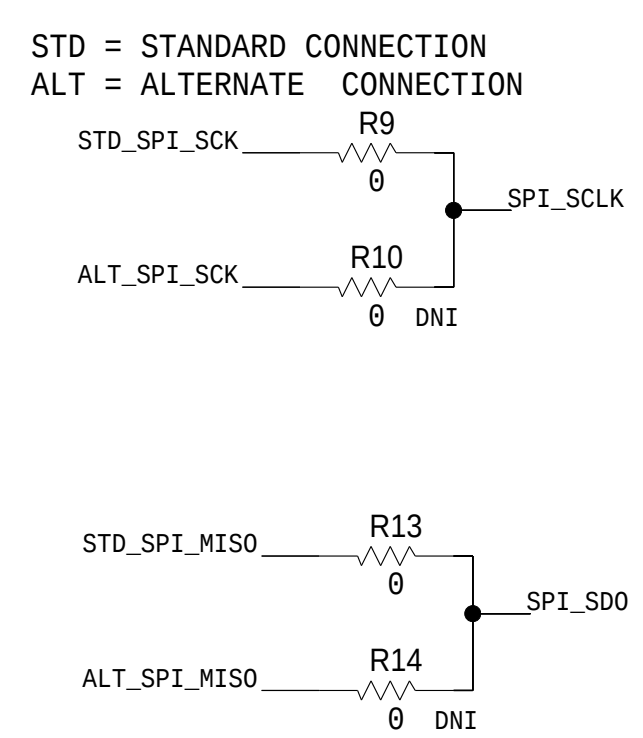
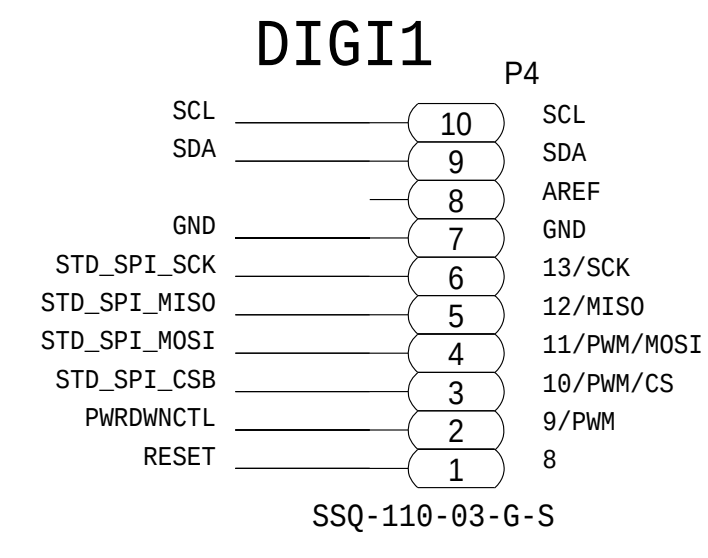
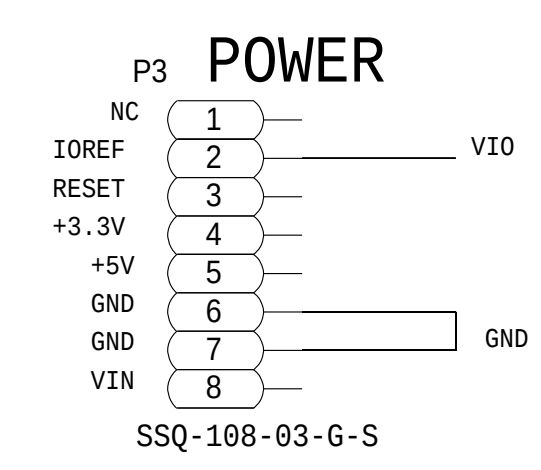
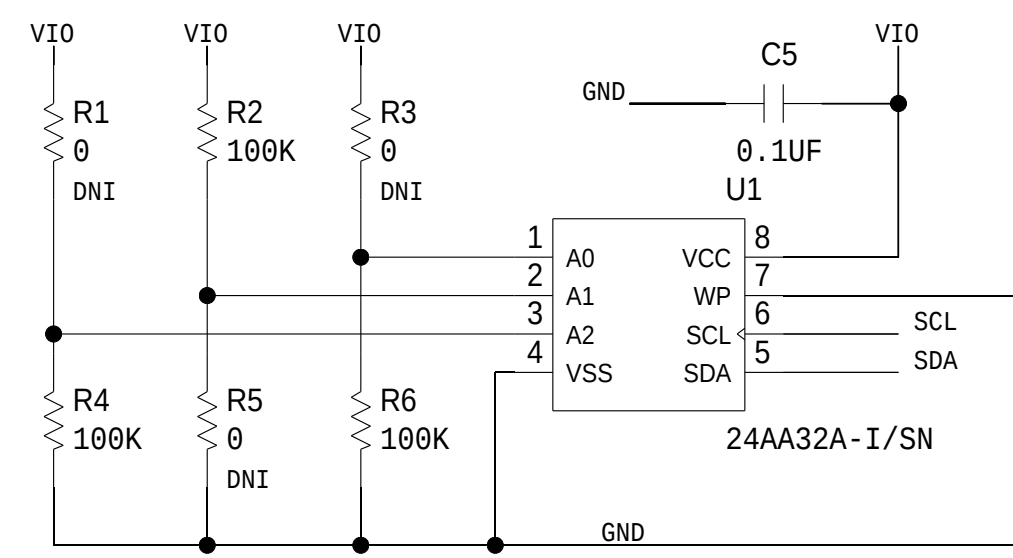
POWER CIRCUIT



PULLDOWN RESISTORS



ARDUINO UNO SHIELD TEMPLATE (REV B)



NOTE: USING THE UART SIGNALS MAY INTERFERE WITH THE SERIAL MONITOR ON OFFICAL ARDUINO BOARDS SUCH AS THE UNO, MEGA, DUE, ETC.

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	HW TYPE : Customer Evaluation Z Product(s): AD9954 : AD9953, AD9952, AD9951, AD9859		
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_076302	REV B
	PTD ENGINEER M. CEE	SIZE D	SCALE 1:1

SHEET 2	OF 3
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