

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	09FEB22	I. ANDAL
B	CHANGES AS PER ECR-114918	12JUL23	D.DALISAY

ASSEMBLY NOTES:

1. BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).
2. REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.
3. REPAIRS ARE NOT ALLOWED IN SOLDERMASK FREE AREAS ON EITHER SIDE OF THE BOARD.
4. RoHS COMPLIANCE: ASSEMBLY VENDOR SHOULD ASSURE COMPLIANCE WITH LEAD-FREE AND RoHS PCB ASSEMBLY STANDARDS (EU RoHS DIRECTIVE 2002/95/EC).
5. BOARDS TO BE SHIPPED SINGULATED AFTER ASSEMBLY PROCESS.
6. SMOOTHEN EDGES AND FREE FROM BURRS AFTER DEPANELIZATION PROCESS.
7. INSTALL JUMPER SHUNTS (M029337) ON THE FOLLOWING LOCATIONS:

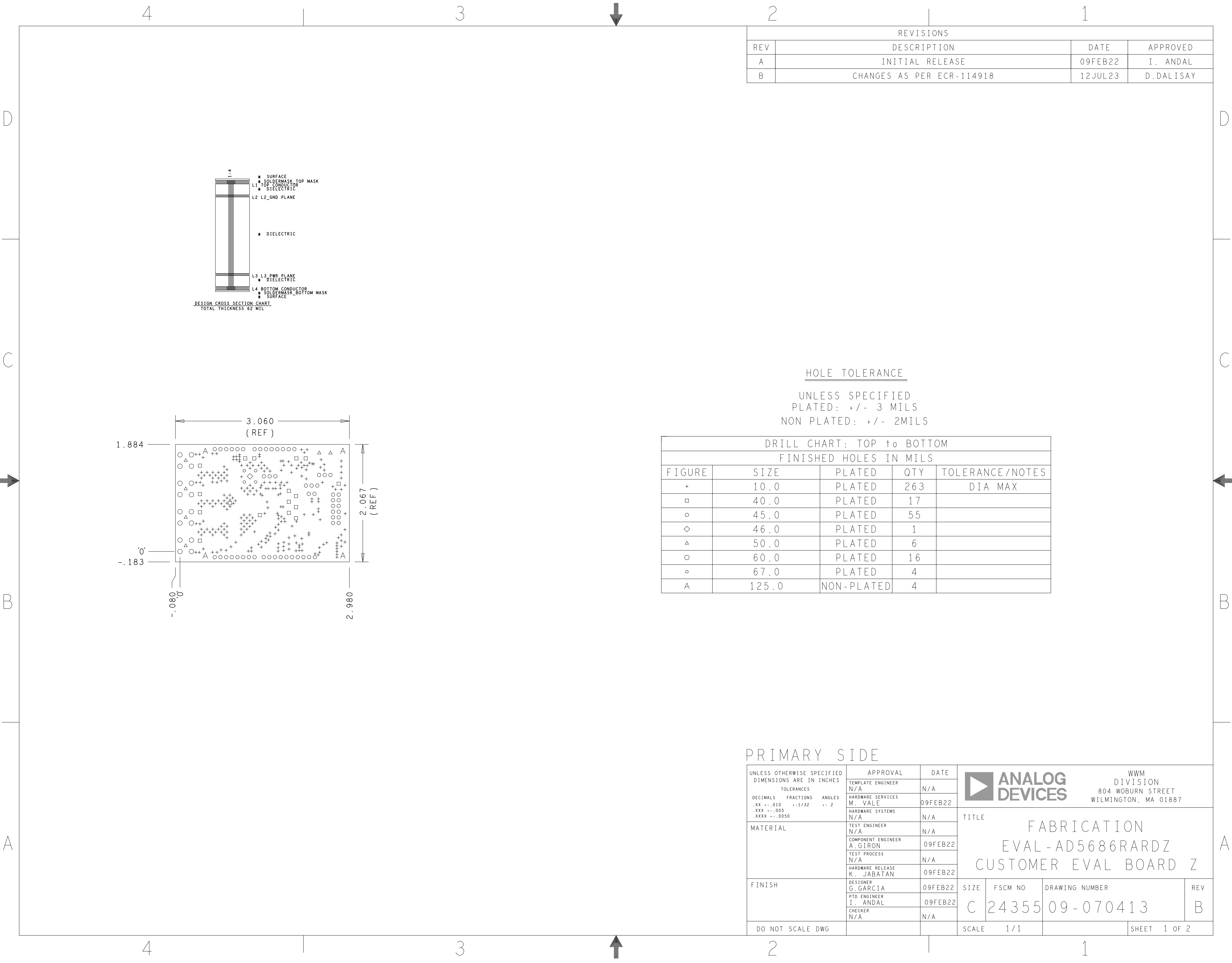
A. VDD_SEL (PIN 2-3) B. REF_SEL (PIN 1-2) C. VDD_VIO (PIN1) D. RSTSEL (PIN 1-2)
8. DO NOT TRIM EXCESS PINS OF THE FOLLOWING LOCATIONS:

A. P4
B. P3
C. P6
D. P7
9. SHIPMENT WILL INCLUDE STANDOFFS(M025474) AND SCREWS(M025339).
10. APPLY STICKER LABEL TO BOX ACCORDING TO BOM VARIANT DECLARATION:

* EVAL-AD5686RARDZ FOR 05-070413-01
* EVAL-AD5696RARDZ FOR 05-070413-02

PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES .XX -.010 --1/32 -- 2 .XXX -.005 .XXXX -.0050	APPROVAL	DATE	<div><div><div></div><div>ANALOG DEVICES</div></div><div>WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887</div></div>			
	TEMPLATE ENGINEER N/A	N/A				
	HARDWARE SERVICES M. VALE	09FEB22	TITLE ASSEMBLY EVAL-AD5686RARDZ CUSTOMER EVAL BOARD Z			
	HARDWARE SYSTEMS N/A	N/A				
MATERIAL	TEST ENGINEER N/A	N/A				
	COMPONENT ENGINEER A.GIRON	09FEB22				
	TEST PROCESS N/A	N/A				
	HARDWARE RELEASE K. JABATAN	09FEB22				
FINISH	DESIGNER G.GARCIA	09FEB22	SIZE	FSCM NO	DRAWING NUMBER	REV
	PTD ENGINEER I. ANDAL	09FEB22	C	24355	01-070413	B
	CHECKER N/A	N/A				
	DO NOT SCALE DWG		SCALE	1 / 1	SHEET 1 OF 1	



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HOLE TOLERANCE
UNLESS SPECIFIED
PLATED: +/- 3 MILS
NON PLATED: +/- 2MILS

DRILL CHART: TOP to BOTTOM				
FINISHED HOLES IN MILS				
FIGURE	SIZE	PLATED	QTY	TOLERANCE/NOTES
+	10.0	PLATED	263	DIA MAX
□	40.0	PLATED	17	
○	45.0	PLATED	55	
◇	46.0	PLATED	1	
△	50.0	PLATED	6	
○	60.0	PLATED	16	
◦	67.0	PLATED	4	
A	125.0	NON-PLATED	4	

PRIMARY REVISION			APPROVAL		DATE		<div><div></div><div><div>ANALOG DEVICES</div></div></div> <div>WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887</div>							
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES .XX .010 .1/32 .2 .XXX .005 .XXXX .0050			TEMPLATE ENGINEER N/A		N/A		TITLE FABRICATION EVAL-AD5686RARDZ CUSTOMER EVAL BOARD Z							
MATERIAL			HARDWARE SERVICES M. VALE		09FEB22									
			HARDWARE SYSTEMS N/A		N/A									
			TEST ENGINEER N/A		N/A									
			COMPONENT ENGINEER A. GIRON		09FEB22									
FINISH			TEST PROCESS N/A		N/A		SIZE C							
			HARDWARE RELEASE K. JABATAN		09FEB22						FSCM NO 24355			
			DESIGNER G. GARCIA		09FEB22									
PTD ENGINEER I. ANDAL		09FEB22		REV B										
CHECKER N/A		N/A						SCALE 1/1						
DO NOT SCALE DWG														SHEET 1 OF 2

4

3

2

1

D

C

B

A

SPECIFICATIONS:

ROHS COMPLIANCE NOTE: HOMOGENOUS MATERIALS IN THIS BOARD SHALL BE COMPLIANT THE EU RoHS DIRECTIVE 2002/95/EC

MATERIALS; ALL LAMINATES AND BONDING MATERIALS SHOULD BE SELECTED FROM IPC-4101 OR IPC-4103, MINIMUM Tg>170degC, Td>300degC, U.L. RATING OF 94 V-0

MATERIAL FAMILY; ISOLA370HR OR S1000-2 OR IT180 OR EQUIVALENT.

CLADDING; EXTERNAL LAYERS .5 OZ. COPPER, OVERPLATE TO 1.5 OZ. INTERNAL PLANE LAYERS 1 OZ. COPPER.
NOTE: IF THE LAYER STACKUP CONFLICTS WITH THE ABOVE CLADDING SPECIFICATIONS THEN THE LAYER STACKUP SHALL TAKE PRECEDENCE.

SOLDER MASK; SHALL BE LIQUID PHOTOIMAGEABLE (LPI) APPLIED ON BOTH SIDES OVER BARE COPPER OR GOLD AND SHALL MEET IPC-SM-840 (LATEST REV.) CLASS 3. COLOR GREEN.

SILK SCREEN; SHALL BE PERMANENT NON-CONDUCTIVE EPOXY INK, COLOR: WHITE SYNTHETIC INKJET PRINTING ALLOWED FOR DENSE BOARDS, COLOR: WHITE

SURFACE FINISH; ENIG (ELECTROLESS NICKEL/IMMERSION GOLD) PER IPC-4552 LATEST REVISION

INTENTIONAL SHORTS; IF SUPPLIED DATA INCLUDES A FILE "READ_ME.2", THEN INTENTIONAL NET SHORTS EXIST. CUSTOMER REVIEW AND APPROVAL IS REQUIRED IF SUPPLIED DATA REPORTS ANY CONDITION THAT DOES NOT MATCH "READ_ME.2" FILE PROVIDED.

TEST REQUIREMENTS; 100% NETLIST ELECTRICAL VERIFICATION USING CUSTOMER SUPPLIED IPC-D-356 NETLIST FOR OPENS AND SHORTS WHEN "GERBER DATA" IS PROVIDED. THIS VERIFICATION ALSO REQUIRED FOR "ODB+." DATA PER EMBEDDED NETLIST.

4 LAYER STACKUP

NOMINAL FINISHED BOARD THICKNESS 0.062" +/- 10%

PRIMARY SILKSCREEN

PRIMARY SOLDER MASK

PRIMARY SIDE (LAYER 1)

L2_GND PLANE (LAYER 2)

L3_PWR PLANE (LAYER 3)

SECONDARY SIDE (LAYER 4)

SECONDARY SOLDER MASK

SECONDARY SILKSCREEN

REQUIREMENTS:

1. REFER TO IPC-6010 SERIES (LATEST REV.), CLASS 2 FOR FABRICATION UNLESS OTHERWISE SPECIFIED.

2. ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00115, (LATEST REVISION.)

3. MODIFICATIONS TO THE ARTWORK ARE NOT ALLOWED WITHOUT WRITTEN AUTHORIZATION.

4. HOLE PATTERN TOLERANCES FOR UNDIMENSIONED HOLES SHALL BE A DIAMETER OF 0.005 INCHES FROM THEIR TRUE POSITION.

5. PLATED HOLE WALL THICKNESS SHALL NOT BE LESS THAN 0.001 INCH MINIMUM AVERAGE, WITH NO READING LESS THAN .0008 INCH BY CROSS SECTION.

6. HOLE DIAMETERS APPLY AFTER PLATING.

7. FINISHED CONDUCTOR WIDTHS SHALL NOT BE REDUCED FROM THE NOMINAL INDICATED ON THE MASTER PATTERN, BY MORE THAN THE CONDUCTOR THICKNESS.

8. MINIMUM DESIGN LINE WIDTH IS .010 INCH.

9. MINIMUM DESIGN SPACING IS .006 INCH.

10. ~~NON-FUNCTIONAL PAD REMOVAL FROM INNER SIGNAL LAYERS MAY BE PERFORMED AFTER CUSTOMER APPROVAL.~~

11. IF PAD SIZES PROVIDED ARE NOT LARGE ENOUGH TO MAINTAIN ANNULAR RING REQUIREMENT, MFGR. MAY REQUEST APPROVAL TO TEAR DROP PADS TO MAINTAIN ANNULAR RING. (AT PAD TO TRACE INTERSECTION ONLY AND ELECTRICAL INTEGRITY MUST BE MAINTAINED.)

12. THIEVING MAY BE ADDED TO COMPENSATE FOR LOW COPPER DENSITY AREAS ON THIS DESIGN ONLY AFTER REVIEW AND APPROVAL FROM THE CUSTOMER:

A. THIEVING TO CARD EDGE, FIDUCIALS, NON-PLATED THROUGH HOLES, ALL OTHER FEATURES TO BE 0.200 INCH MINIMUM.

B. THERE SHALL BE NO THIEVING IN ANY AREAS FREE OF SOLDER MASK OR INTERNAL COPPER PLANES.

13. MFGR. TO LEGIBLY ETCH OR STAMP/SCREEN WITH PERMANENT NON-CONDUCTIVE INK ON SECONDARY SIDE IN A CLEAR AREA UNLESS OTHERWISE INDICATED;

A. U.L. CODE-FLAMMABILITY RATING

B. DATE CODE (STAMP).

C. LOT NUMBER

D. MFGR LOGO

E. SUCCESSFUL ELECTRICAL TEST.

14. REPAIRS PER IPC-7711/21 (LATEST REV.) ARE ALLOWED.

15. BOARDS TO BE SHIPPED IN ARRAY AND KEEP INTACT.

16. PANEL TO BE SUBJECTED TO CUSTOMER'S APPROVAL.

17. PANELED SOLDER PASTE STENCIL SHOULD BE SENT TO THE CUSTOMER.

PRIMARY SIDE

ANALOG DEVICES

WWM DIVISION

804 WOBURN STREET

WILMINGTON, MA 01887

SIZE

C

FSCM NO

24355

DRAWING NUMBER

09-070413

REV

B

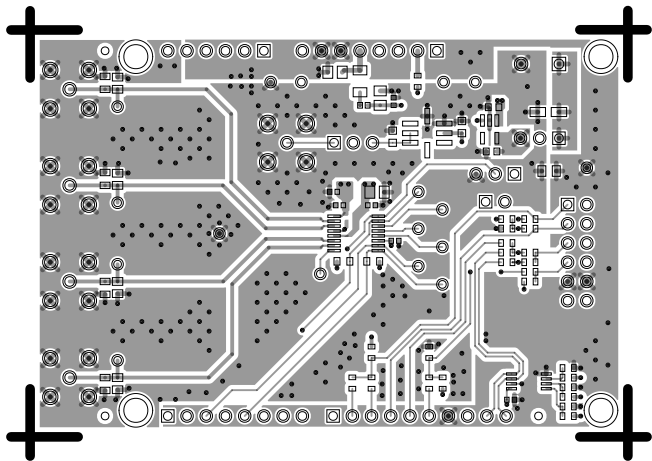
SCALE

1/1

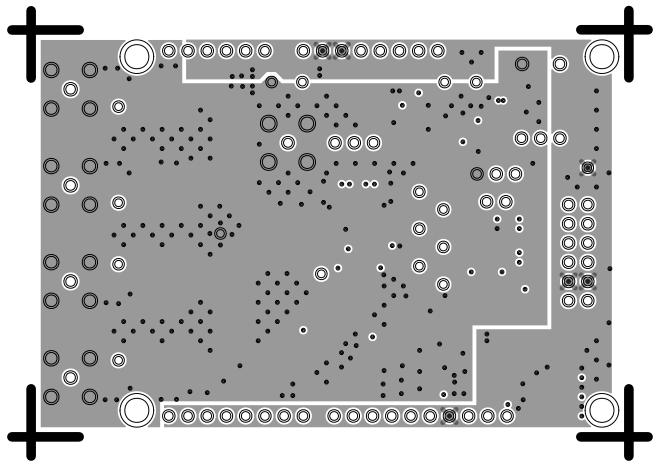
SHEET

2 OF 2

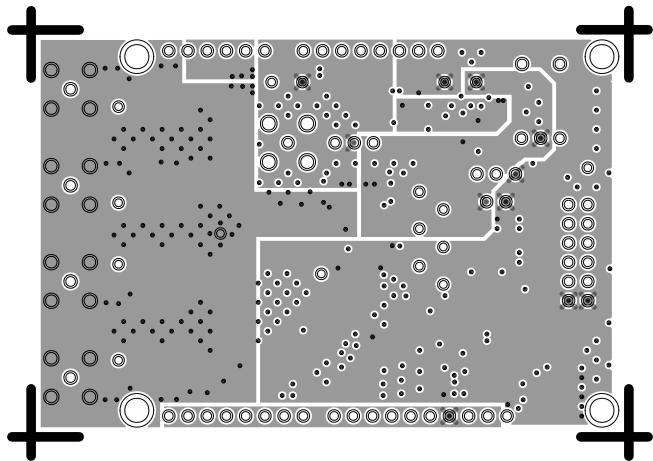
L1 PRIMARY
08-070413-01
REV B



L2 GND
08-070413-06
REV B



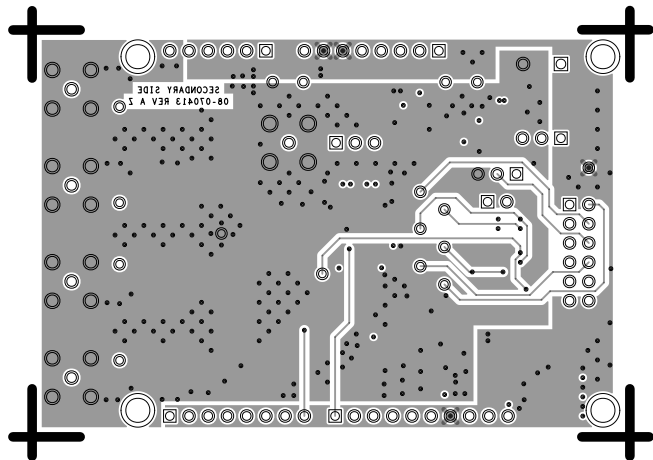
L3 PWR
08-070413-07
REV B



L4 SECONDARY

08-070413-02

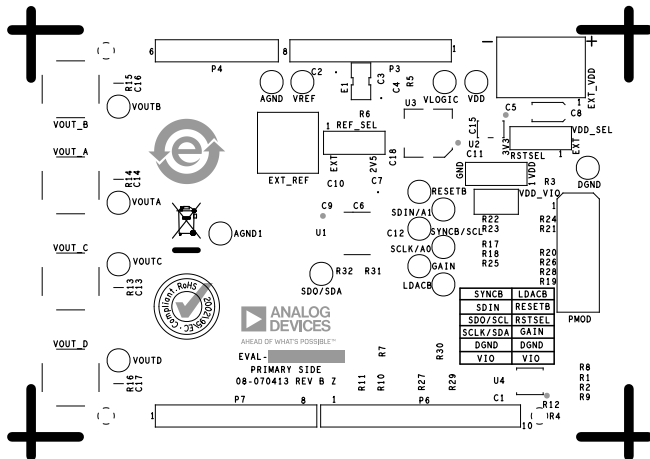
REV B



SILKSCREEN PRIMARY

08-070413-03

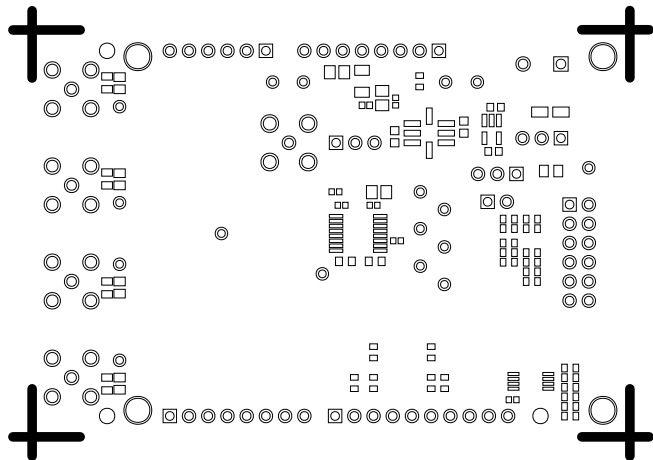
REV B



SOLDERMASK PRIMARY

08-070413-04

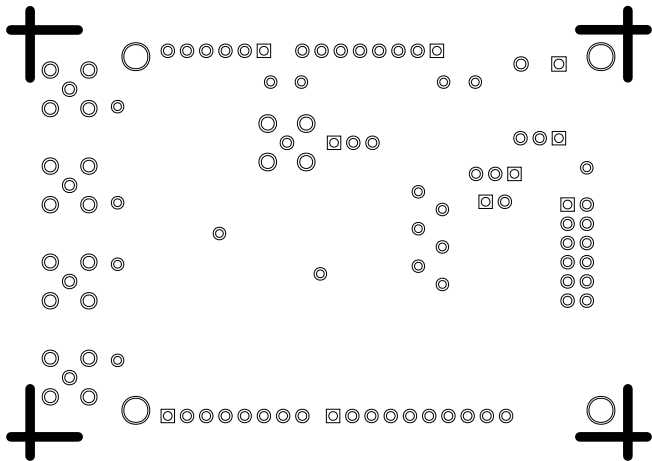
REV B



SOLDERMASK SECONDARY

08-070413-05

REV B



PASTEMASK PRIMARY

08-070413-08

REV B

