

D

C

B

A

87654321

THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES.  
THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.

RELAY CONTROL CHART

CONTROL	CODE	DEVICE	FUNCTION	CONNECTOR
P.O SPEC.	BK/BD SPEC.	SOCKET OEM	OEM PART#	HANDLER

JUMPER TABLE

JP#	ON	OFF
1		
2		
3		
4		
5		

\* SEE ASSEMBLY INSTRUCTIONS

REVISIONS

REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE CHANGES AS PER ECR-121247	02JUL23 24JUL24	S.KUMAR S.KUMAR

PAGE 2 - TOP VIEW

PAGE 3 - BOARD POWER

PAGE 4 - DIGITAL INTERFACE

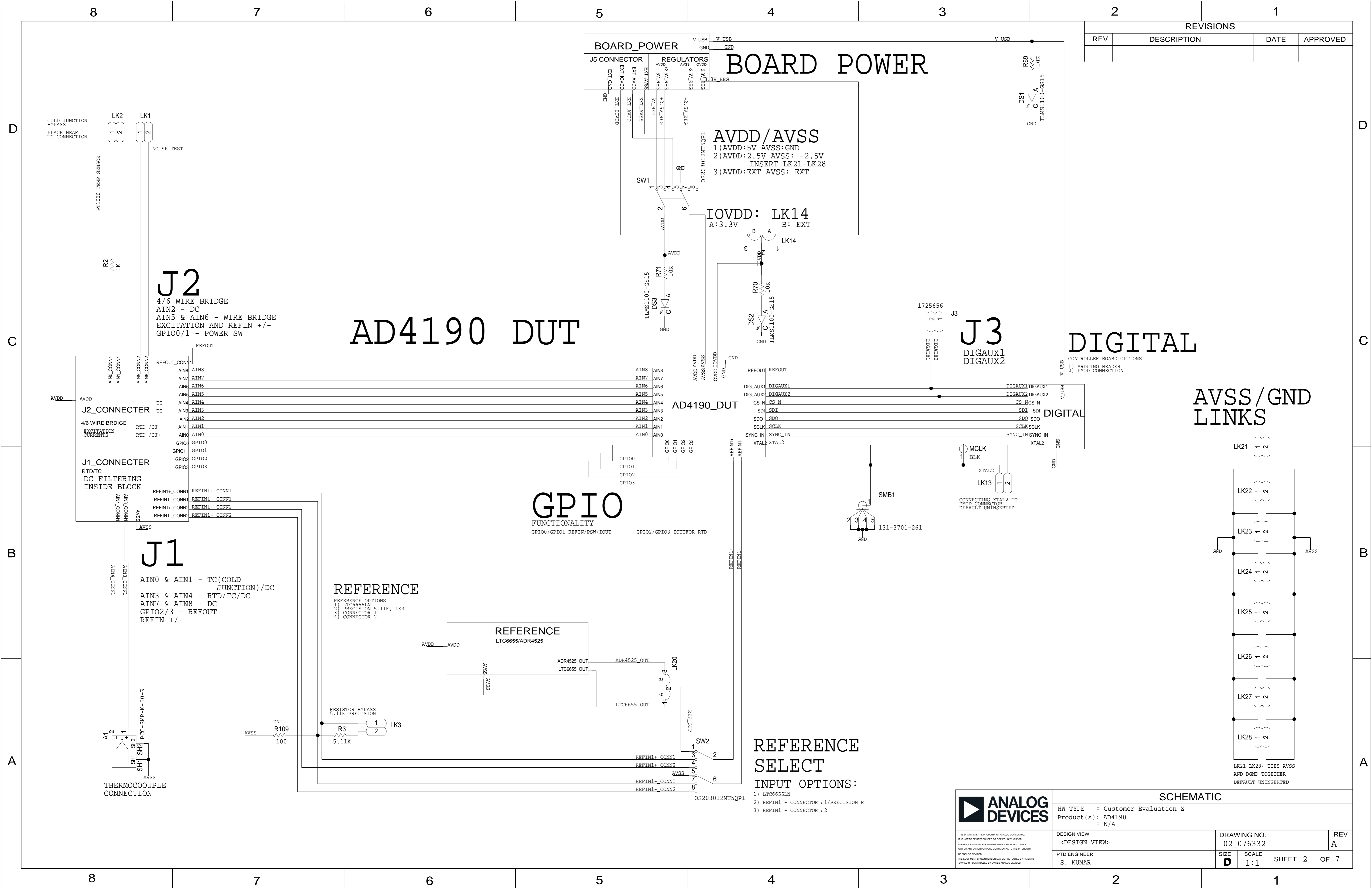
PAGE 5 - AD4190 CONNECTIONS

PAGE 6 - REFERENCE AND GPIOS

PAGE 7 - J1 AND J2 CONNECTORS

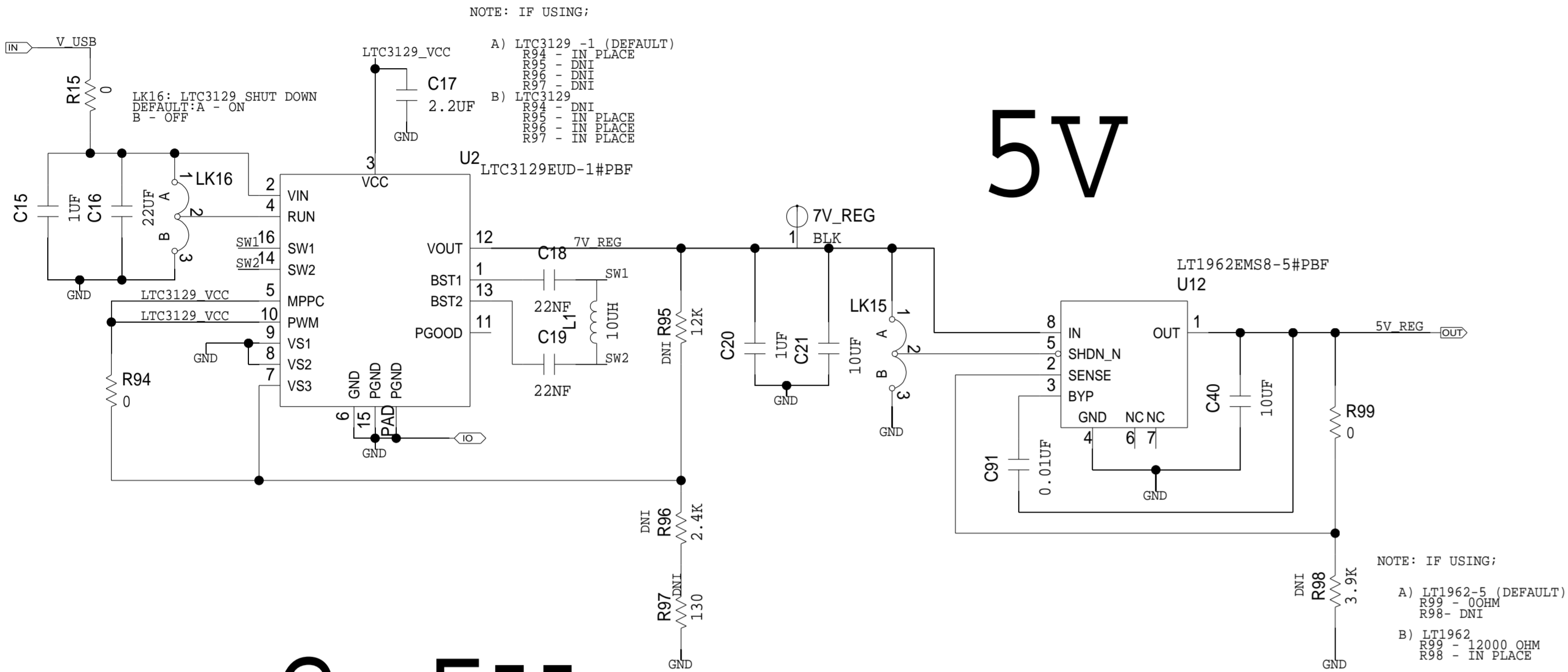
TEMPLATE ENGINEER N/A	DATE	SCHEMATIC							
HARDWARE SERVICES M. VALE	02JUL23	HW TYPE : Customer Evaluation Z Product(s): AD4190 : N/A  PACKAGE : LeadCount-lead BodySize PackageType-family : Pitch-pitch StyleVendor Style  <User Define> <User Define> <User Define>							
HARDWARE SYSTEMS N/A									
TEST ENGINEER N/A									
COMPONENT ENGINEER N. CATALAN	02JUL23								
TEST PROCESS N/A									
HARDWARE RELEASE K. JABATAN	02JUL23								
DESIGNER S. NODADO	02JUL23	MASTER PROJECT TEMPLATE TBD	TESTER TEMPLATE 064489_c	DRAWING NO. 02_076332		REV. A			
PTD ENGINEER S. KUMAR	02JUL23	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES							
CHECKER N/A		DECIMALS XXX +0.010 X.XXX +-0.005	FRACTIONS +/-1/32	ANGLES +2	SIZE D	SCALE 1:1	CODE ID NO. CodeID	SHEET 1	OF 7

87654321

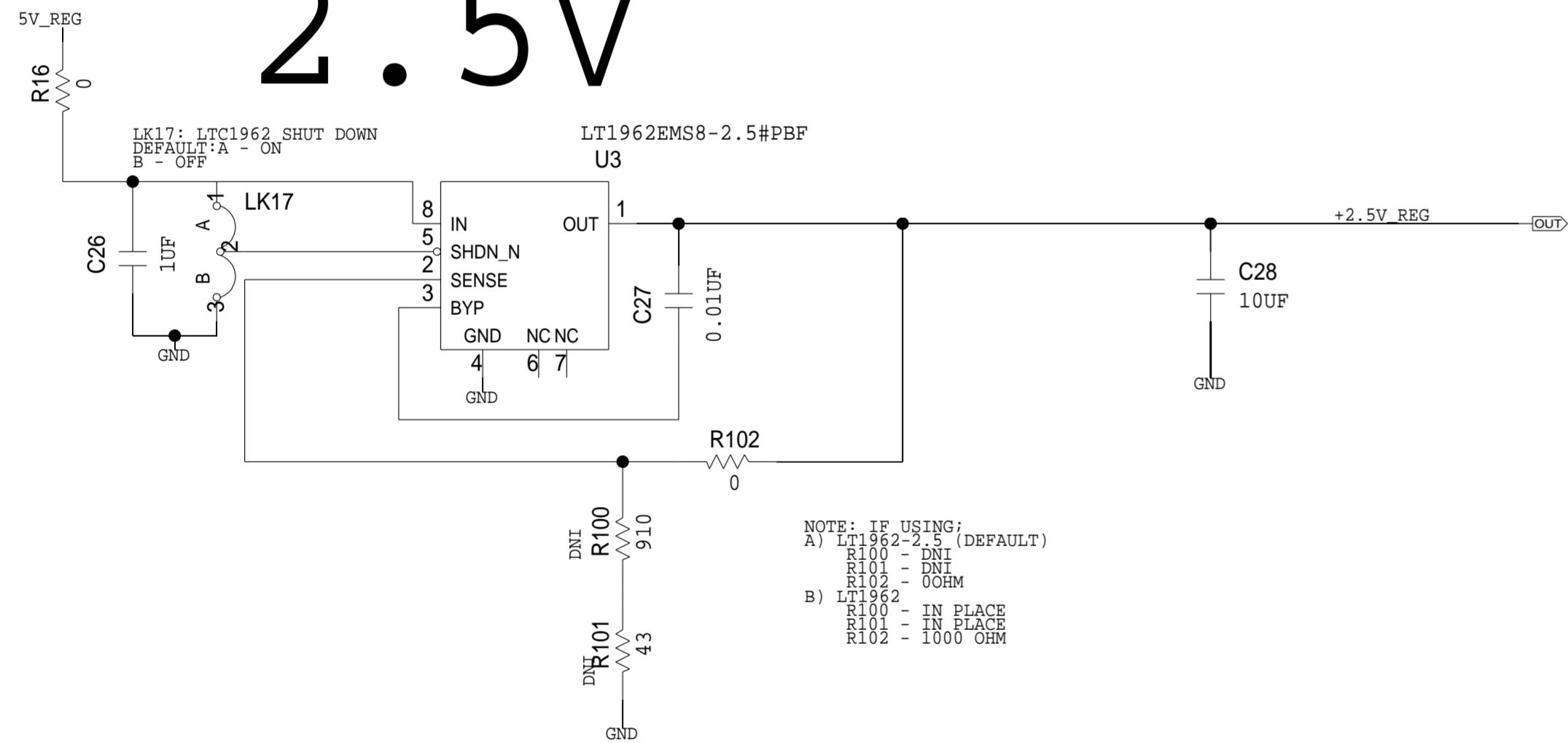


BOARD POWER

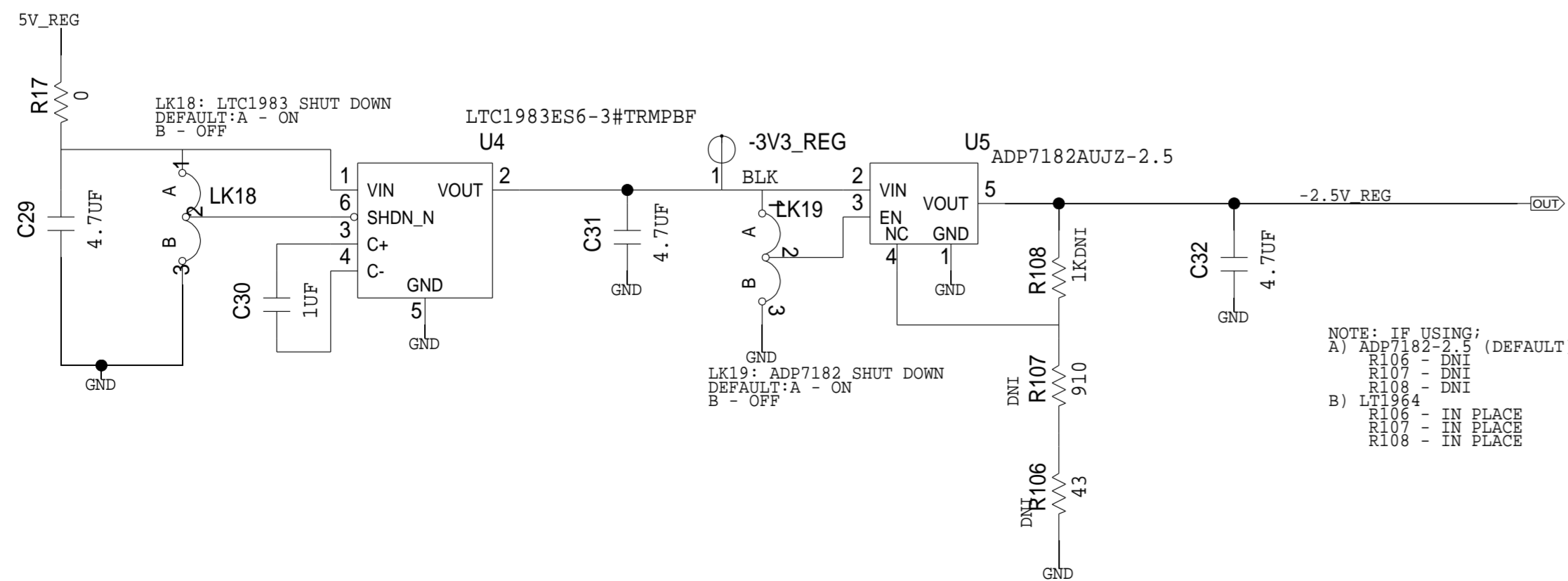
AVDD



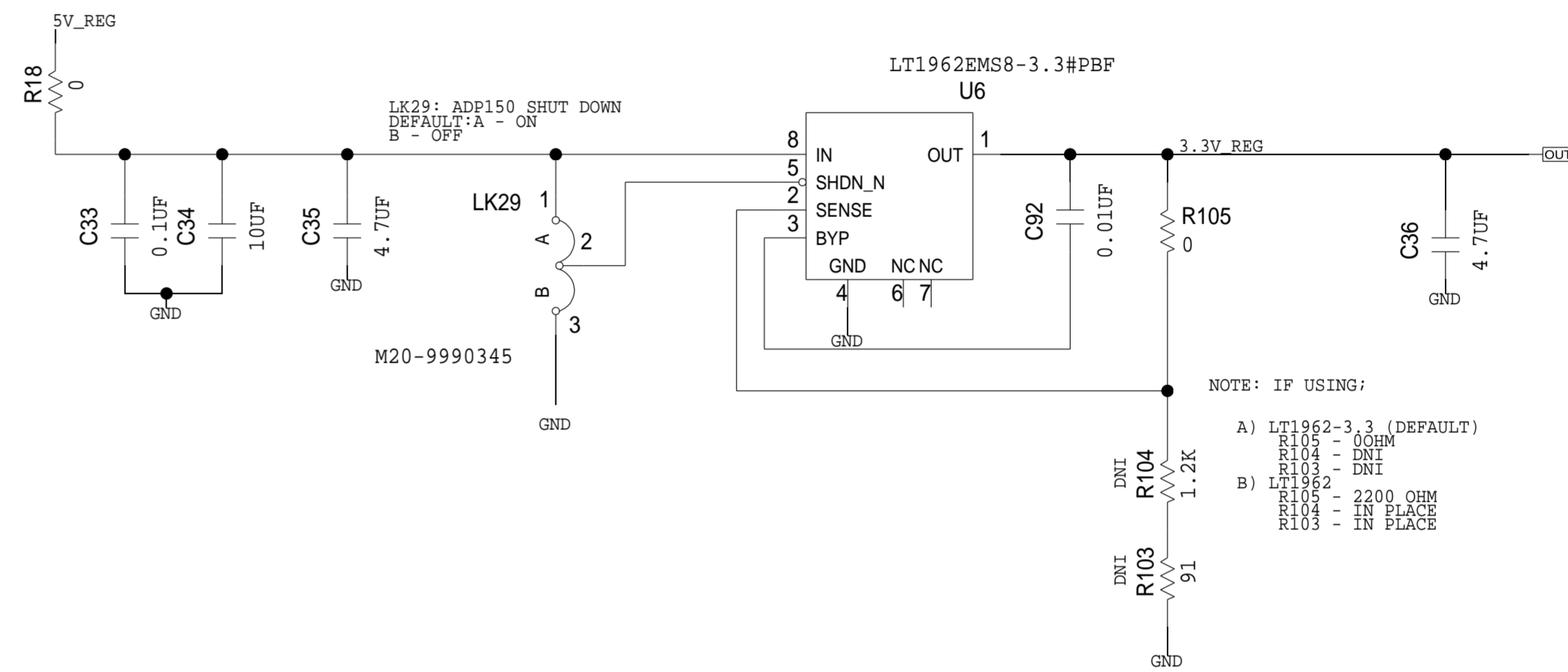
2.5V



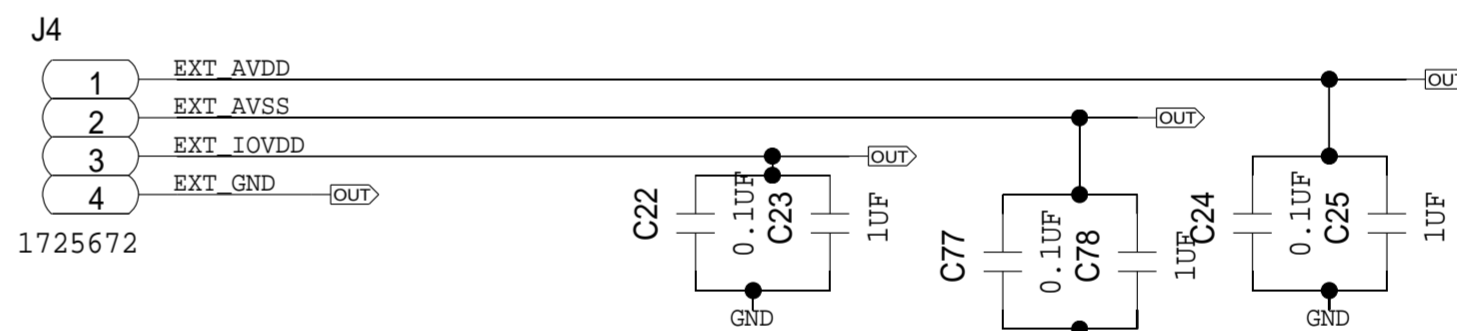
AVSS -2.5V



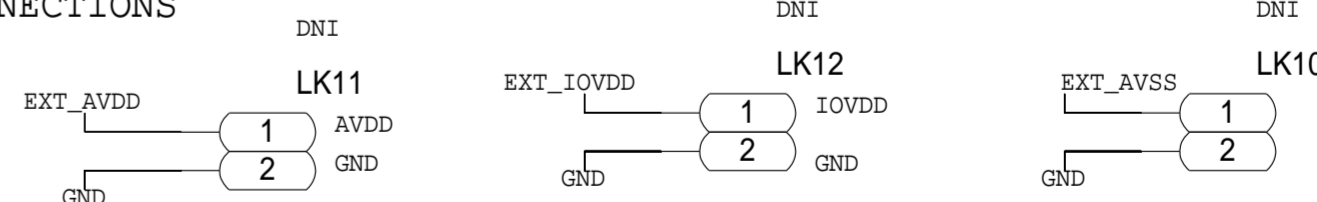
IOVDD 3.3V



EXT POWER



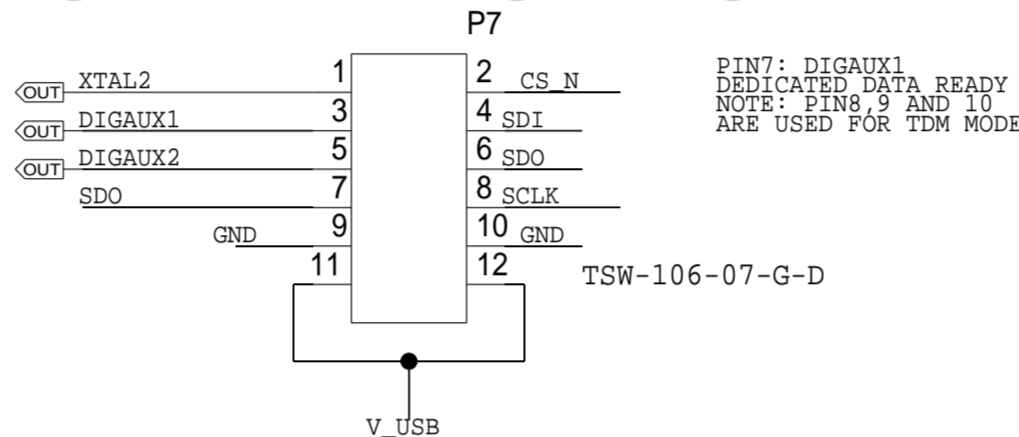
SCP CONNECTIONS



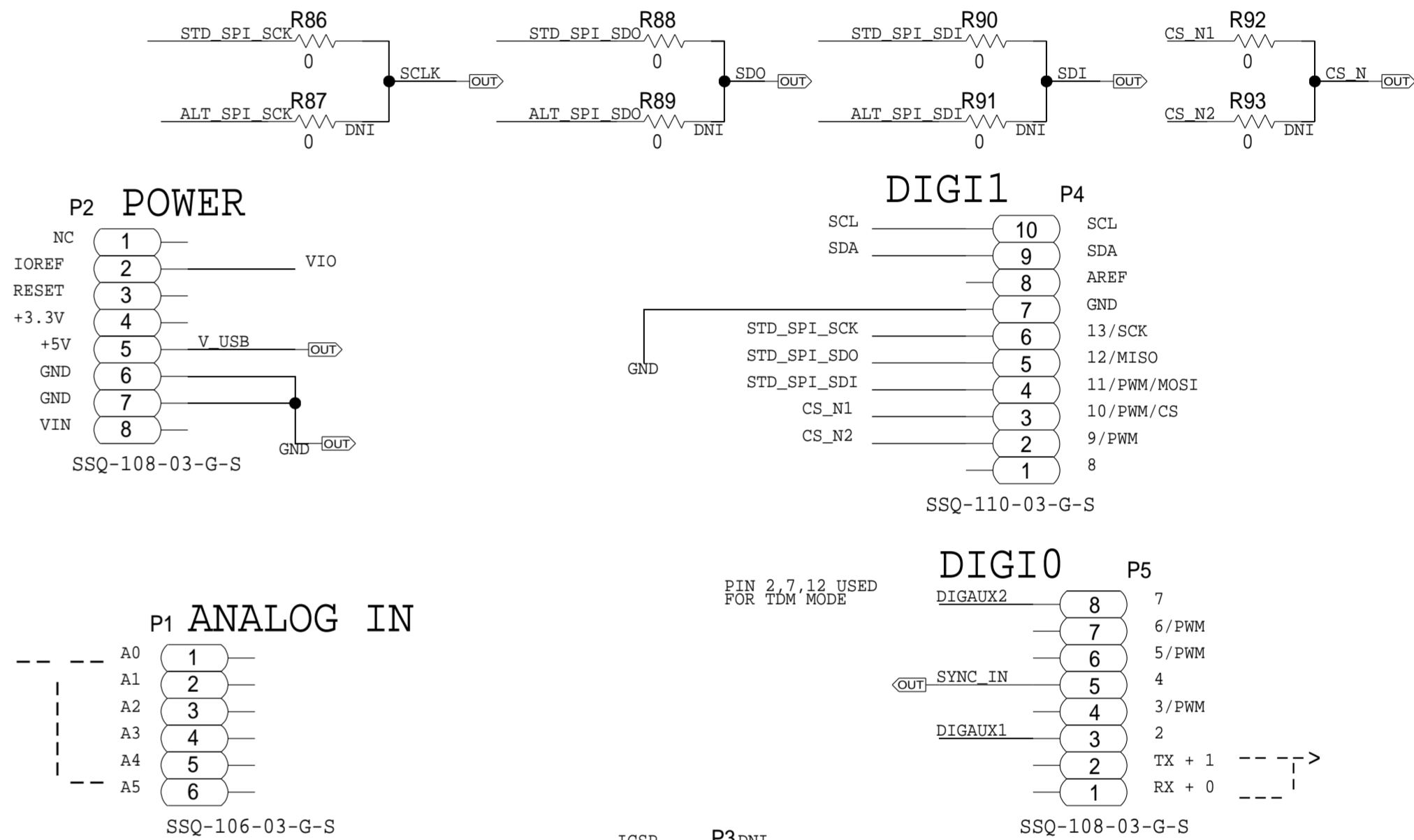
SCHEMATIC			
HW TYPE : Customer Evaluation Z			
Product(s): AD4190			
: N/A			
DESIGN VIEW	DRAWING NO.		REV
<DESIGN_VIEW>	02_076332		A
PTD ENGINEER	SIZE	SCALE	SHEET 3 OF 7
S. KUMAR	D	1:1	

DIGITAL

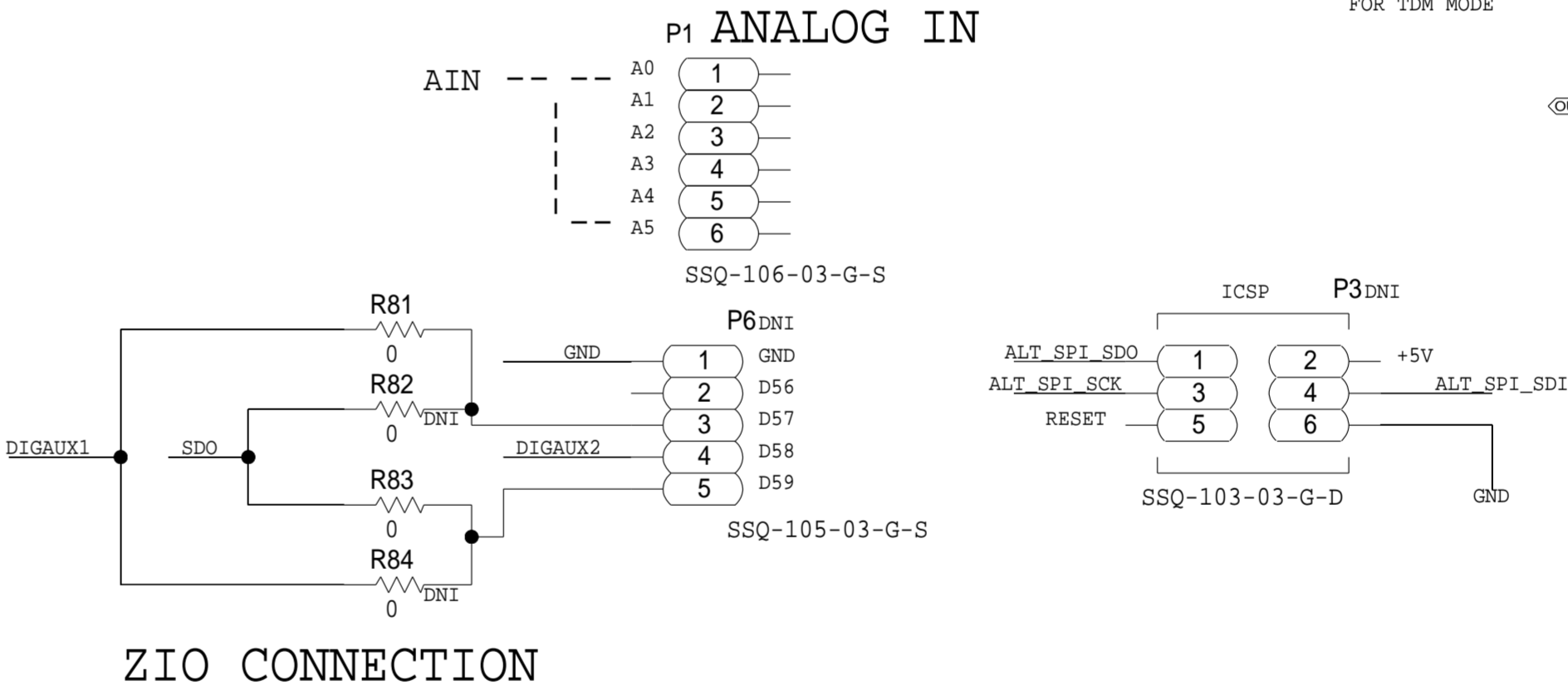
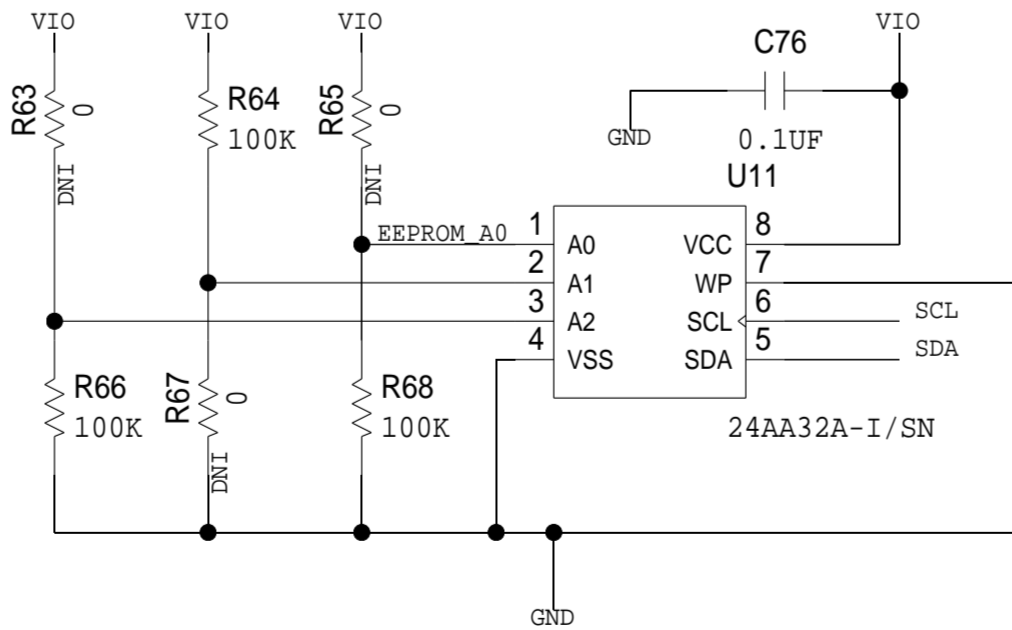
PMOD CONNECTOR



ARDUINO CONNECTOR



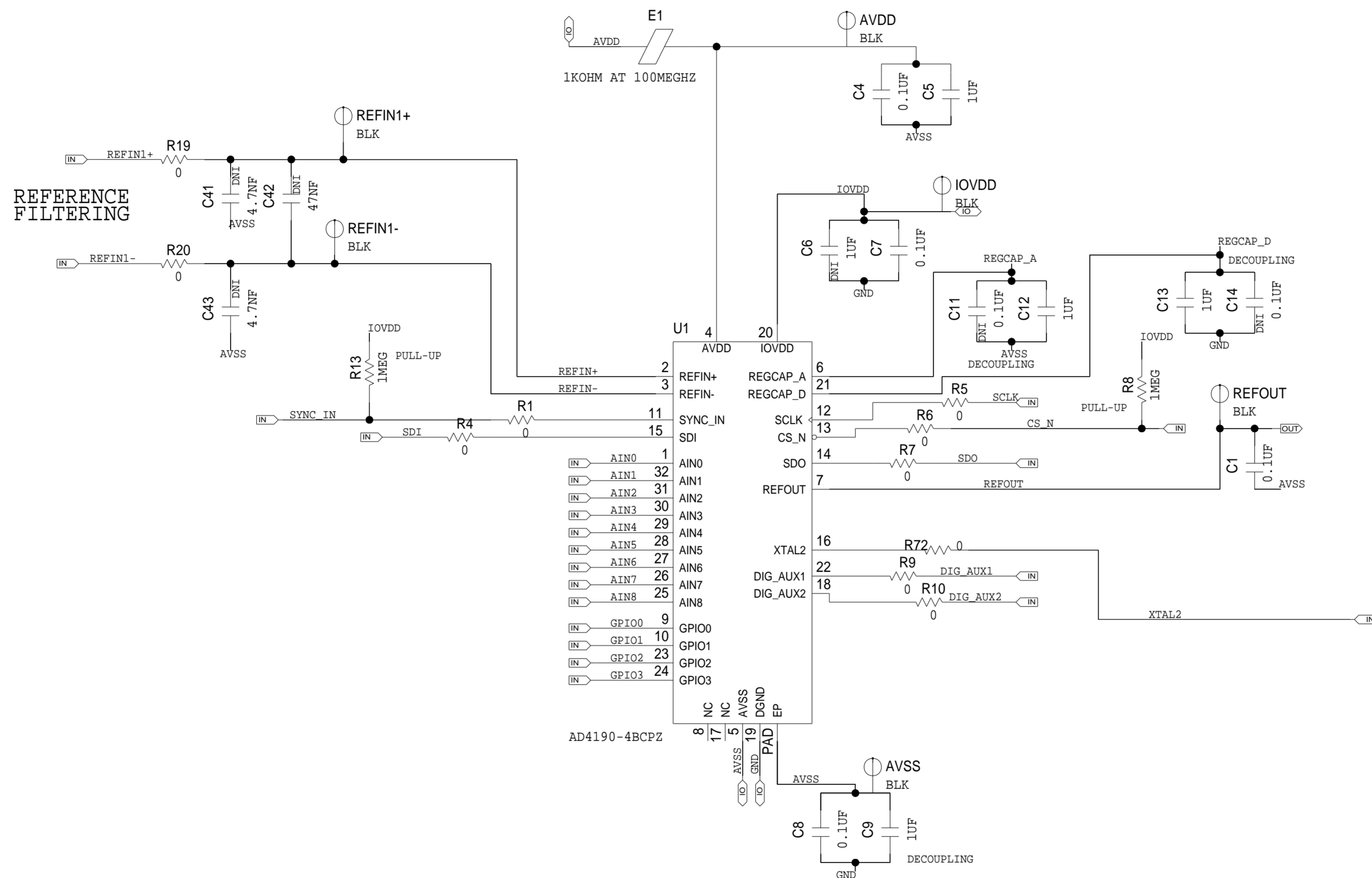
EEPROM CONNECTOR



ANALOG DEVICES	SCHEMATIC			
	HW TYPE : Customer Evaluation Z	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_076332	REV A
Product(s): AD4190		PTD ENGINEER S. KUMAR	SIZE D	SCALE 1:1
			SHEET 4	OF 7

AD4190 DUT

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



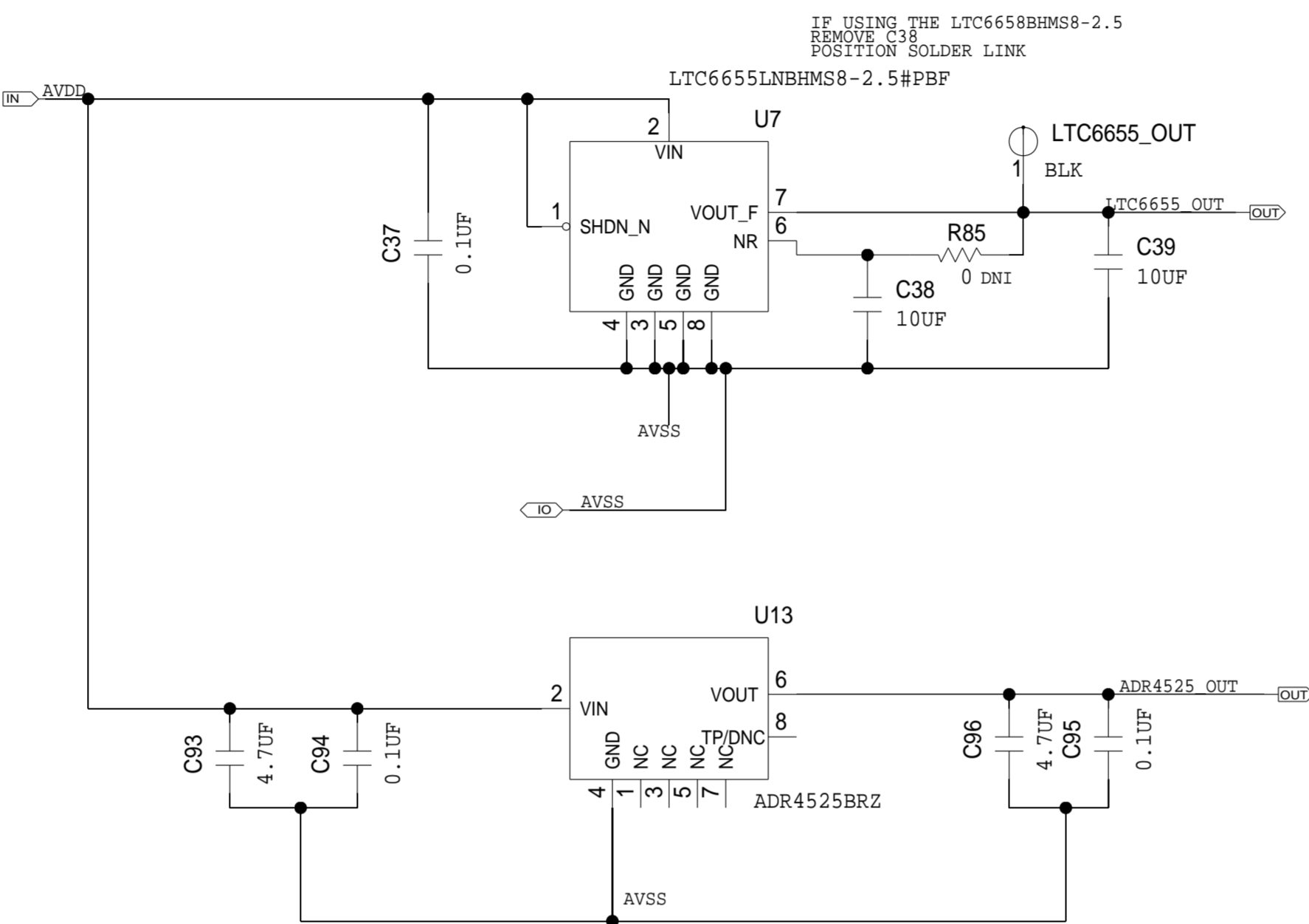
AVDD  
BOARD AVDD


AVSS  
BOARD AVSS

SCHEMATIC			
HW TYPE : Customer Evaluation Z Product(s): AD4190 : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_076332	REV A	
PTD ENGINEER S. KUMAR	SIZE D	SCALE 1:1	SHEET 5 OF 7

REFERENCE AND GPIO

REFERENCE STRUCTURE





SCHEMATIC

HW TYPE : Customer Evaluation Z  
Product(s): AD4190  
: N/A

DESIGN VIEW  
<DESIGN\_VIEW>

PTD ENGINEER  
S. KUMAR

DRAWING NO.  
02\_076332

SIZE  
D

REV  
A

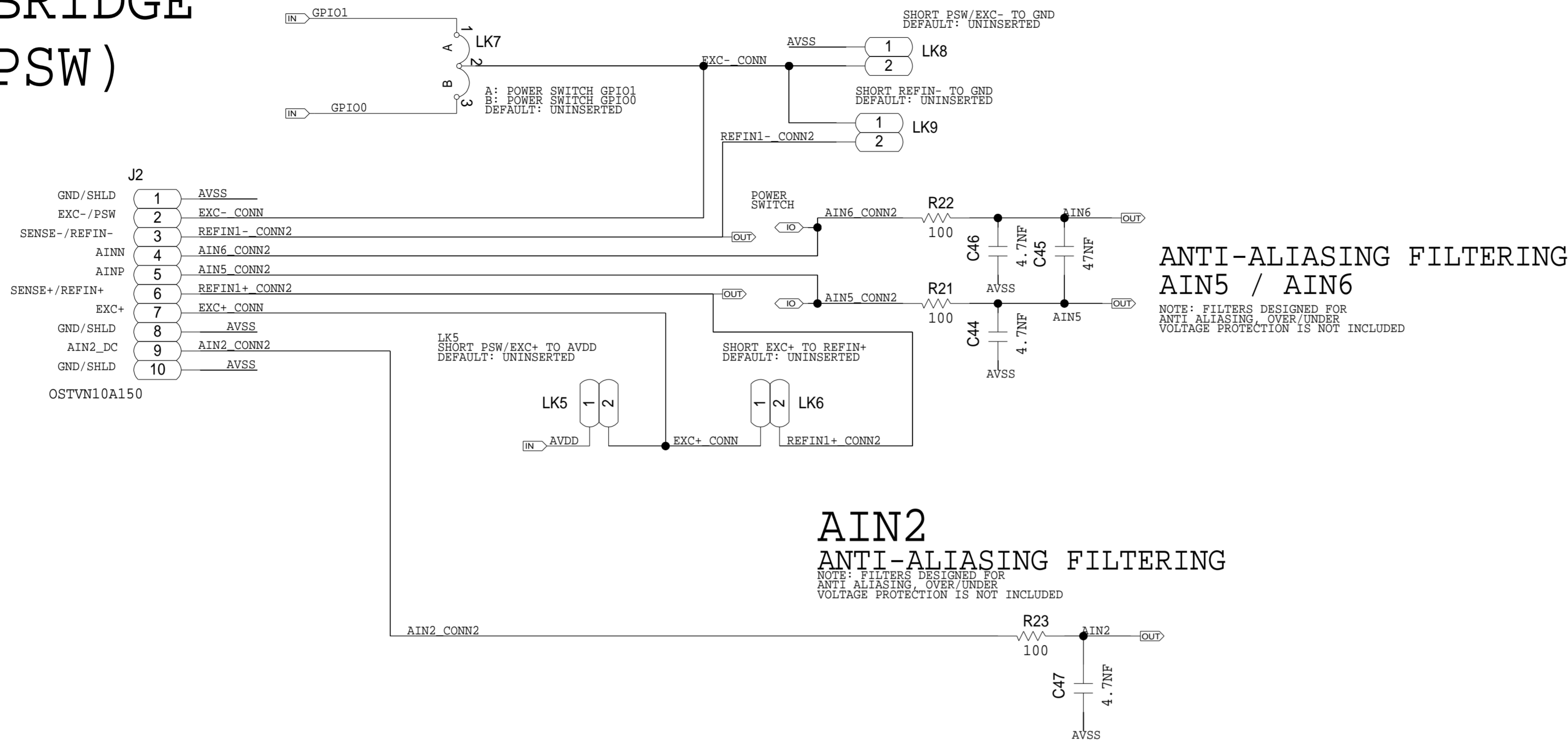
SCALE  
1:1

SHEET 6 OF 7

# J2 CONNECTOR

4/6 WIIRE BRIDGE

GPIO 0/1 (PSW)



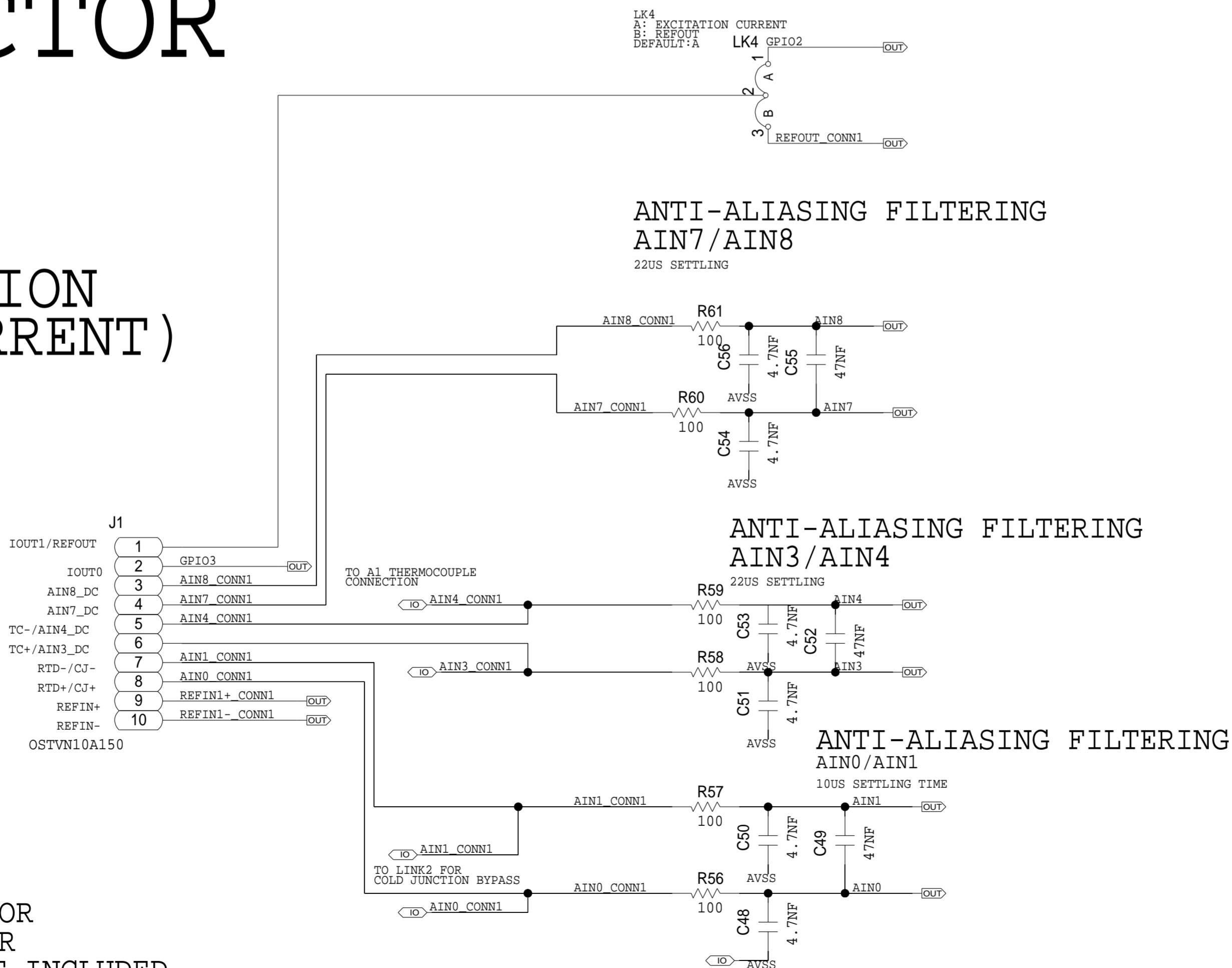
# J1 CONNECTOR

RTD

THERMOCOUPLE

REFOUT

GPIO 2/3 (EXCITATION CURRENT)



NOTE: FILTERS DESIGNED FOR ANTI ALIASING, OVER/UNDER VOLTAGE PROTECTION IS NOT INCLUDED

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY OWNED ANALOG DEVICES.

SCHEMATIC			
HW TYPE : Customer Evaluation Z			
Product(s): AD4190			
: N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_076332	REV A	
PTD ENGINEER S. KUMAR	SIZE D	SCALE 1:1	SHEET 7 OF 7