

ANALOG SIGNAL PATH

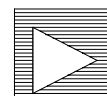
ADC

The diagram shows two pin connection configurations for the TMM-103-02-L-S connector. The top configuration is for ADDR2, where pin 3 is connected to VIO, pin 2 is connected to JP10, and pin 1 is connected to GND. The bottom configuration is for ADDR0, where pin 3 is connected to VIO, pin 2 is connected to JP11, and pin 1 is connected to GND. In both cases, the GND pin is connected to a ground symbol.

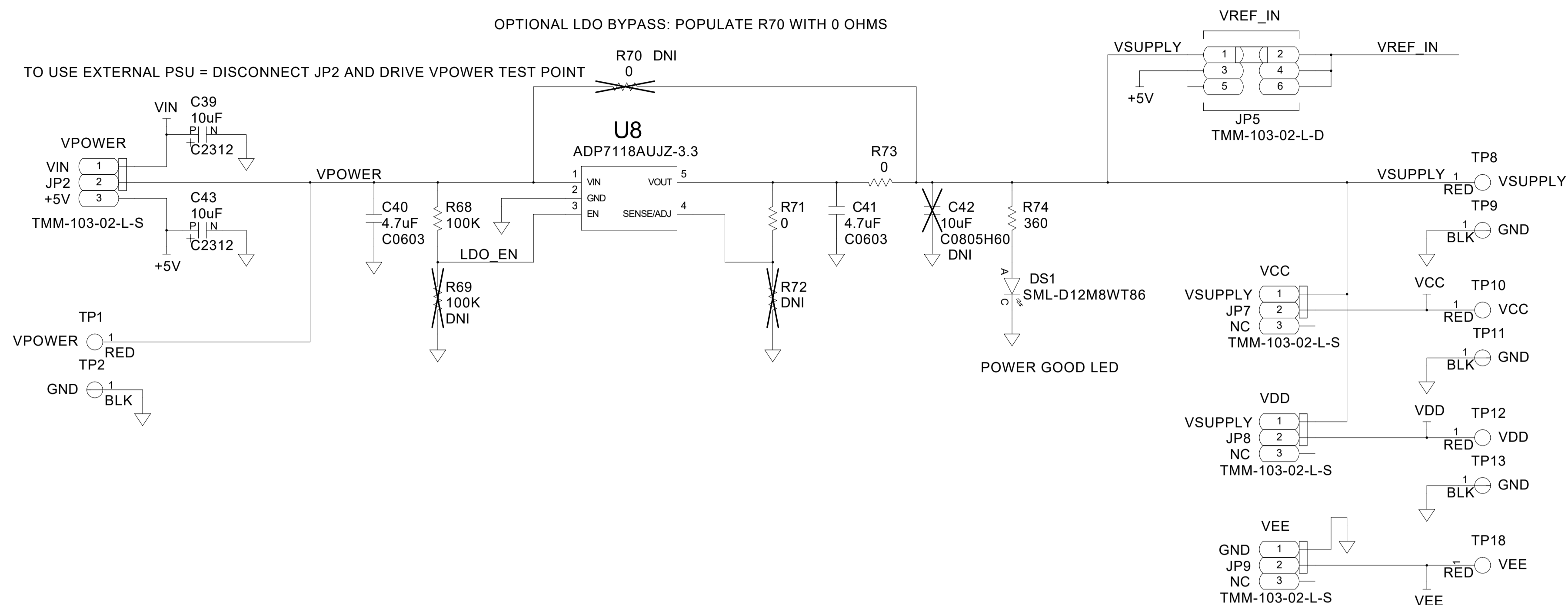


NOTES: UNLESS OTHERWISE SPECIFIED

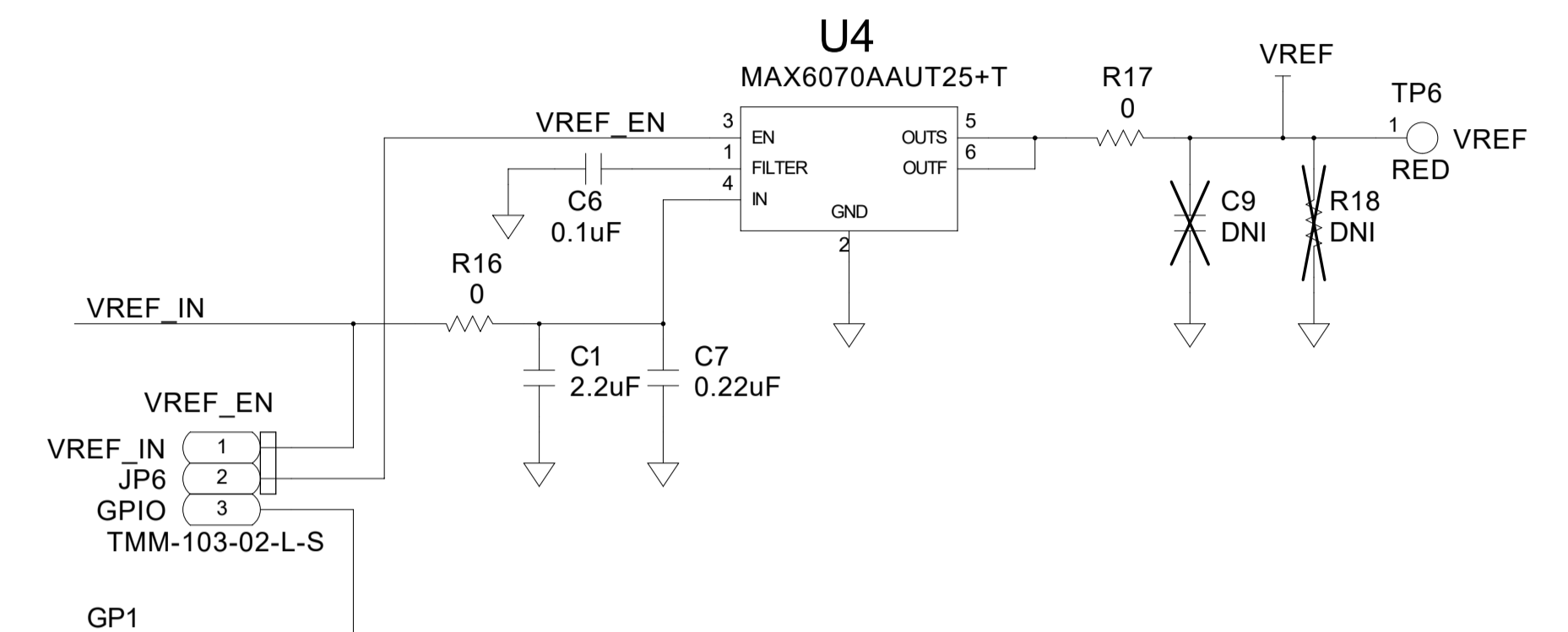
- | VARIANTS | PRODUCT GENERIC | BIT |
|----------|-----------------|-----|
| 01 | AD4062BCPZ | 16 |
| 02 | AD4060BCPZ | 12 |

TEMPLATE ENGINEER -	DATE	SCHEMATIC		 ANALOG DEVICES			
HARDWARE SERVICES M. VALE	04AUG23						
HARDWARE SYSTEMS -		HW TYPE : CUSTOMER EVALUATION Z PRODUCT(S): AD4062/60 BCPZ, LFCSP14 PACKAGE LOW POWER, 16/12-BIT EASY DRIVE SAR ADC					
TEST ENGINEER -							
COMPONENT ENGINEER ADGT LIBRARY	04AUG23						
TEST PROCESS -							
HARDWARE RELEASE K. JABATAN							
DESIGNER KIM T.	04AUG23	MASTER PROJECT TEMPLATE	TESTER TEMPLATE -	DRAWING NO. 02-076509-01		REV. B	
PTD ENGINEER TYLER S.	04AUG23	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES X.XX +0.010 +1/32 +2 X.XXX +0.005					
CHECKER -				SIZE D	SCALE 1:1	CODE ID NO.	SHEET 1 OF 2

POWER SUPPLY

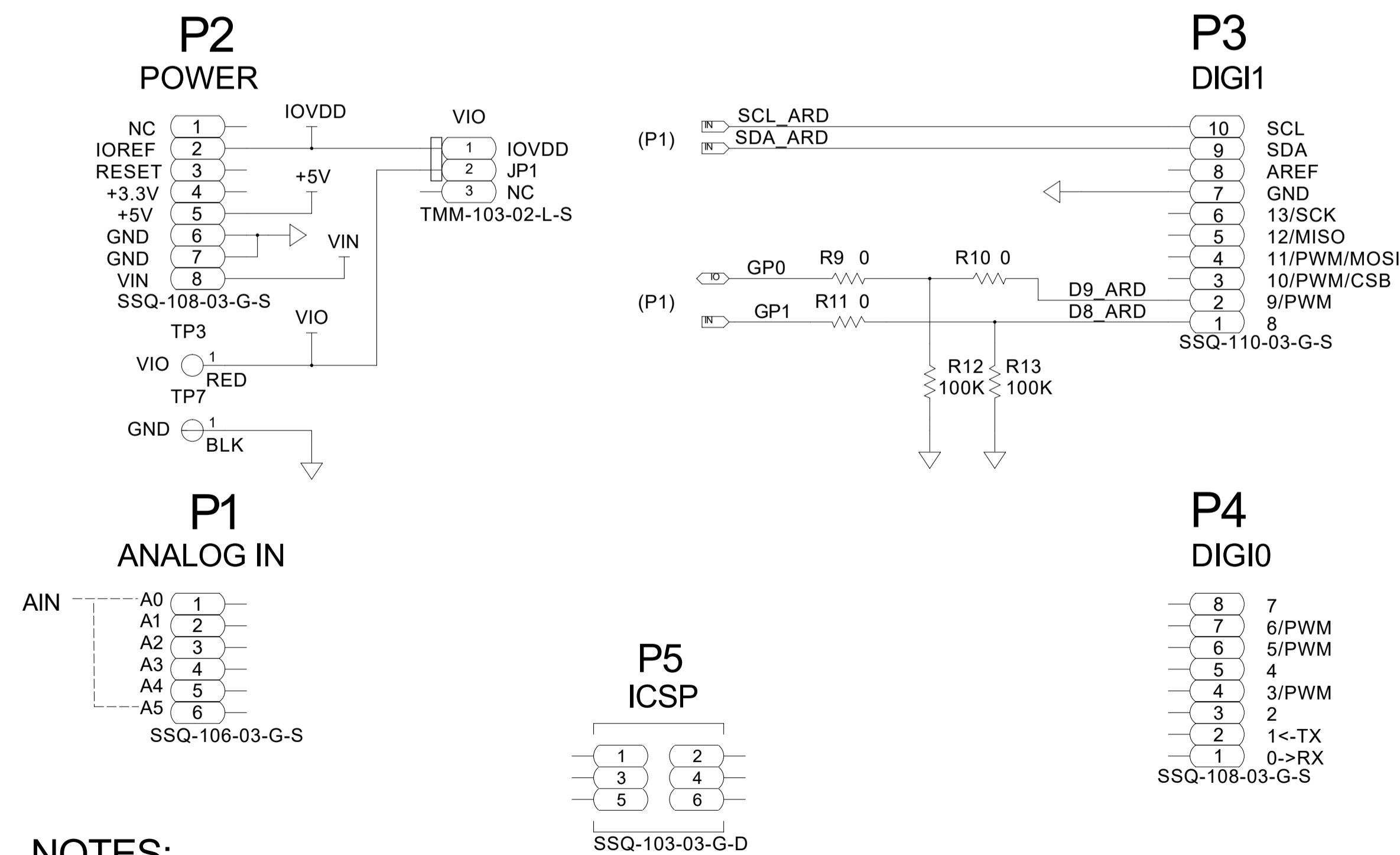


VOLTAGE REFERENCE



ARDUINO UNO SHIELD

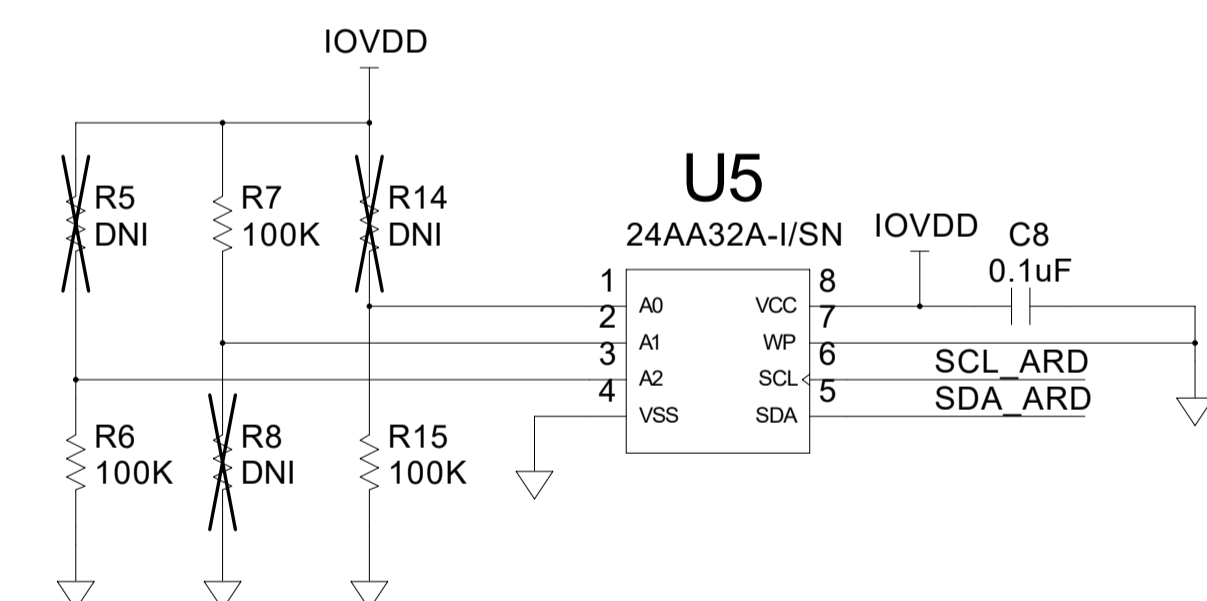
BIDIRECTIONAL HOST SUPPLY -->
VIN RANGE = 7V TO 12V
AD405X SUPPORTS 1.8V AND 3.3V LOGIC -->
3.3V LOGIC IS RECOMMENDED
DO NOT USE 5V LOGIC WITH AD405X




NOTES:

1. P5, ICSP, NOT SUPPORTED. PROVIDED FOR PASS-THROUGH TO OTHER SHIELDS ONLY
2. INSTALL P1 - P4 ON TOP SIDE AND P5 ON BOTTOM SIDE. DO NOT CUT ANY PIN OF THESE COMPONENTS AFTER ASSEMBLY.

EEPROM



 ANALOG DEVICES		<h1 style="margin: 0;">SCHEMATIC</h1>				
		<p>HW TYPE : CUSTOMER EVALUATION Z PRODUCT(S): AD4062/60 BCPZ, LFCSP14 PACKAGE LOW POWER, 16/12-BIT EASY DRIVE SAR ADC</p>				
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		PTD ENGINEER TYLER S.		SIZE D	SCALE 1:1	SHEET 2 OF 2