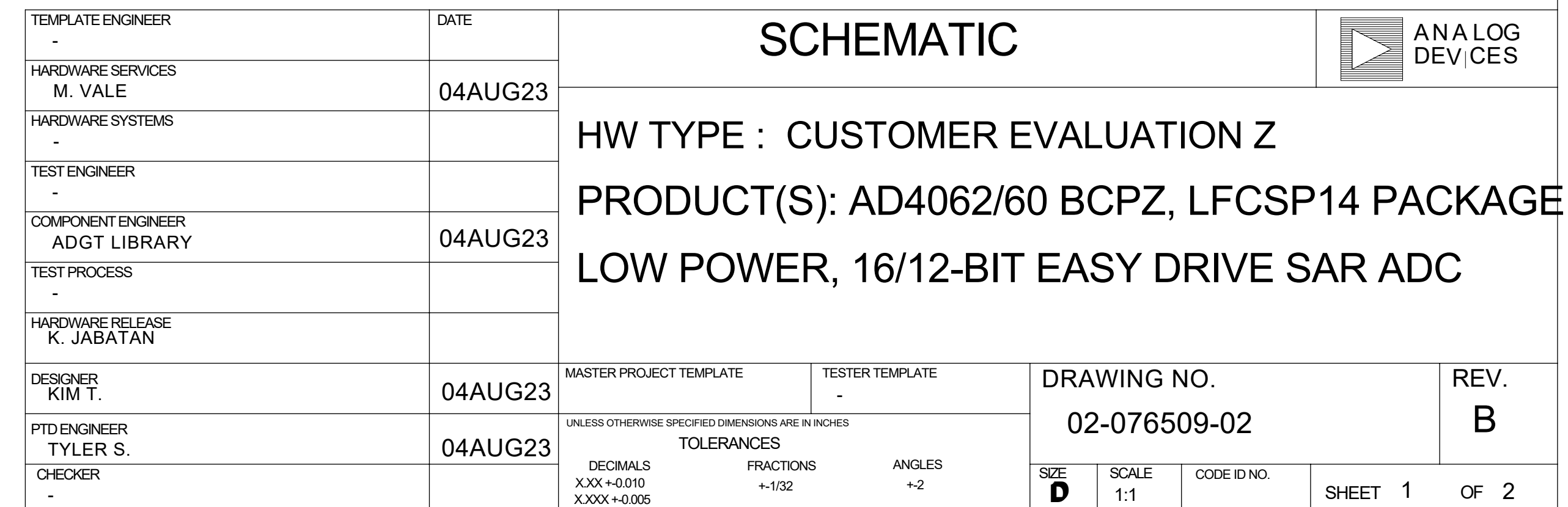
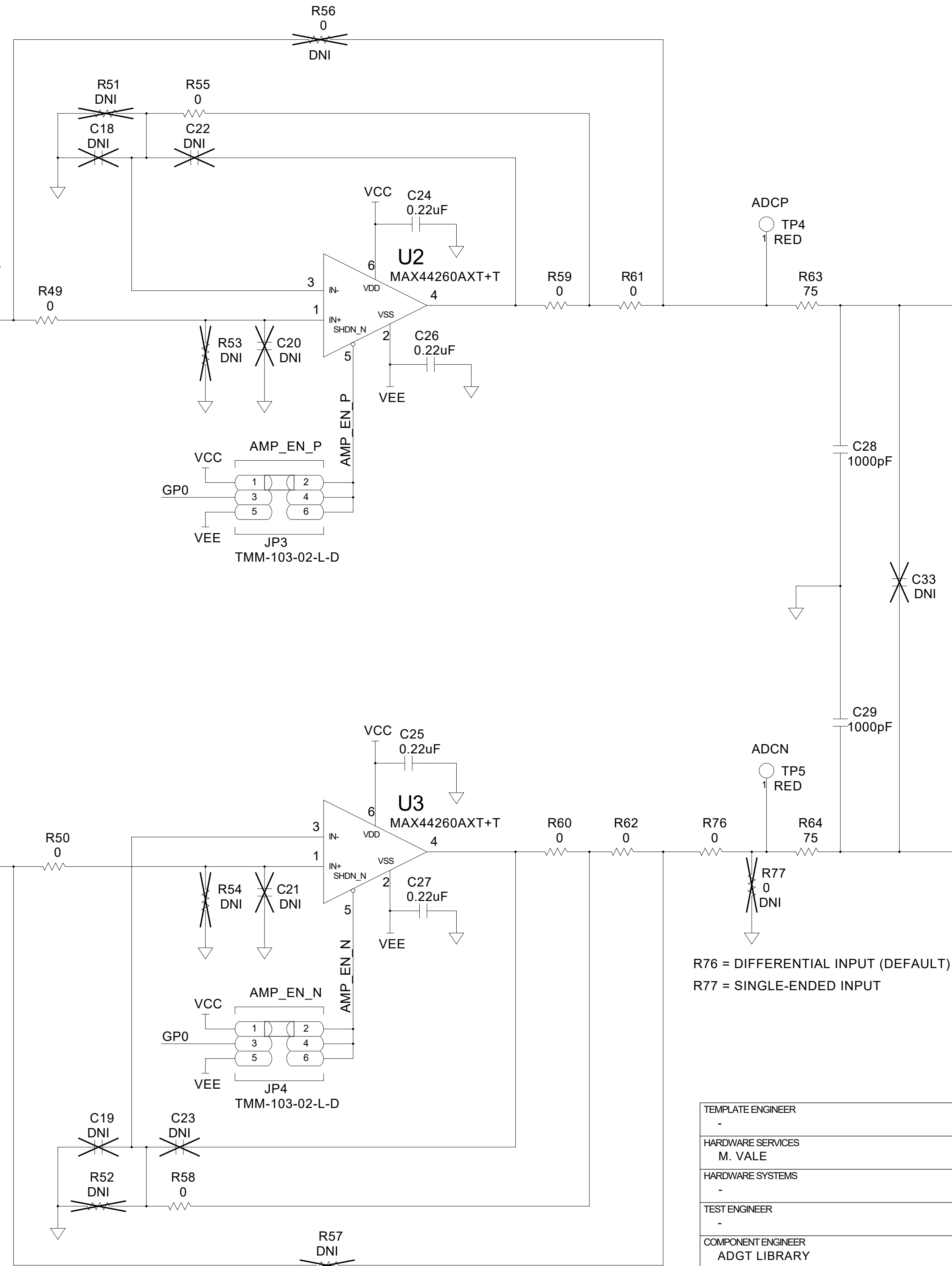


## ANALOG SIGNAL PATH

# ADC

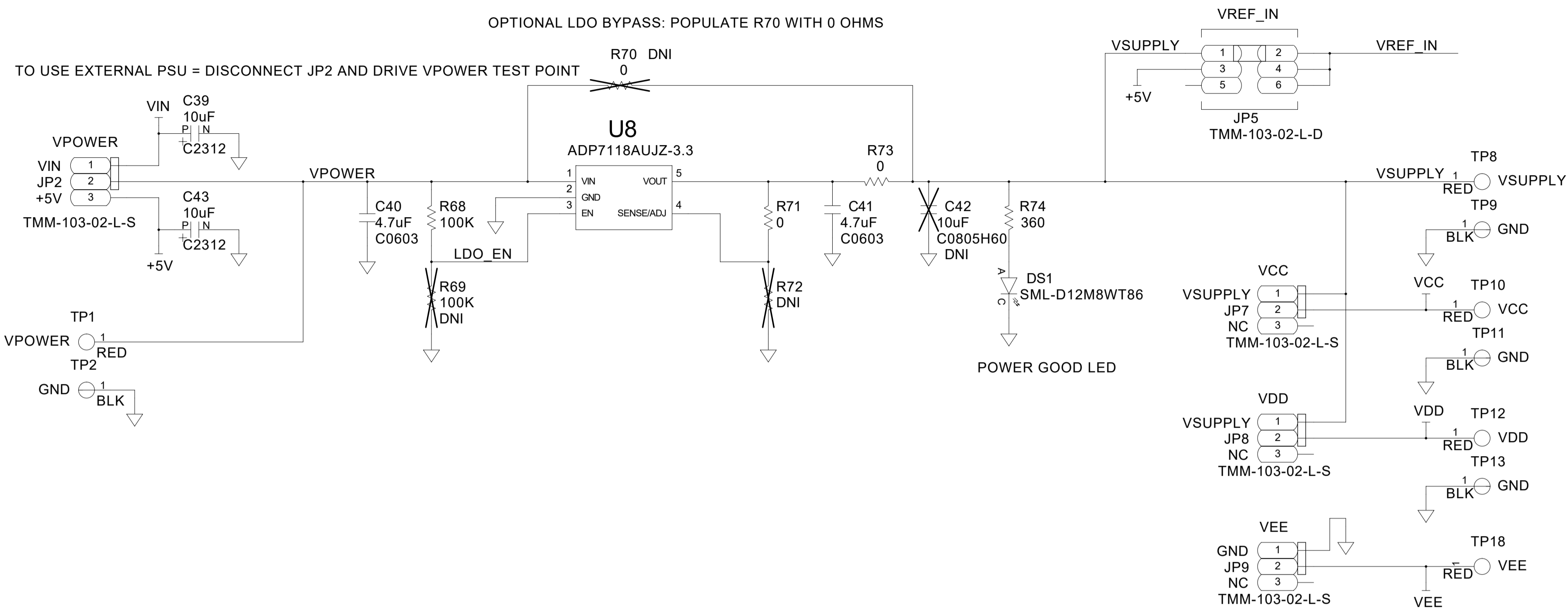
Diagram showing the connection for Pin 1 of the TMM-103-02-L-S connector. The pin is labeled '1' and 'GND'. It is connected to a ground symbol. The adjacent pin is labeled '2' and 'JP10'. The top pin is labeled '3' and 'VIO'. The connector is labeled 'TMM-103-02-L-S'.



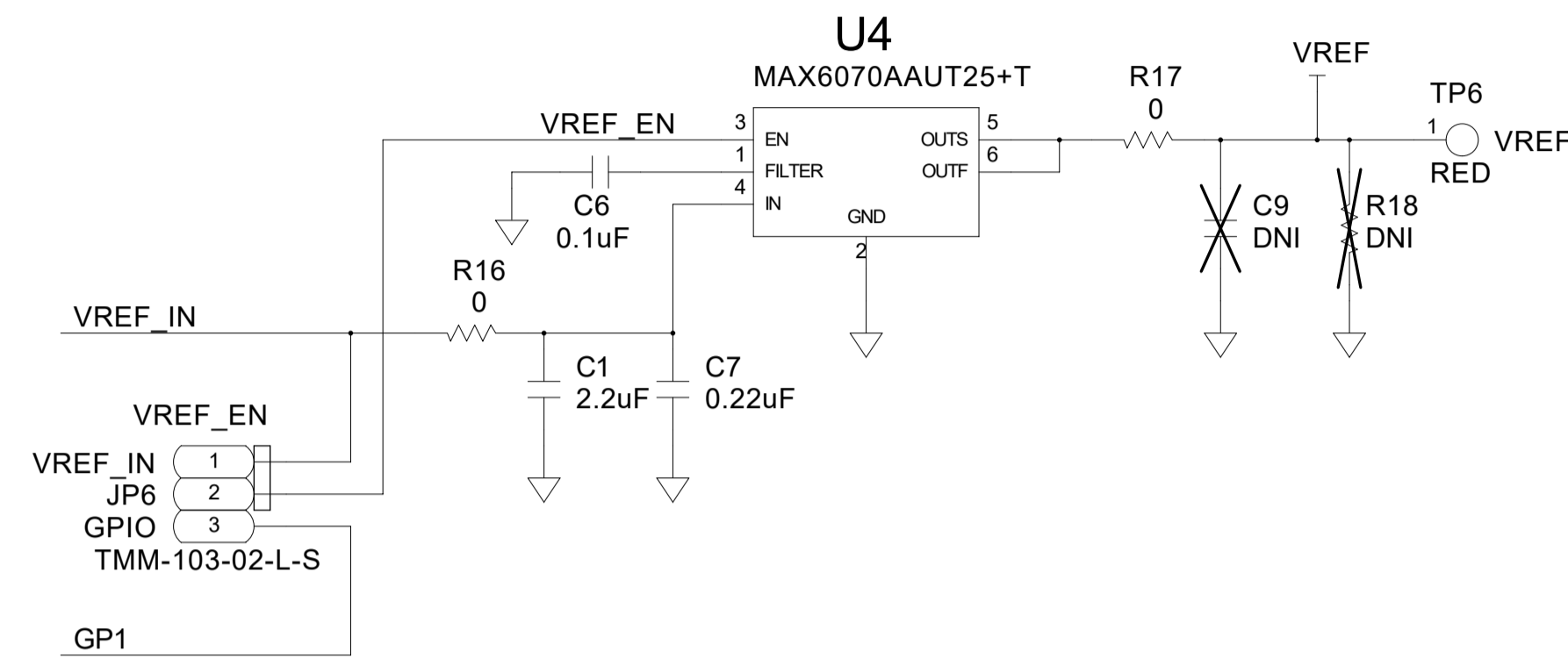
1. ALL RESISTORS AND CAPACITORS ARE 0402.
2. INSTALL SHUNTS ON ALL JUMPERS AS SHOWN.
3. THERE ARE 2 ASSEMBLY VARIANTS FOR THIS DESIGN.  
SEE BILL OF MATERIALS FOR COMPONENT VALUES ON EACH.

VARIANTS	PRODUCT GENERIC	BIT
01	AD4062BCPZ	16
02	AD4060BCPZ	12

# POWER SUPPLY

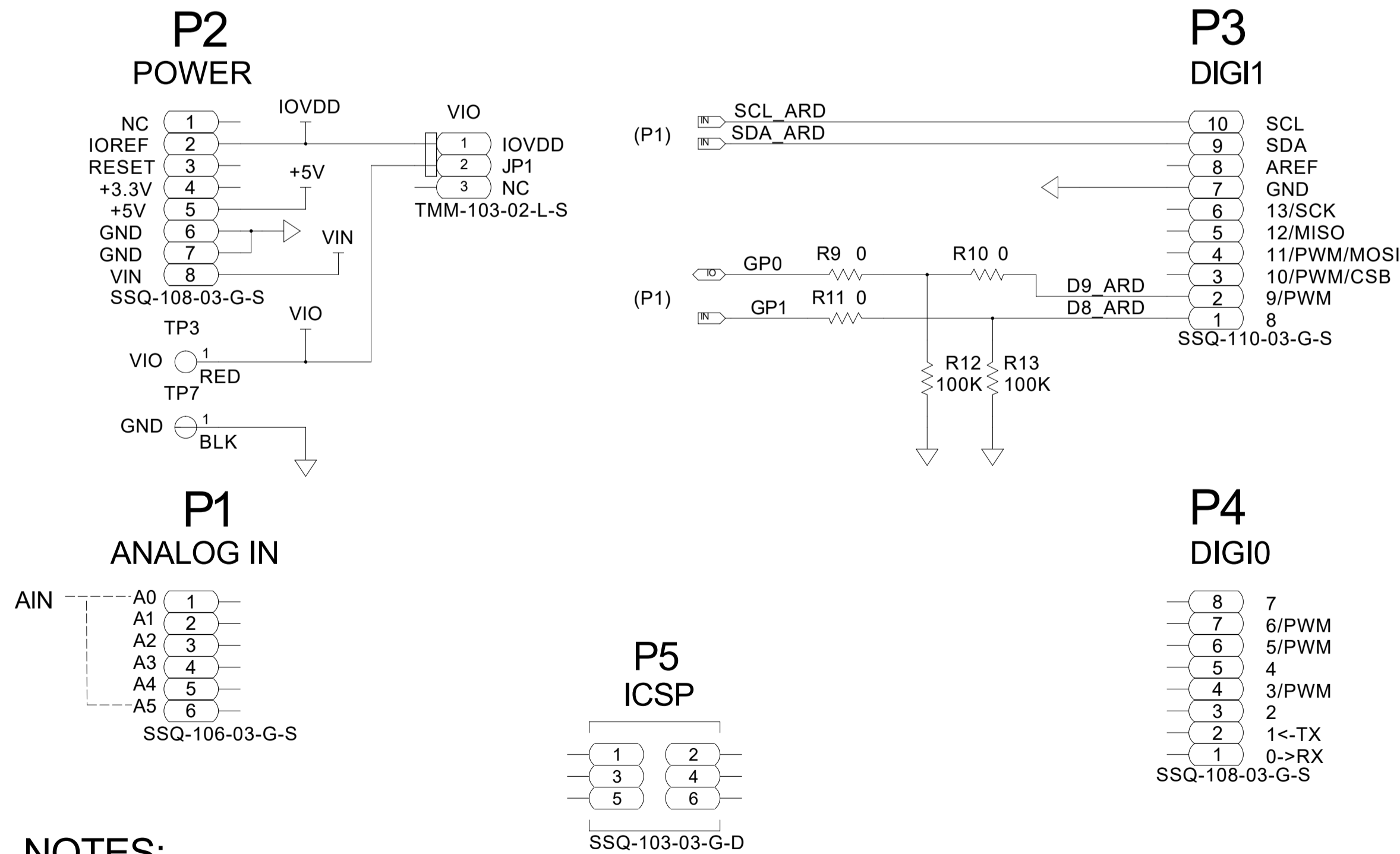


# VOLTAGE REFERENCE



# ARDUINO UNO SHIELD

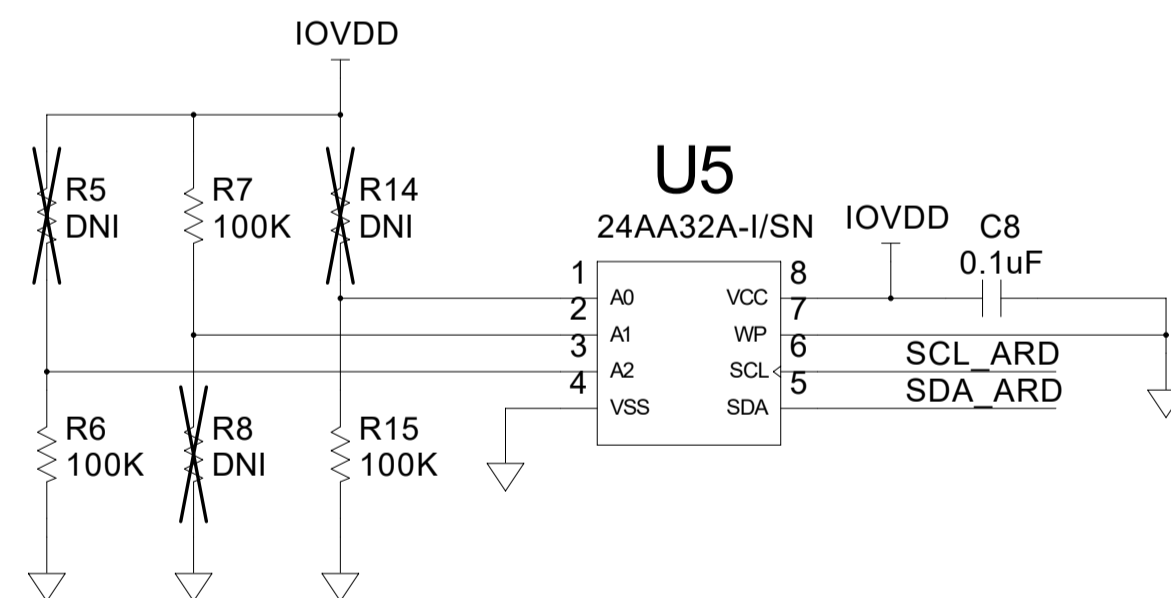
BIDIRECTIONAL HOST SUPPLY -->  
VIN RANGE = 7V TO 12V  
AD405X SUPPORTS 1.8V AND 3.3V LOGIC -->  
3.3V LOGIC IS RECOMMENDED  
DO NOT USE 5V LOGIC WITH AD405X



## NOTES:

- P5, ICSP, NOT SUPPORTED. PROVIDED FOR PASS-THROUGH TO OTHER SHIELDS ONLY
- INSTALL P1 - P4 ON TOP SIDE AND P5 ON BOTTOM SIDE. DO NOT CUT ANY PIN OF THESE COMPONENTS AFTER ASSEMBLY.

# EEPROM



REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

	SCHEMATIC		
	HW TYPE : CUSTOMER EVALUATION Z PRODUCT(S): AD4062/60 BCPZ, LFCSP14 PACKAGE LOW POWER, 16/12-BIT EASY DRIVE SAR ADC		
	DESIGN VIEW KIMT.	DRAWING NO. 02-076509-02	REV B
PTD ENGINEER TYLER S.	SIZE D	SCALE 1:1	SHEET 2 OF 2