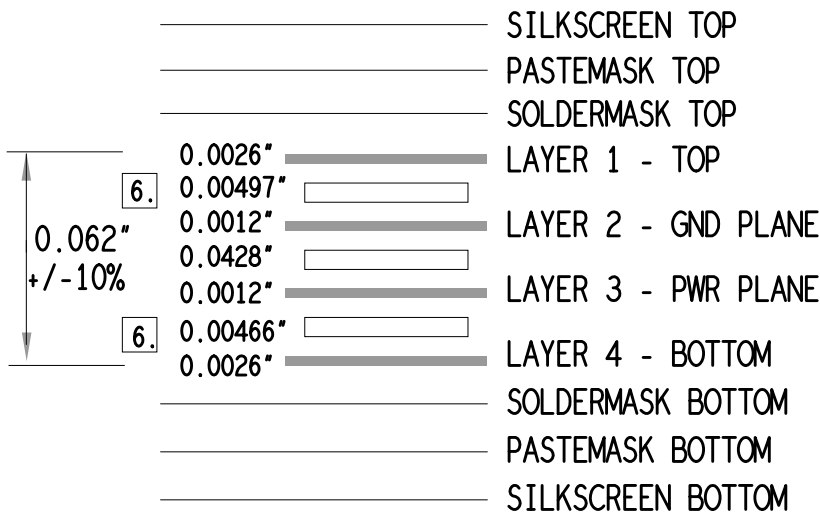


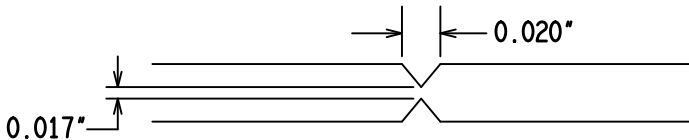
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	04AUG23	TYLER S.
B	CHANGES PER ECR-123370	19DEC24	G. MANN

4-LAYER STACKUP



SPECIFICATIONS:

- MATERIALS:** LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT. ALL LAMINATES AND BONDING MATERIALS SHOULD BE SELECTED FROM IPC-4101 OR IPC-4103. U.L. RATING OF 94 V-0
- CLADDING:** EXTERNAL LAYERS 2 OZ. COPPER. INTERNAL LAYERS 1 OZ. COPPER. NOTE: IF THE LAYER STACKUP CONFLICTS WITH THE ABOVE CLADDING SPECIFICATIONS THEN THE LAYER STACKUP SHALL TAKE PRECEDENCE.
- SOLDER MASK:** SHALL BE LIQUID PHOTOIMAGEABLE (LPI) APPLIED ON BOTH SIDES OVER BARE COPPER OR GOLD AND SHALL MEET IPC-SM-840 (LATEST REV.) CLASS 3. COLOR GREEN.
- SILK SCREEN:** SHALL BE PERMANENT NON-CONDUCTIVE EPOXY INK, COLOR: WHITE SYNTHETIC INKJET PRINTING ALLOWED FOR DENSE BOARDS,
- SURFACE FINISH:** GOLD IMMERSION BOTH SIDES.
- SUBJECT TO CHANGE BY MANUFACTURER,** DEPENDING ON DIELECTRIC CONSTANT DEVIATIONS. PLEASE CONSULT ADI.
- CONTROLLED 50 OHM +/-5% IMPEDANCE** FOR LAYERS 1-2 USING 0.019" TRACE AT 2GHZ PREQ.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):**

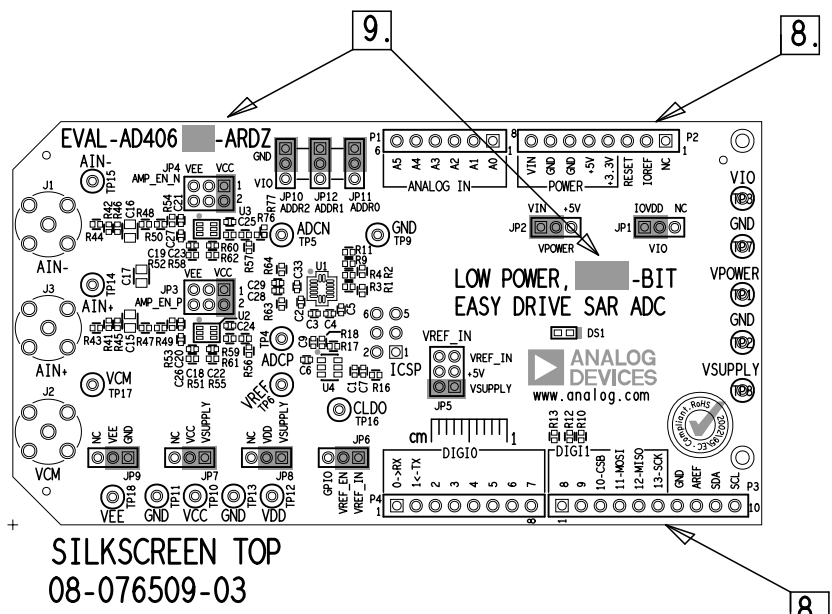


REQUIREMENTS:

- REFER TO IPC-6010 SERIES (LATEST REV.), CLASS 2 FOR FABRICATION UNLESS OTHERWISE SPECIFIED.
- ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00115, (LATEST REVISION.)
- MODIFICATIONS TO THE ARTWORK ARE NOT ALLOWED WITHOUT WRITTEN AUTHORIZATION.
- HOLE PATTERN TOLERANCES FOR UNDIMENSIONED HOLES SHALL BE A DIAMETER OF 0.005 INCHES FROM THEIR TRUE POSITION.
- PLATED HOLE WALL THICKNESS SHALL NOT BE LESS THAN 0.001 INCH MINIMUM AVERAGE, WITH NO READING LESS THAN .0008 BY CROSS SECTION.
- HOLE DIAMETERS APPLY AFTER PLATING.
- FINISHED CONDUCTOR WIDTHS SHALL NOT BE REDUCED FROM THE NOMINAL INDICATED ON THE MASTER PATTERN, BY MORE THAN THE CONDUCTOR THICKNESS.
- MINIMUM DESIGN LINE WIDTH IS .010 INCH.
- MINIMUM DESIGN SPACING IS .006 INCH.
- NON-FUNCTIONAL PAD REMOVAL FROM INNER SIGNAL LAYERS MAY BE PERFORMED AFTER CUSTOMER APPROVAL.

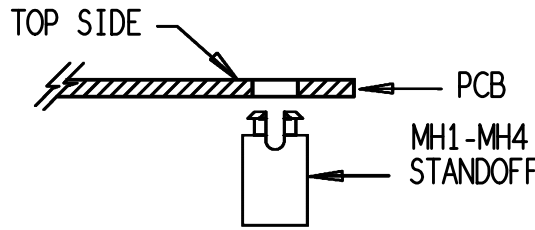
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES		APPROVAL	DATE	<div><div></div><div>ANALOG DEVICES</div><div>WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887</div></div> <div>TITLE FABRICATION EVALAD4062-ARDZ LOW POWER, 16/12-BIT EASY DRIVE SAR ADC</div>			
DECIMALS	FRACTIONS	TEST ENGINEER	08AUG23				
.XX --.010	--1/32	COMPONENT ENGINEER	08AUG23				
.XXX --.005	--	ADGT LIBRARY	08AUG23				
.XXXX --.0050	--	TEST PROCESS	ddMMMyy	SIZE C FSCM NO 24355 DRAWING NUMBER 09-076509 REV B			
MATERIAL		HARDWARE SERVICES	ddMMMyy				
		HARDWARE SYSTEMS	ddMMMyy				
		HARDWARE RELEASE	ddMMMyy				
FINISH		DESIGNER K. TRAN	08AUG23	SCALE 1 / 1 SHEET 1 OF 1			
		PTD ENGINEER T. SCHMITT	08AUG23				
		CHECKER	ddMMMyy				
DO NOT SCALE DWG							

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	04AUG23	TYLER S.
B	CHANGES PER ECR-123370	19DEC24	G. MANN



NOTES: UNLESS OTHERWISE SPECIFIED

- WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
- ASSEMBLY PROCESS SHALL INCLUDE REFLOW SOLDER TOP AND BOTTOM SIDE.
MAXIMUM TEMPERATURE SHALL NOT EXCEED 240 DEGREES CELSIUS.
- OPTIONAL (OR DNI) PARTS ARE SPECIFIED ON THE BILL OF MATERIALS.
LAND PATTERNS FOR THESE PARTS SHALL BE FREE OF SOLDER.
MASK THE SOLDER STENCIL AT THESE LOCATIONS.
- INSTALL SHUNTS ON JUMPERS AS SHOWN ON ASSEMBLY DRAWING.
- DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON ALL BOARD EDGES.
- DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP
- INSTALL STANDOFFS AS SHOWN BELOW:

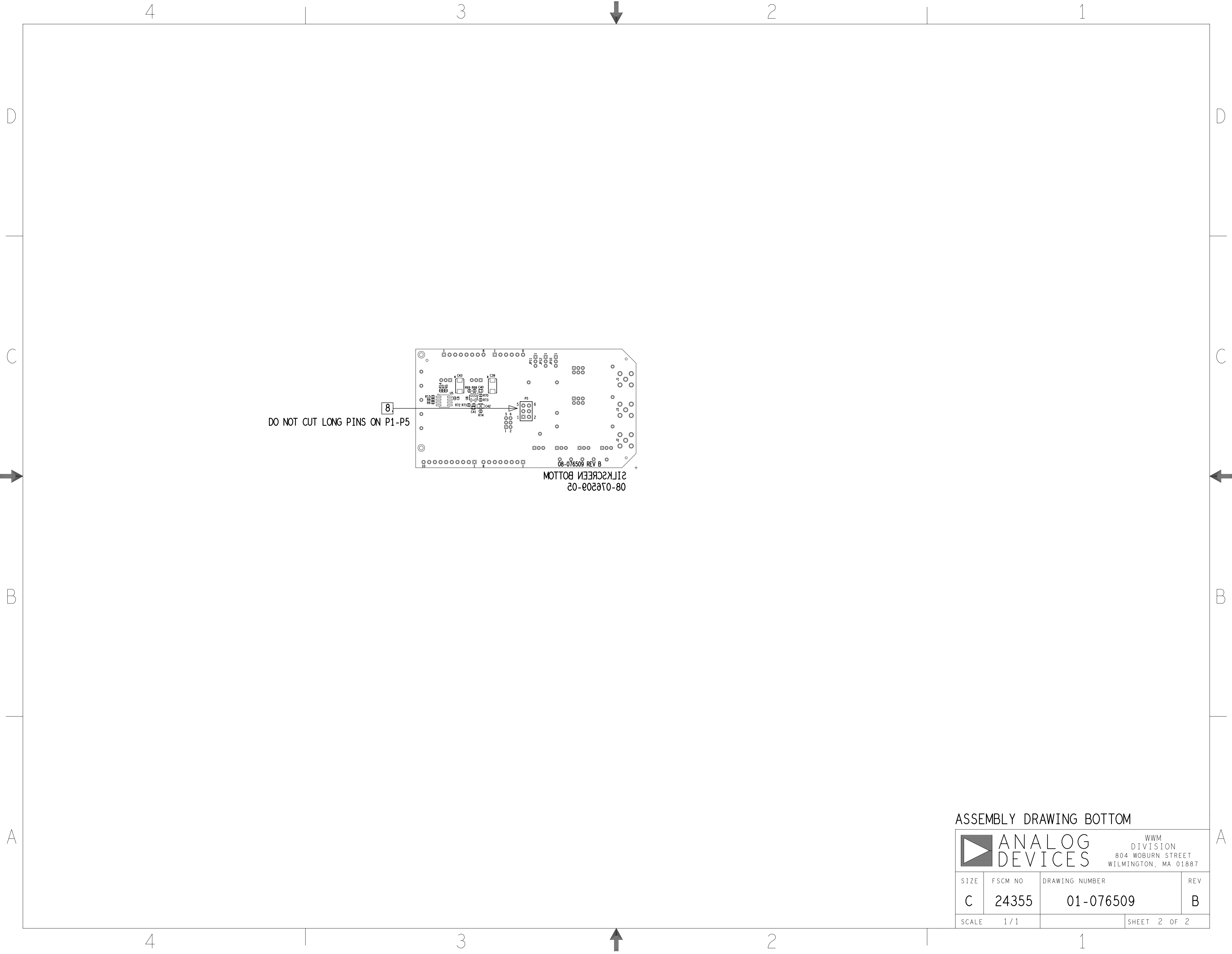


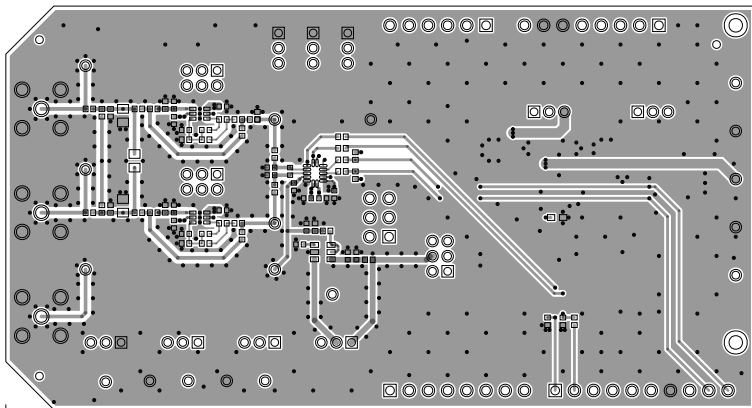
8. P1-P5 HAVE LONG PINS AFTER INSTALLED. DO NOT CUT ANY OF THESE PINS.
9. USE BLACK PERMANENT MARKER TO WRITE ON THE WHITE BLOCK AREAS AS SHOWN BELOW:

MODEL NAME	-BIT	VARIANTS	SPEC
EVAL-AD406 2 -ARDZ	16	01	C-11598-A
EVAL-AD406 0 -ARDZ	12	02	C-11800-A

10. RoHS COMPLIANCE: ASSEMBLY VENDOR SHOULD ASSURE COMPLIANCE WITH LEAD-FREE AND AND RoHS PCB ASSEMBLY STANDARDS (EU RoHS DIRECTIVE 2002/95/EC).

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES			APPROVAL		DATE		<div><div></div><div>ANALOG DEVICES</div></div> <div>WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887</div> <div>TITLE ASSEMBLY DRAWING TOP EVALAD4062-ARDZ LOW POWER, 16/12-BIT EASY DRIVE SAR ADC</div>							
TOLERANCES			TEMPLATE ENGINEER X		ddMMMy y									
DECIMALS FRACTIONS ANGLES .XX -.010 --1/32 -- 2 .XXX -.005 .XXXX -.0050			HARDWARE SERVICES X		ddMMMy y									
			HARDWARE SYSTEMS X		ddMMMy y									
MATERIAL			TEST ENGINEER X		ddMMMy y		SIZE		FSCM NO		DRAWING NUMBER		REV	
			COMPONENT ENGINEER ADGT LIBRARY		08AUG23									
			TEST PROCESS X		ddMMMy y									
			HARDWARE RELEASE X		ddMMMy y									
FINISH			DESIGNER X K. TRAN		08AUG23		C		24355		01-076509		B	
			PTD ENGINEER X T. SCHMITT		08AUG23									
			CHECKER X		ddMMMy y									
DO NOT SCALE DWG							SCALE		1 / 1				SHEET 1 OF 2	





L1 TOP

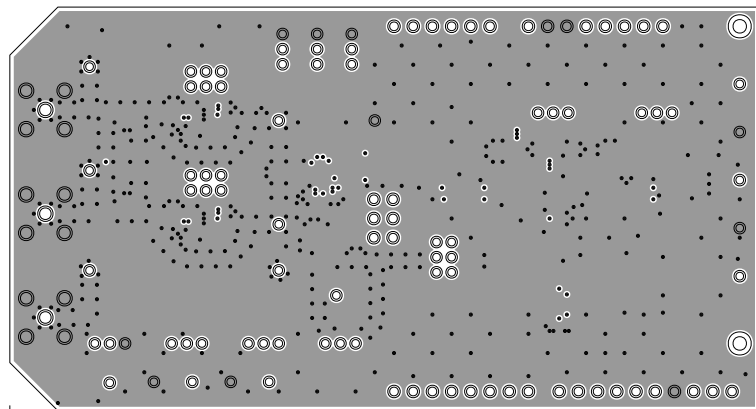
DATE: 19-DEC-24

08-076509-01

EVAL-AD4062/60-ARDZ

REV B

LOW POWER, 16/12-BIT EASY DRIVE SAR ADC



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L2 GND

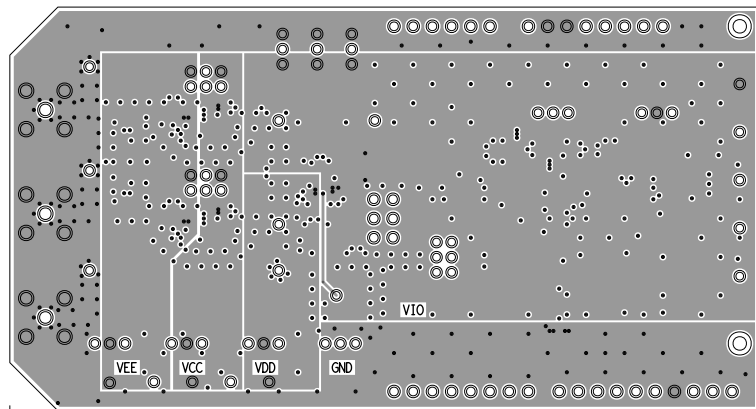
DATE: 19-DEC-24

08-076509-07

EVAL-AD4062/60-ARDZ

REV B

LOW POWER, 16/12-BIT EASY DRIVE SAR ADC



L3 PWR

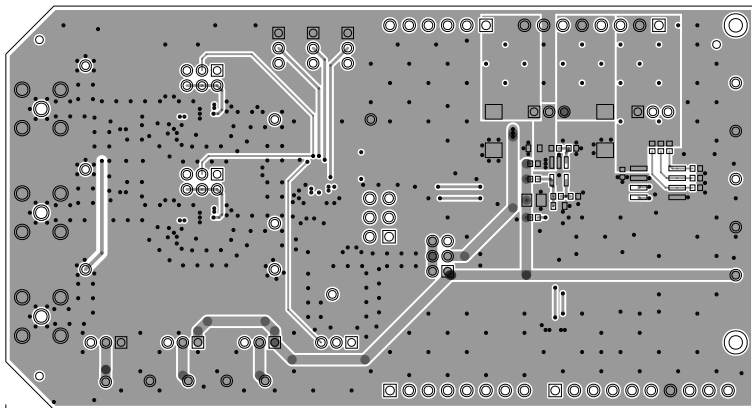
DATE: 19-DEC-24

08-076509-10

EVAL-AD4062/60-ARDZ

REV B

LOW POWER, 16/12-BIT EASY DRIVE SAR ADC



L4 BOTTOM

DATE: 19-DEC-24

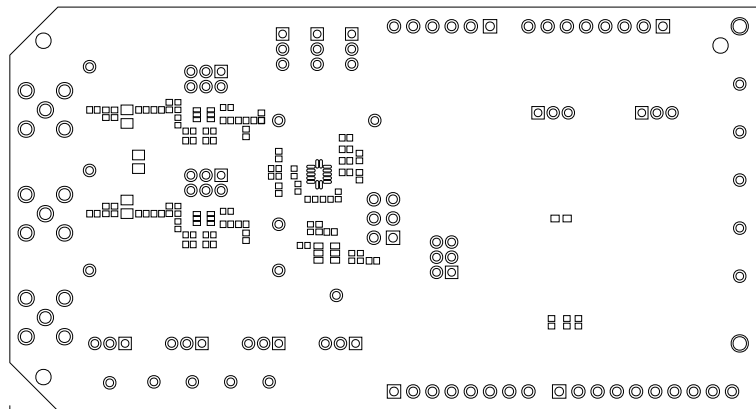
08-076509-02

EVAL-AD4062/60-ARDZ

REV B

LOW POWER, 16/12-BIT EASY DRIVE SAR ADC

REV B



SOLDERMASK TOP

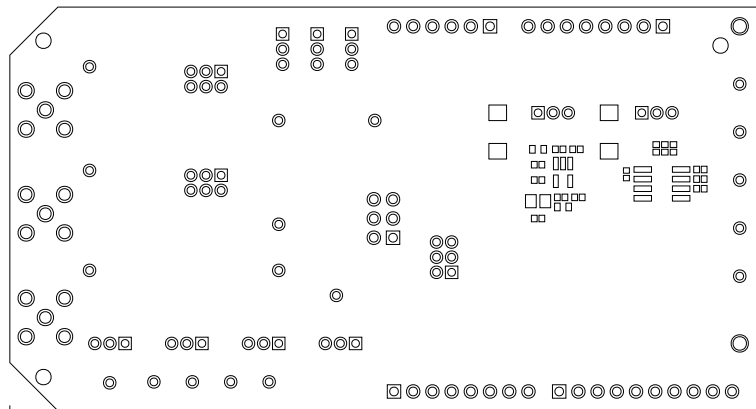
DATE: 19-DEC-24

08-076509-04

EVAL-AD4062/60-ARDZ

REV B

LOW POWER, 16/12-BIT EASY DRIVE SAR ADC



SOLDERMASK BOTTOM

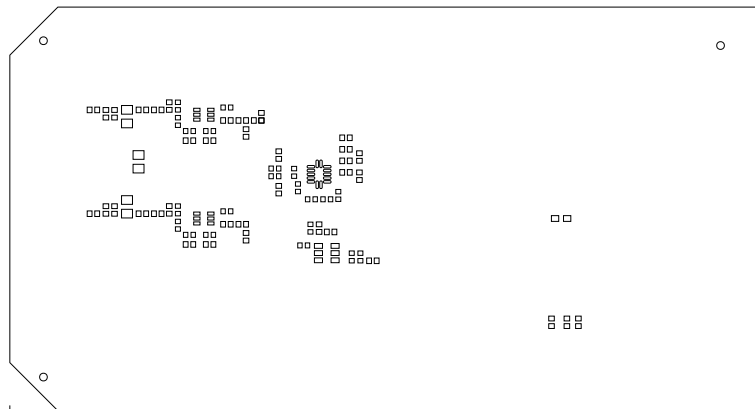
DATE: 19-DEC-24

08-076509-06

EVAL-AD4062/60-ARDZ

REV B

LOW POWER, 16/12-BIT EASY DRIVE SAR ADC



PASTEMASK TOP

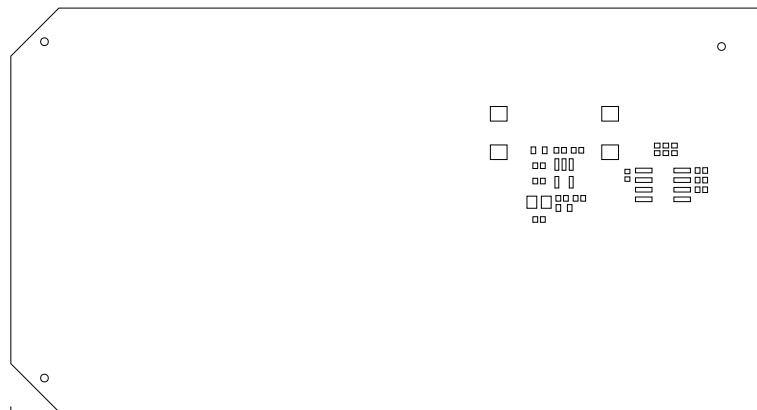
DATE: 19-DEC-24

08-076509-13

EVAL-AD4062/60-ARDZ

REV B

LOW POWER, 16/12-BIT EASY DRIVE SAR ADC



+

PASTEMASK BOTTOM

DATE: 19-DEC-24

08-076509-14

EVAL-AD4062/60-ARDZ

REV B

LOW POWER, 16/12-BIT EASY DRIVE SAR ADC