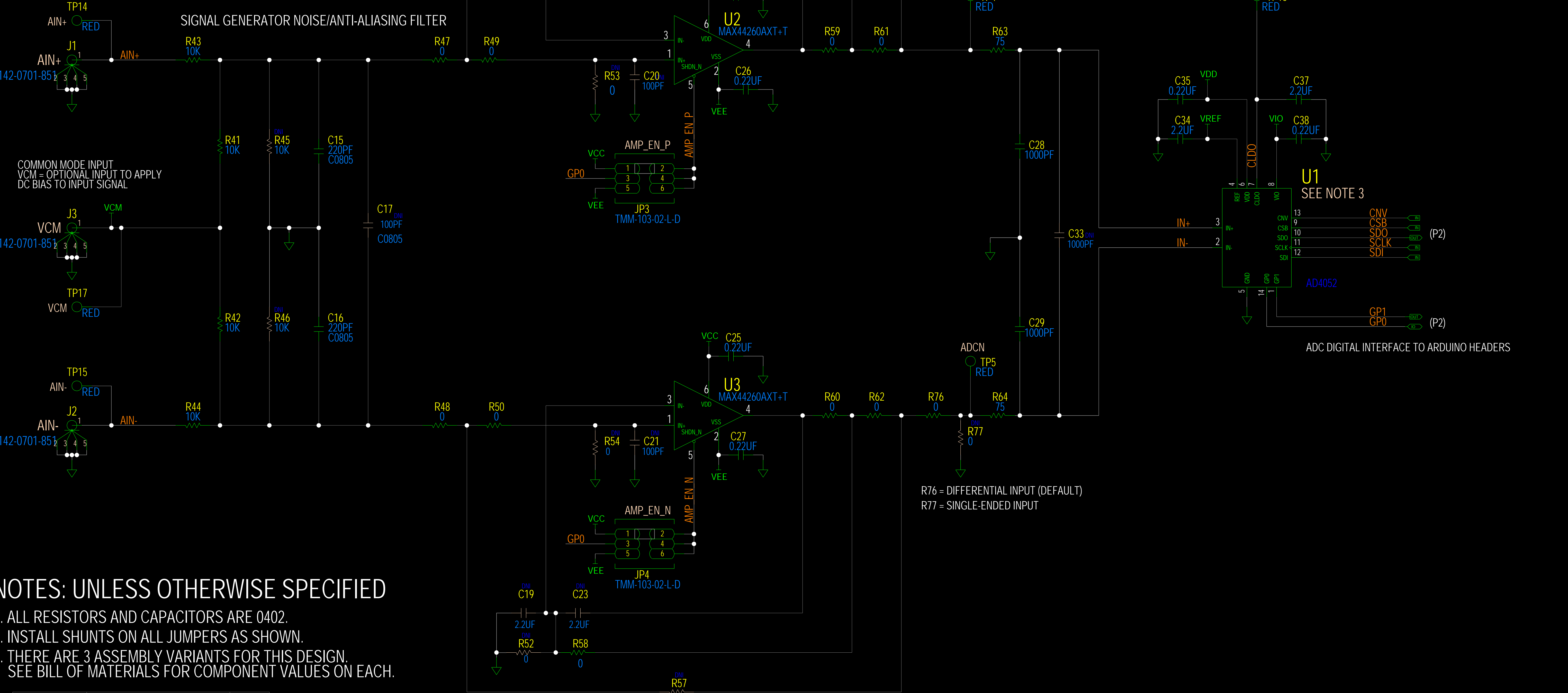


ANALOG SIGNAL PATH

ADC

ANALOG SIGNAL INPUTS



- NOTES: UNLESS OTHERWISE SPECIFIED
- 1. ALL RESISTORS AND CAPACITORS ARE 0402.
 - 2. INSTALL SHUNTS ON ALL JUMPERS AS SHOWN.
 - 3. THERE ARE 3 ASSEMBLY VARIANTS FOR THIS DESIGN.
SEE BILL OF MATERIALS FOR COMPONENT VALUES ON EACH.

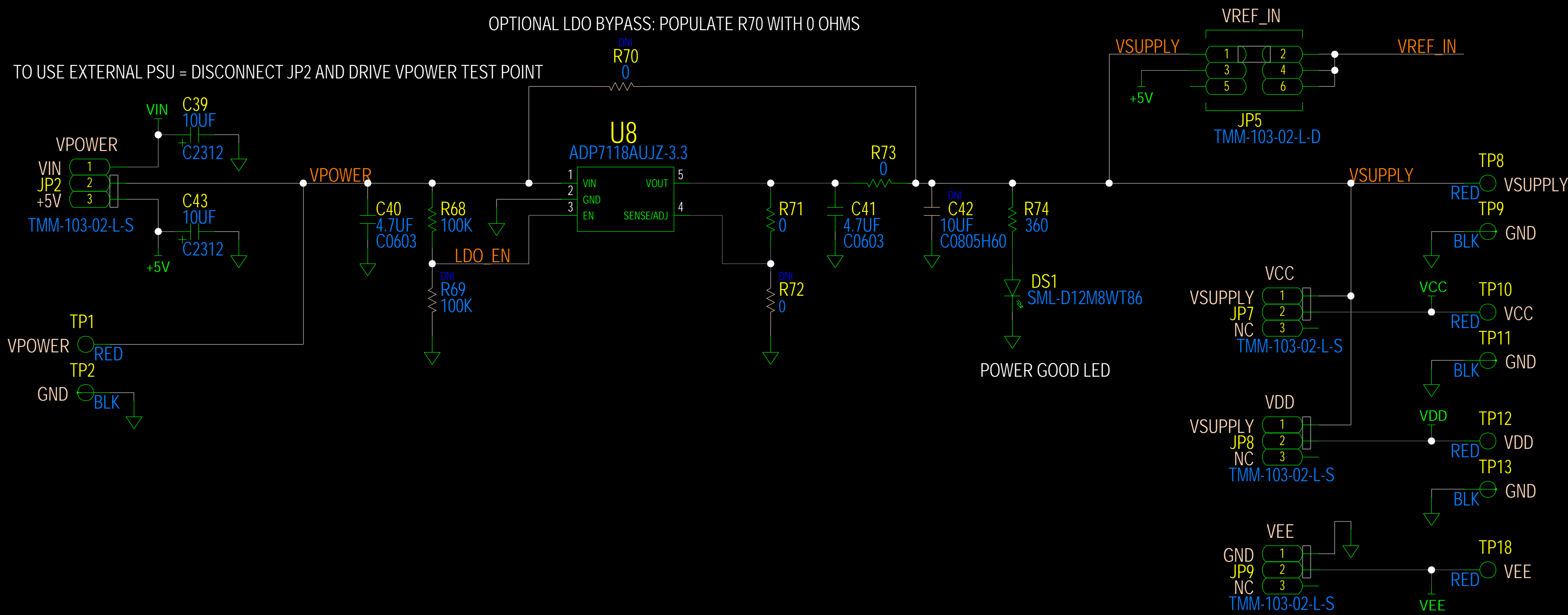
VARIANTS	PRODUCT GENERIC	BIT
01	AD4052BCPZ	16

SCHEMATIC				
DESIGN VIEW KIM T.		DRAWING NO. 02-072604-01		REV D
PTD ENGINEER TYLER S.		SIZE D	SCALE 1:1	SHEET 2 OF 3

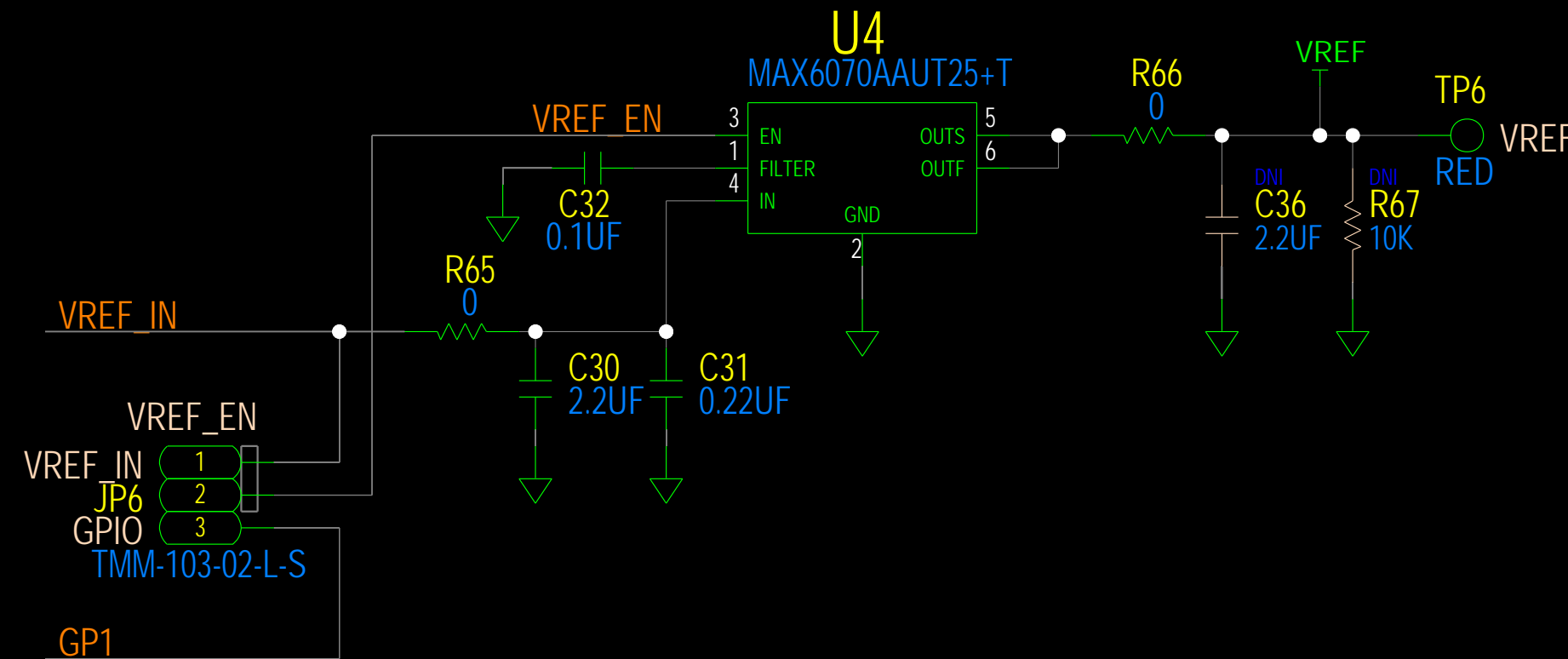
POWER SUPPLY

OPTIONAL LDO BYPASS: POPULATE R70 WITH 0 OHMS

TO USE EXTERNAL PSU = DISCONNECT JP2 AND DRIVE VPOWER TEST POINT

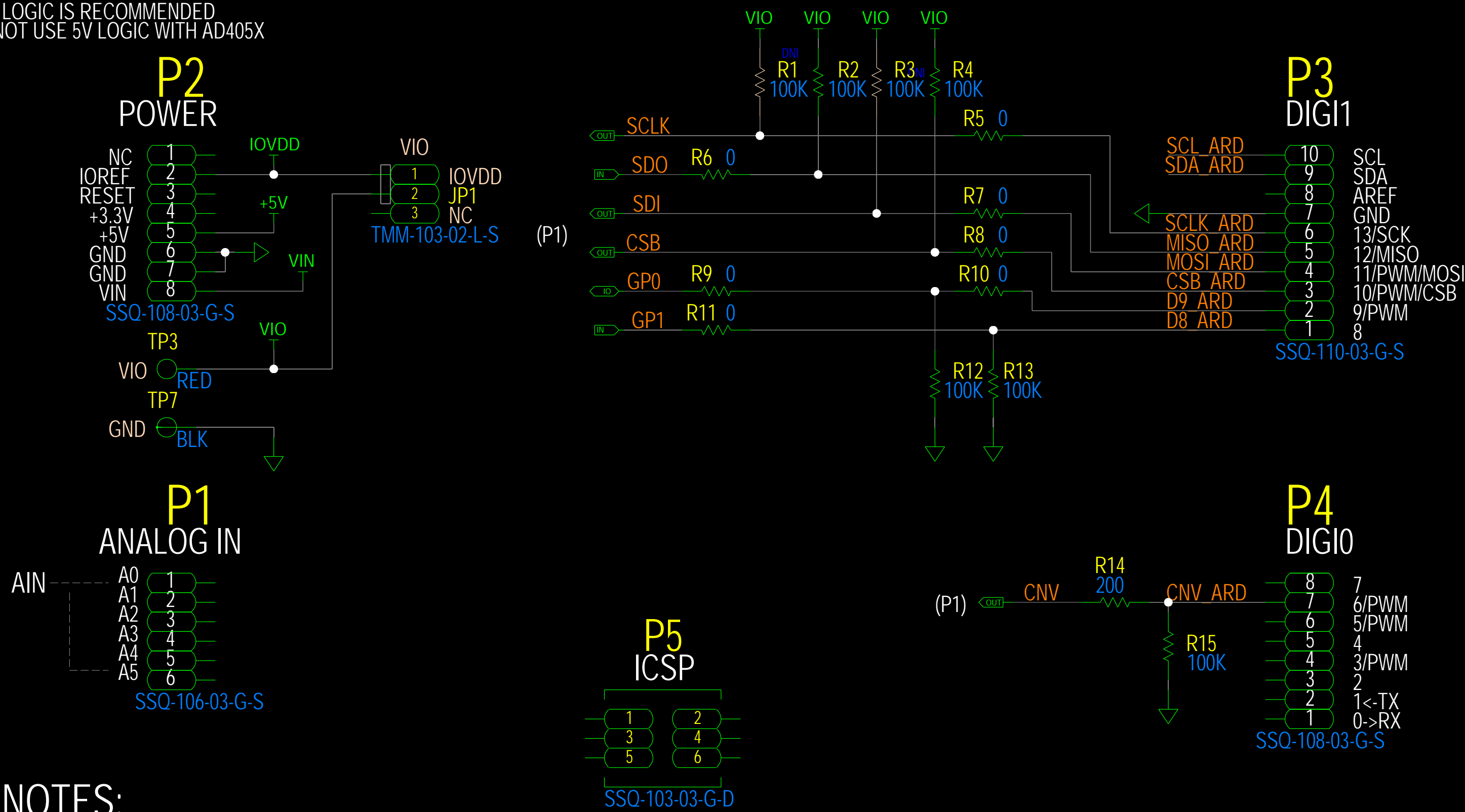


VOLTAGE REFERENCE



ARDUINO UNO SHIELD

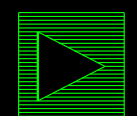
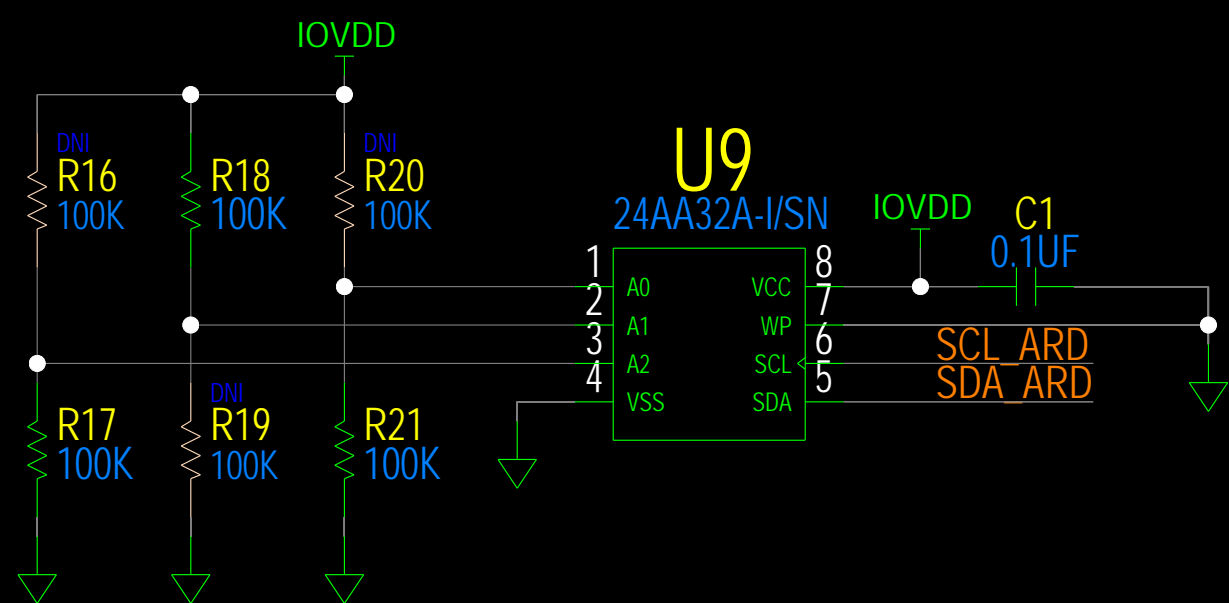
BIDIRECTIONAL HOST SUPPLY -->
VIN RANGE = 7V TO 12V
AD405X SUPPORTS 1.8V AND 3.3V LOGIC -->
3.3V LOGIC IS RECOMMENDED
DO NOT USE 5V LOGIC WITH AD405X



NOTES:

- P5, ICSP, NOT SUPPORTED. PROVIDED FOR PASS-THROUGH TO OTHER SHIELDS ONLY
- INSTALL P1 - P4 ON TOP SIDE AND P5 ON BOTTOM SIDE. DO NOT CUT ANY PIN OF THESE COMPONENTS AFTER ASSEMBLY.

EEPROM



ANALOG
DEVICES

SCHEMATIC

DESIGNED BY: KIM T.
CHECKED BY: TYLER S.
DATE: 02-07-2004-01

DESIGN VIEW
KIM T.

PTD ENGINEER
TYLER S.

DRAWING NO.
02-072604-01

REV
D

SIZE D SCALE 1:1 SHEET 3 OF 3