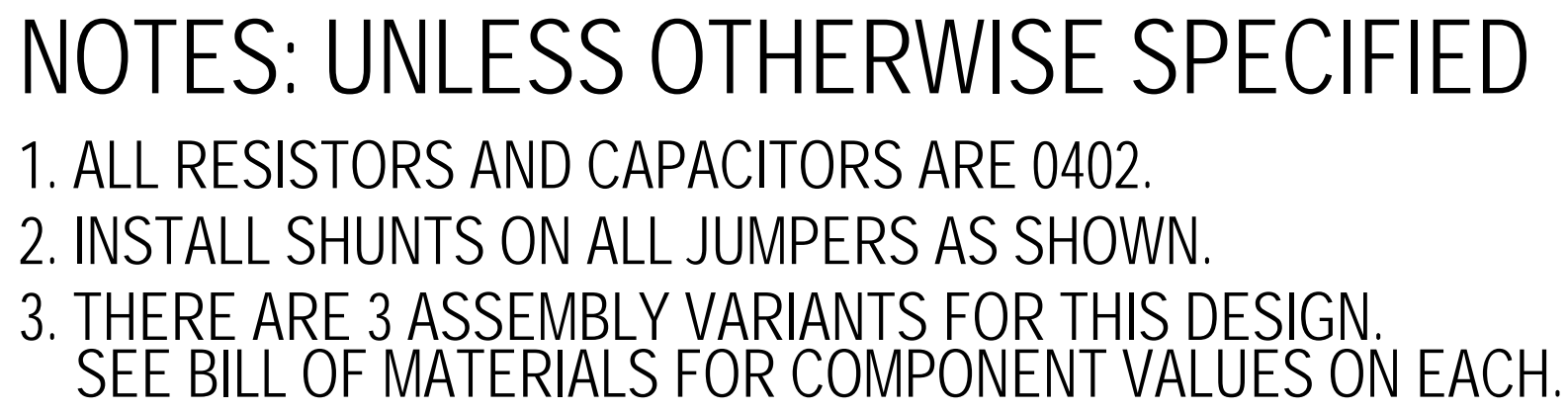



8		7		6		5		4		3		2		1																																														
D	THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.					<table><tr><th colspan="3">JUMPER TABLE</th></tr><tr><th>JP#</th><th>ON</th><th>OFF</th></tr><tr><td>1</td><td></td><td></td></tr><tr><td>2</td><td></td><td></td></tr><tr><td>3</td><td></td><td></td></tr><tr><td>4</td><td></td><td></td></tr><tr><td>5</td><td></td><td></td></tr></table> <p>* SEE ASSEMBLY INSTRUCTIONS</p>			JUMPER TABLE			JP#	ON	OFF	1			2			3			4			5			<table><tr><th colspan="4">REVISIONS</th></tr><tr><th>REV</th><th>DESCRIPTION</th><th>DATE</th><th>APPROVED</th></tr><tr><td>A</td><td>INITIAL RELEASE</td><td>26JUL22</td><td>TYLER S.</td></tr><tr><td>B</td><td>CHANGES PER ECR-115479</td><td>01AUG23</td><td>TYLER S.</td></tr><tr><td>C</td><td>CHANGES PER ECR-118281</td><td>12DEC23</td><td>JEFFRY C.</td></tr><tr><td>D</td><td>CHANGES PER ECR-118947</td><td>24JAN23</td><td>JEFFRY C.</td></tr></table>							REVISIONS				REV	DESCRIPTION	DATE	APPROVED	A	INITIAL RELEASE	26JUL22	TYLER S.	B	CHANGES PER ECR-115479	01AUG23	TYLER S.	C	CHANGES PER ECR-118281	12DEC23	JEFFRY C.	D	CHANGES PER ECR-118947	24JAN23	JEFFRY C.
	JUMPER TABLE																																																											
	JP#	ON	OFF																																																									
	1																																																											
	2																																																											
	3																																																											
	4																																																											
	5																																																											
	REVISIONS																																																											
	REV	DESCRIPTION	DATE	APPROVED																																																								
A	INITIAL RELEASE	26JUL22	TYLER S.																																																									
B	CHANGES PER ECR-115479	01AUG23	TYLER S.																																																									
C	CHANGES PER ECR-118281	12DEC23	JEFFRY C.																																																									
D	CHANGES PER ECR-118947	24JAN23	JEFFRY C.																																																									
RELAY CONTROL CHART																																																												
CONTROL	CODE	DEVICE	FUNCTION	CONNECTOR																																																								
C																																																												
B																																																												
A																																																												
8		7		6		5		4		3		2		1																																														

# ADC



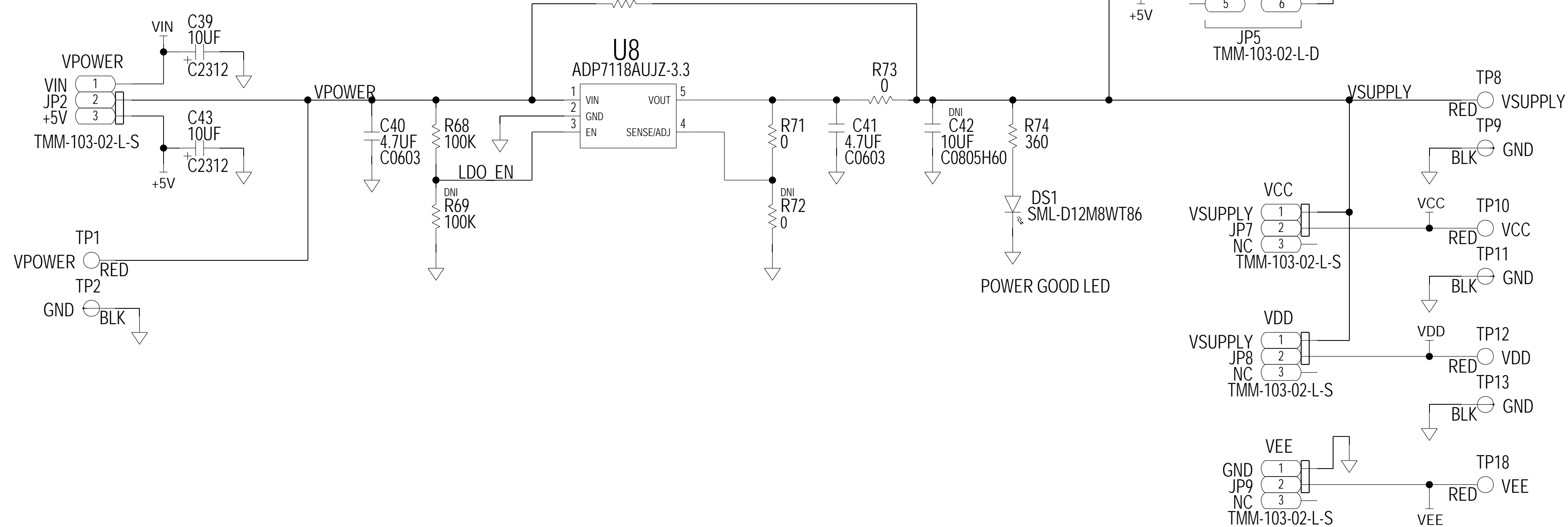
VARIANTS	PRODUCT GENERIC	BIT
01	AD4052BCPZ	16

 <b>ANALOG DEVICES</b>	SCHEMATIC				
<small>             ALL DRAWINGS ARE THE PROPERTY OF ANALOG DEVICES INC.              IT IS NOT TO BE REPRODUCED OR COPIED IN WHOLE OR              IN PART, OR USED IN ANY MANNER, AND EXCEPT TO THE EXTENT              SET FORTH ON THE PARTS LIST, EXCEPT TO THE EXTENT              OF ANALOG DEVICES.              ALL EQUIPMENT SHOULD BE IDENTIFIED BY ANALOG DEVICES PART              NUMBERS OR CONTROLLED BY OWNED ANALOG DEVICES.           </small>	<b>DESIGN VIEW</b> <b>KIM T.</b>	<b>DRAWING NO.</b> <b>02-072604-03</b>		<b>REV</b> <b>D</b>	
	<b>PTD ENGINEER</b> <b>TYLER S.</b>	<b>SIZE</b> <b>D</b>	<b>SCALE</b> <b>1:1</b>	<b>SHEET</b> <b>2</b> <b>OF</b> <b>3</b>	

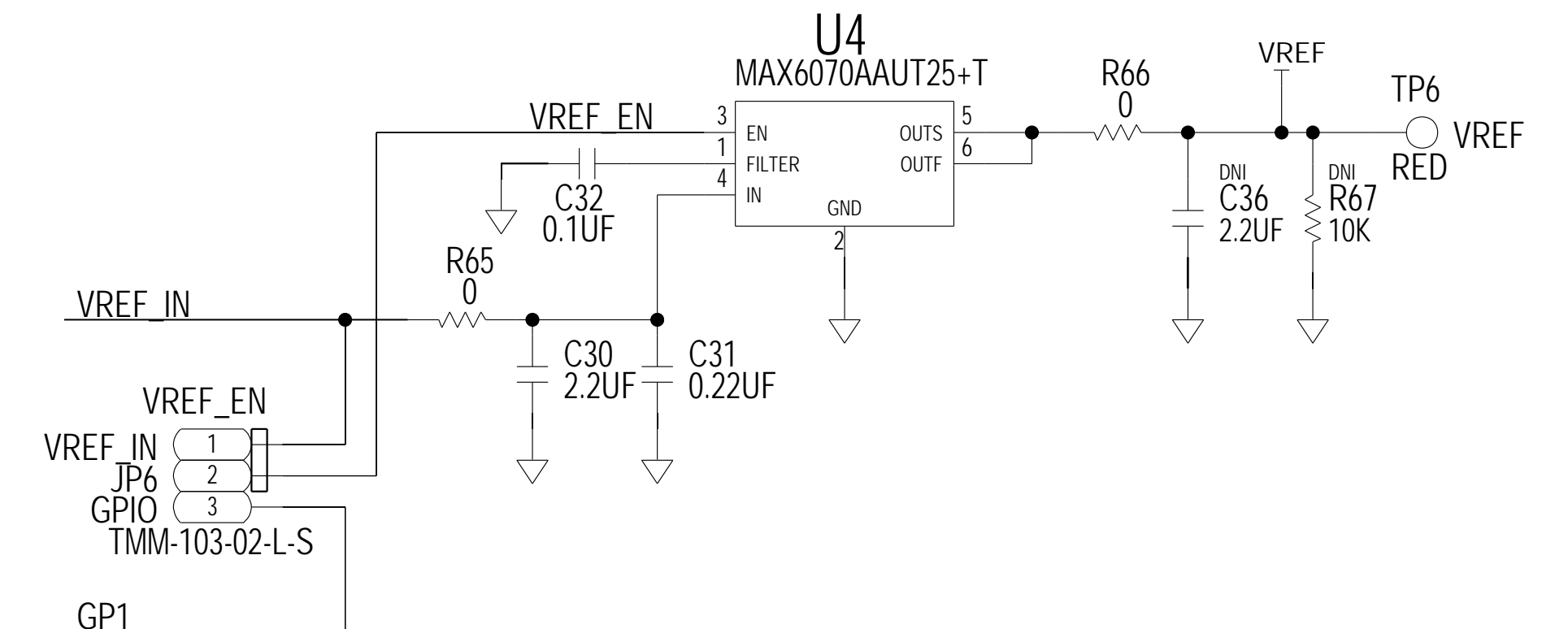
# POWER SUPPLY

OPTIONAL LDO BYPASS: POPULATE R70 WITH 0 OHMS

TO USE EXTERNAL PSU = DISCONNECT JP2 AND DRIVE VPOWER TEST POINT

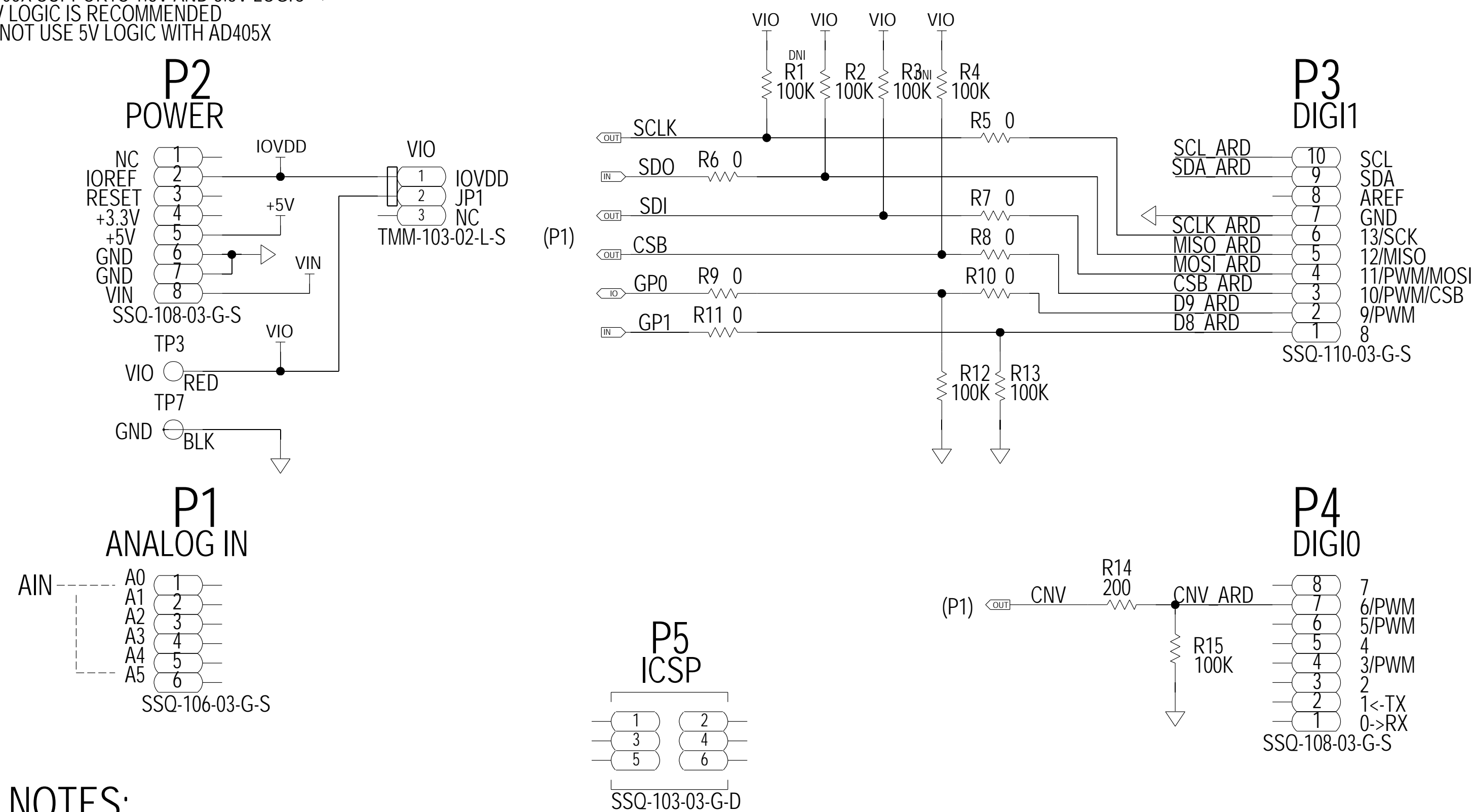


# VOLTAGE REFERENCE

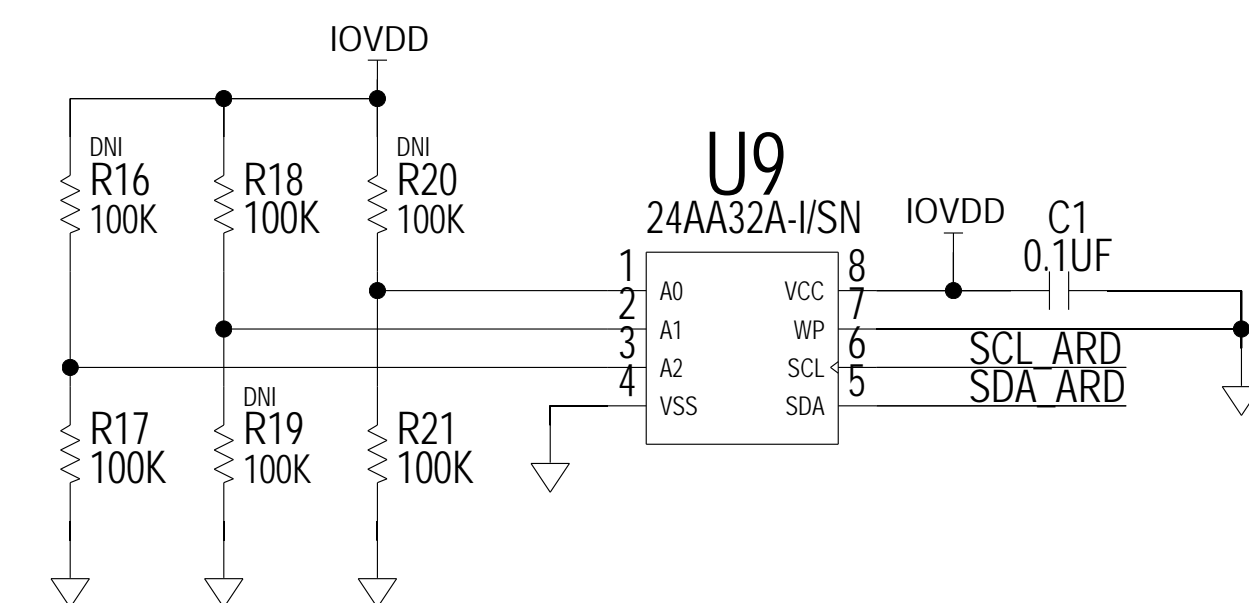


# ARDUINO UNO SHIELD

BIDIRECTIONAL HOST SUPPLY -->  
VIN RANGE = 7V TO 12V  
AD405X SUPPORTS 1.8V AND 3.3V LOGIC -->  
3.3V LOGIC IS RECOMMENDED  
DO NOT USE 5V LOGIC WITH AD405X



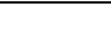
## EEPROM



NOTES:

1. P5, ICSP, NOT SUPPORTED. PROVIDED FOR PASS-THROUGH TO OTHER SHIELDS ONLY
2. INSTALL P1 - P4 ON TOP SIDE AND P5 ON BOTTOM SIDE. DO NOT CUT ANY PIN OF THESE COMPONENTS AFTER ASSEMBLY.

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

 <b>ANALOG DEVICES</b>	SCHEMATIC									
<small>THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED TO DISSEMINATE INFORMATION TO OTHERS, AND FOR ANY OTHER PURPOSES (REFERENCE TO THE AUTHORITY OF ANALOG DEVICES).</small>	DESIGN VIEW KIM T.		DRAWING NO. 02-072604-03				REV D			
	PTD ENGINEER TYLER S.		SIZE D	SCALE 1:1	SHEET 3 OF 3					