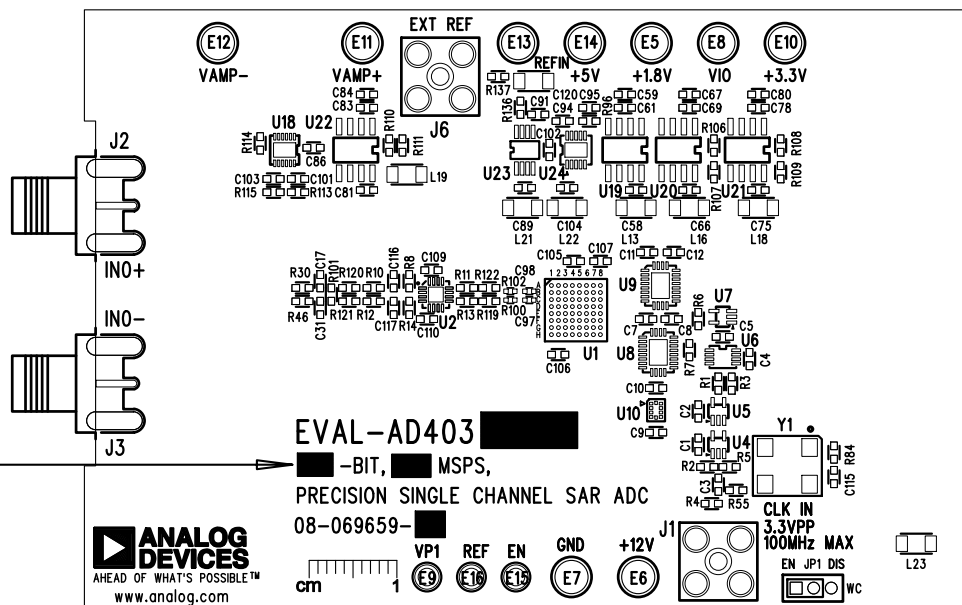


REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	A	PRODUCTION	CLARENCE M.	11-02-21



# 8. EXAMPLE:

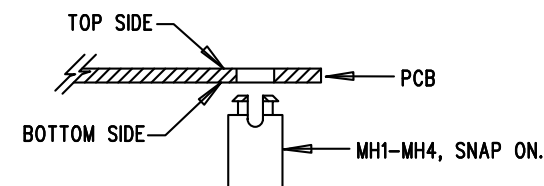
**EVAL-AD4030-24**  
 24-BIT, 2 MSPS,  
 PRECISION SINGLE CHANNEL SAR ADC  
 08-069659-01 (ASSY)


8. MARK EACH SUFFIX, NUMBER OF -BIT AND MSPS IN THE WHITE BLOCK AREAS WITH BLACK PERMANENT MARKER AS SHOWN IN TABLE BELOW:

ASSY	U1	SUFFIX	-BIT	MSPS
01	AD4030-24	30-24	24	2

## NOTES: UNLESS OTHERWISE SPECIFIED

- WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
- ASSEMBLY PROCESS SHALL INCLUDE REFLOW SOLDER TOP AND BOTTOM SIDES. MAXIMUM TEMPERATURE SHALL NOT EXCEED 240 DEGREES CELSIUS.
- OMITTED (OPTIONAL) PARTS ARE SPECIFIED ON THE BILL OF MATERIALS. LAND PATTERNS FOR THESE PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL AT THESE LOCATIONS.
- INSTALL SHUNTS AS SHOWN ON ASSY DRAWING.
- DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON ALL BOARD EDGES.
- DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.
- INSTALL 4 STANDOFFS AT 4 LOCATIONS AS SHOWN BELOW:



APPROVALS		 AHEAD OF WHAT'S POSSIBLE™ 2555 AUGUSTINE DRIVE SANTA CLARA, CA 95054 www.analog.com	
PCB DES.	KIM T.		
APP ENG.	CLARENCE M.	TITLE: TOP ASSEMBLY DRAWING	
		PRECISION SINGLE CHANNEL SAR ADC	
		SIZE	IC NO.
		N/A	AD403X-XXFMCZ FAMILY
		REV.	A
SCALE = NONE		FILENAME:	EVAL-069659-A.PCB
		SHT	1 OF 2