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REVISIONS

REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	23JUL24	I.ANDAL
B	CHANGES AS PER ECR-125997	12JAN26	D.DALISAY

EVAL-AD3532R-ARDZ SCHEMATIC REV (B)

PAGE 2 - POWER CIRCUITRY
PAGE 3 - MAIN DEVICE CIRCUITRY
PAGE 4 - ARDUINO HEADER AND IO CIRCUITRY

P.O SPEC.	BK/BD SPEC.	SOCKET OEM	OEM PART#	HANDLER

TEMPLATE ENGINEER -		
HARDWARE SERVICES M.VALE	26JUN24	
HARDWARE SYSTEMS -		
TEST ENGINEER -		
COMPONENT ENGINEER N. CATALAN	26JUN24	
TEST PROCESS -		
HARDWARE RELEASE K. JABATAN	26JUN24	
DESIGNER D. VILLANUEVA	26JUN24	
PTD ENGINEER I. ANDAL	26JUN24	
CHECKER -		

SCHEMATIC

ANALOG DEVICES

HW TYPE : Customer Evaluation Z
Product(s): AD3532R
: EVAL-AD3532RARDZ
PACKAGE : 32-lead 4 x 4 mm Lead Frame Chip Scale Package
: [LFCSP]
<User Define>
<User Define>
<User Define>

MASTER PROJECT TEMPLATE	TESTER TEMPLATE
TBD	-

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES
DECIMALS X.XX +-0.010 X.XXX +-0.005
FRACTIONS +-1/32
ANGLES +-2

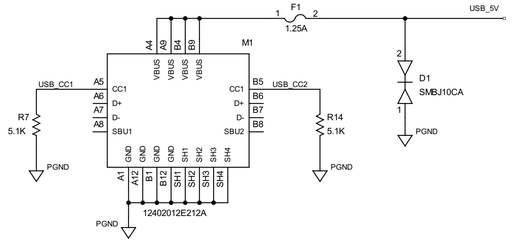
DRAWING NO. 02_083449

REV. B

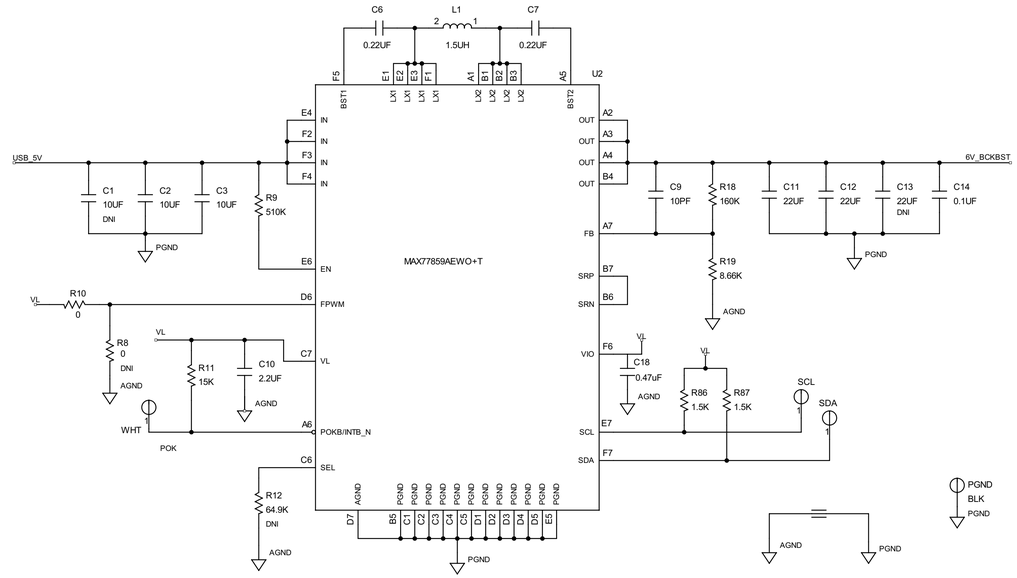
SIZE	SCALE	CODE ID NO.	SHEET
D	1:1	CodeID	SHEET 1 OF 4

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

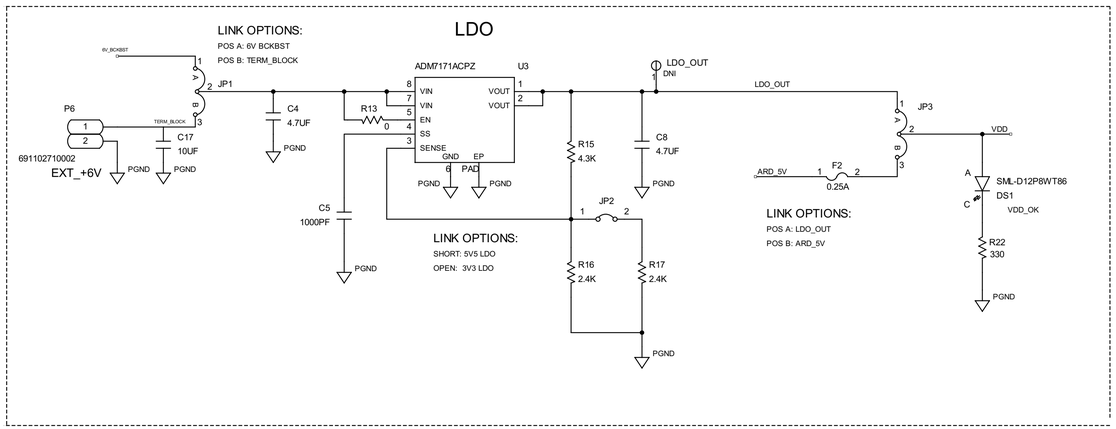
USB-C CONTROLLER



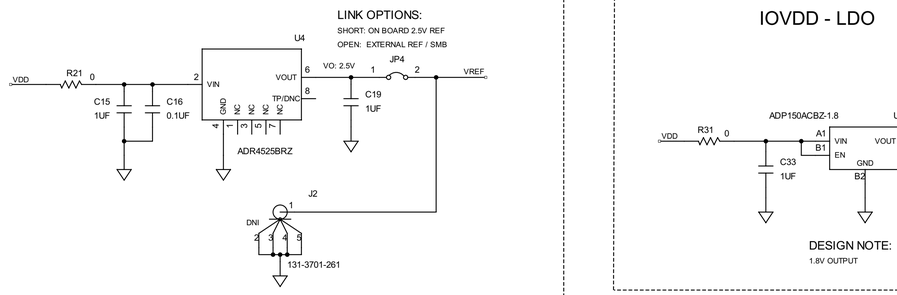
BUCKBOOST



LDO



PRECISION REFERENCE

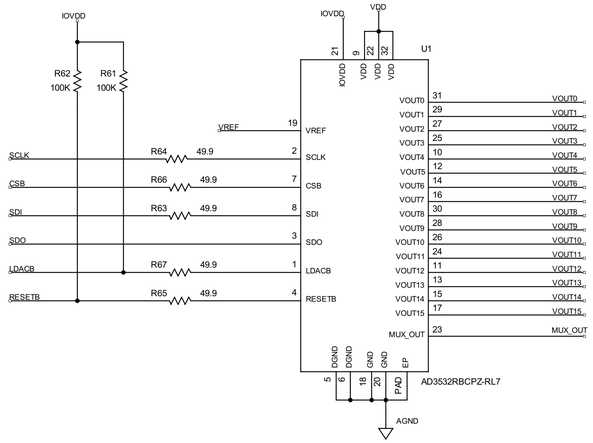


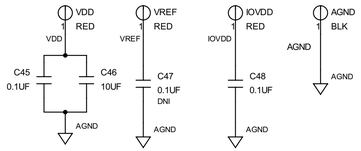
IOVDD - LDO

ANALOG DEVICES	SCHEMATIC		
	HW TYPE : Customer Evaluation Z Product(s): AD3532R : EVAL-AD3532RARDZ		
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	PTD ENGINEER: I. ANDAL	SIZE: D	SCALE: 1:1
		SHEET 2 OF 4	

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

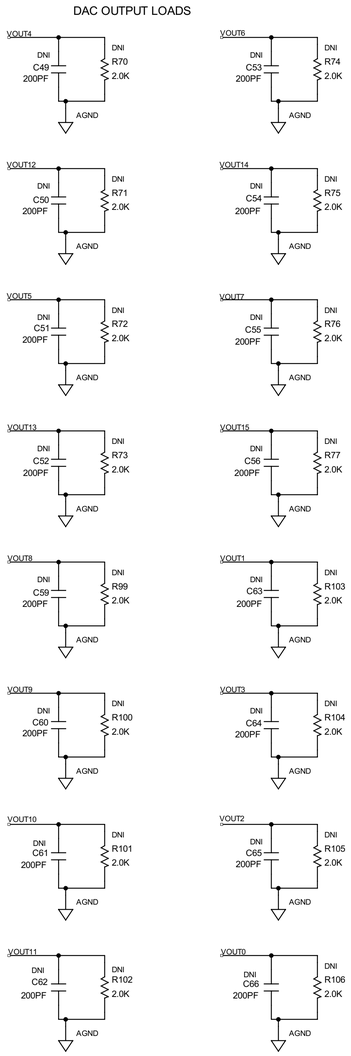
MAIN CIRCUIT





DESIGN NOTE:
PLACE CAPACITORS CLOSE TO U1 PINS

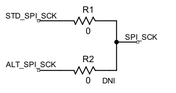
DAC OUTPUT INTERFACE

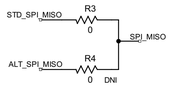


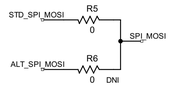
ANALOG DEVICES	SCHEMATIC		
	HW TYPE : Customer Evaluation Z Product(s): AD3532R : EVAL-AD3532RARDZ		
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	PTD ENGINEER I. ANDAL	SIZE D	SCALE 1:1
		SHEET 3 OF 4	

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

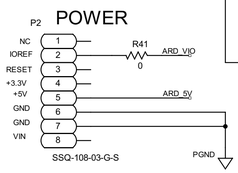
ARDUINO HEADERS

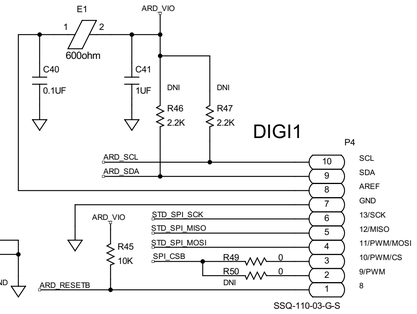




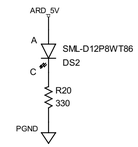


DESIGN NOTE:
SDPK1 IOREF SHOULD BE SET TO 3.3V

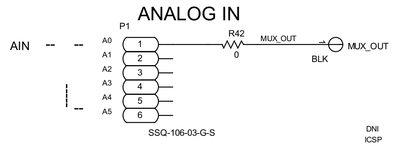


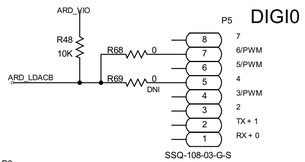


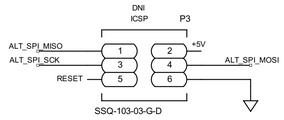
DESIGN NOTE:
AREF IS THE REFERENCE VOLTAGE FOR THE ANALOG LINES A0 TO A5
AREF VOLTAGE LIMITS (SDP-K1) 1.7 <= AREF <= IOREF



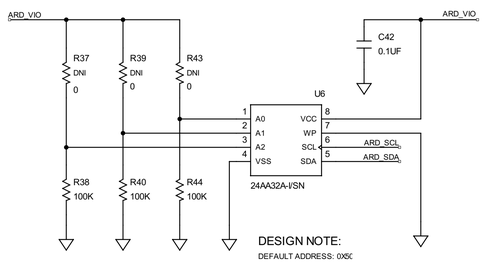
ANALOG IN



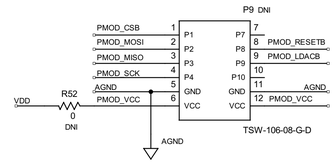




EEPROM - BOARD ID

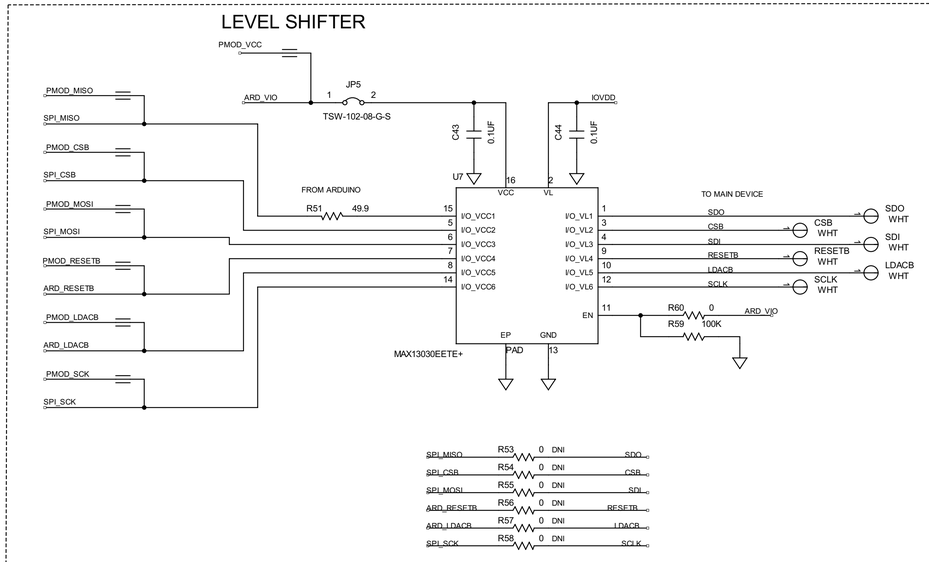


DIGITAL I/F HEADERS



12-PIN SPI PMOD CONNECTOR

LEVEL SHIFTER



ANALOG DEVICES	SCHEMATIC		
	HW TYPE : Customer Evaluation Z Product(s): AD3532R : EVAL-AD3532RARDZ		
	DESIGN VIEW: $DESIGN_VIEW	DRAWING NO.: 02_083449	REV: B
	PTD ENGINEER: I. ANDAL	SIZE: D / SCALE: 1:1	SHEET 4 OF 4

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