THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS
NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR
USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER
PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES.
THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS
OWNED OR CONTROLLED BY ANALOG DEVICES.

REVISIONS

REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	09OCT24	I.ANDAL

EVAL-AD3531R-ARDZ SCHEMATIC REV (A)

PAGE 2 - POWER CIRCUITRY

PAGE 3 - MAIN DEVICE CIRCUITRY

PAGE 4 - ARDUINO HEADER AND IO CIRCUITRY

P.O SPEC.	BK/BD SPEC.	SOCKET OEM	OEM PART#	HANDLER

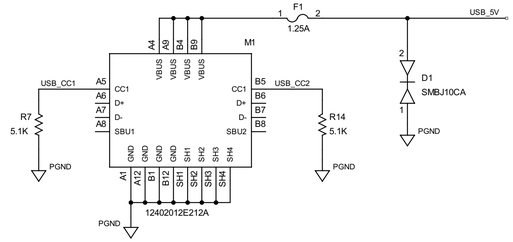
TEMPLATE ENGINEER -	DATE	SCHEMATIC
HARDWARE SERVICES M.VALE	09OCT24	HW TYPE : Customer Evaluation Z Product(s): AD3531R : EVAL-AD3531R-ARDZ PACKAGE : LeadCount-lead BodySize PackageType-family : Pitch-pitch StyleVendor Style <User Define> <User Define> <User Define>
HARDWARE SYSTEMS -		
TEST ENGINEER -		
COMPONENT ENGINEER K.EMPENSANDO	13SEP24	
TEST PROCESS -		
HARDWARE RELEASE K.JABATAN	09OCT24	
DESIGNER J.PANGANIBAN	09OCT24	MASTER PROJECT TEMPLATE: TBD; TESTER TEMPLATE: 064489_c
PTD ENGINEER I.ANDAL	09OCT24	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES DECIMALS X.XX +-0.010 X.XXX +-0.005 FRACTIONS +-1/32 ANGLES +-2
CHECKER -		

DRAWING NO. 02_084207 | REV. A

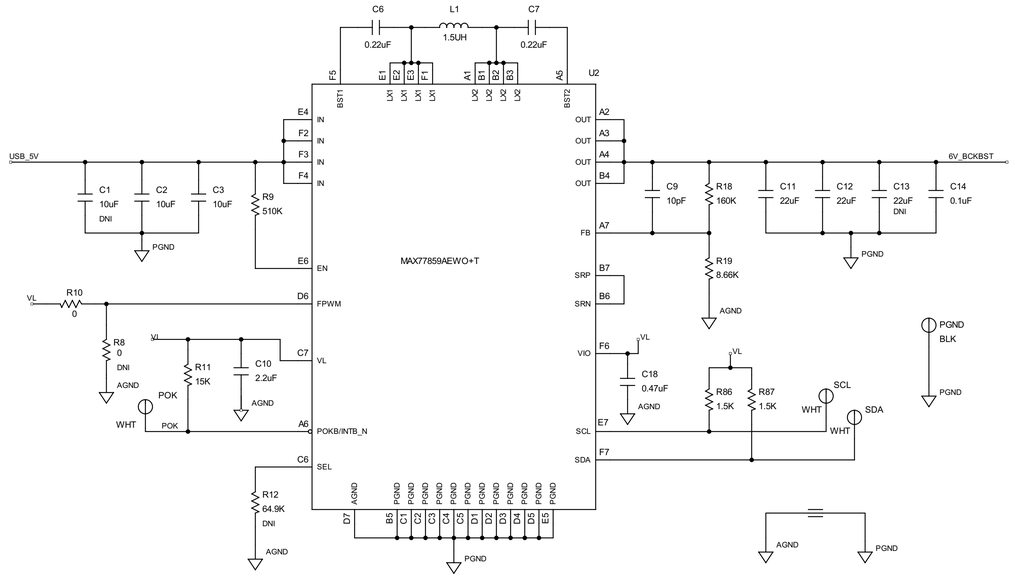
SIZE D | SCALE 1:1 | CODE ID NO. CodeID | SHEET 1 OF 4

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

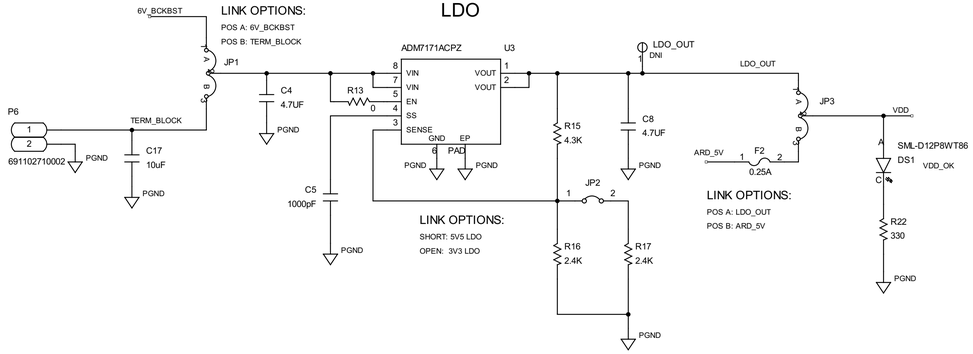
USB-C CONNECTOR



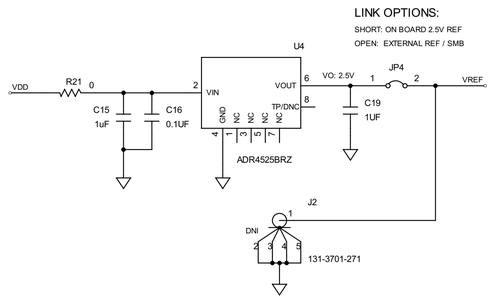
BUCKBOOST



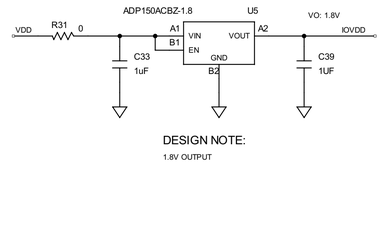
LDO



PRECISION REFERENCE



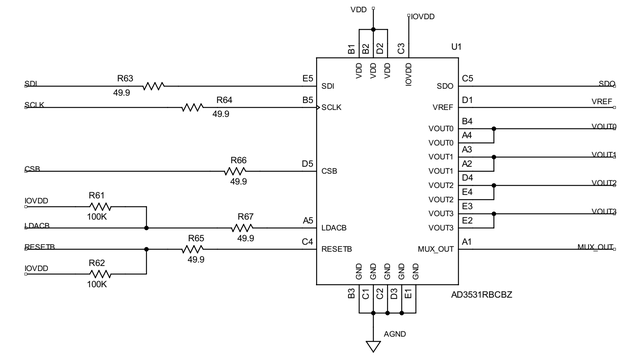
IOVDD - LDO

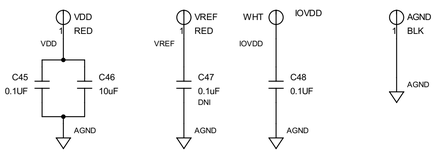


ANALOG DEVICES	SCHEMATIC			
	HW TYPE : Customer Evaluation Z Product(s): AD3531R : EVAL-AD3531R-ARDZ			
THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN PURCHASING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THIS DESIGN IS NOT TO BE USED IN ANY DESIGN WHICH MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY OWNERS ANALOG DEVICES.	DESIGN VIEW $DESIGN_VIEW	DRAWING NO. 02_084207	REV A	
	PTD ENGINEER I.ANDAL	SIZE D	SCALE 1:1	SHEET 2 OF 4

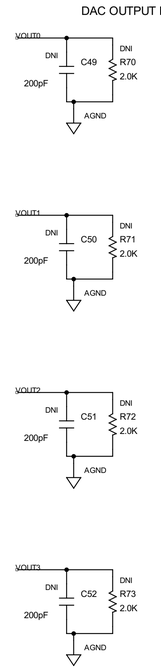
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

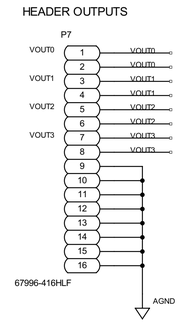
MAIN CIRCUIT

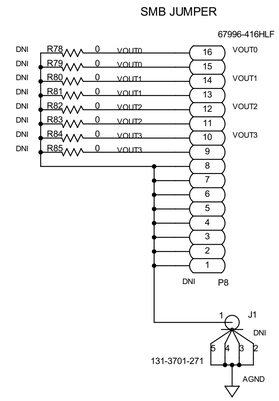




DAC OUTPUT INTERFACE



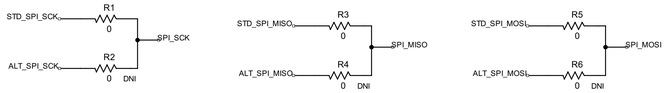


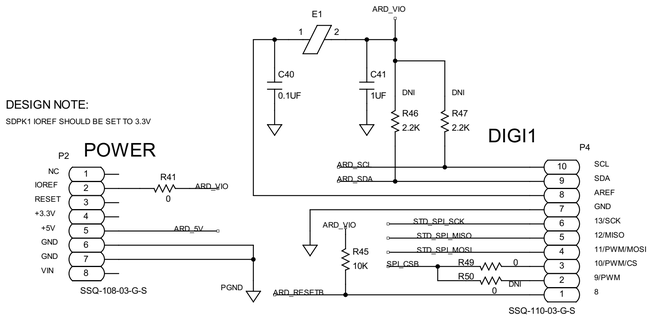


ANALOG DEVICES	SCHEMATIC		
	HW TYPE : Customer Evaluation Z Product(s): AD3531R : EVAL-AD3531R-ARDZ		
THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN AUTHORIZING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THIS DRAWING MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY OWNED ANALOG DEVICES.	DESIGN VIEW $DESIGN_VIEW	DRAWING NO. 02_084207	REV A
	PTD ENGINEER I:ANDAL	SIZE D	SCALE 1:1
		SHEET 3 OF 4	

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

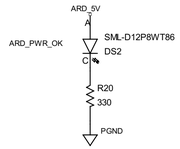
ARDUINO HEADERS

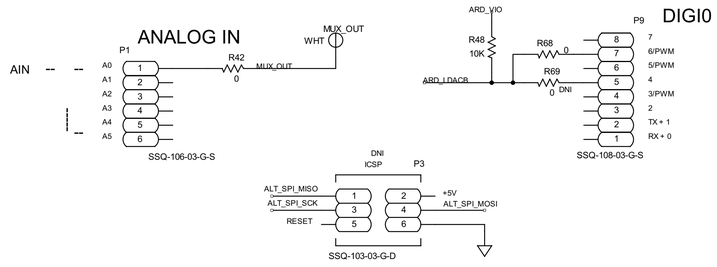




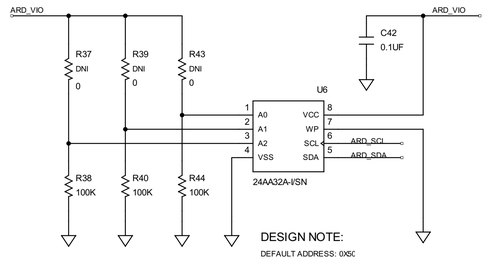
DESIGN NOTE:

AREF IS THE REFERENCE VOLTAGE FOR THE ANALOG LINES A0 TO A5
AREF VOLTAGE LIMITS (SDP-K1) 1.7 <= AREF <= IOREF

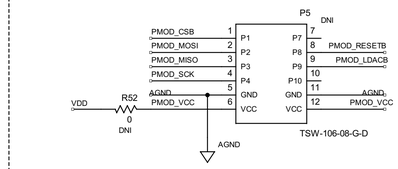




EEPROM - BOARD ID

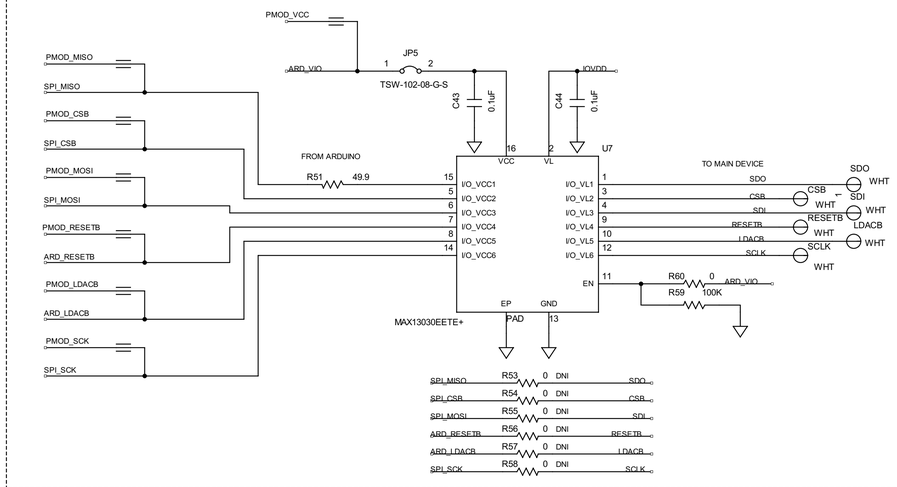


DIGITAL I/F HEADERS



12-PIN SPI PMOD CONNECTOR

LEVEL SHIFTER



ANALOG DEVICES	SCHEMATIC		
	HW TYPE : Customer Evaluation Z Product(s): AD3531R : EVAL-AD3531R-ARDZ		
THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN AUTHORISING INSTRUMENTS TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THIS EQUIPMENT MICHIN HORIZON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY OWNED ANALOG DEVICES	DESIGN VIEW: $DESIGN_VIEW	DRAWING NO.: 02_084207	REV: A
	PTD ENGINEER: I.ANDAL	SIZE: D / SCALE: 1:1	SHEET 4 OF 4