REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	09OCT24	I.ANDAL

ASSEMBLY NOTES:

1. BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).

2. REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.

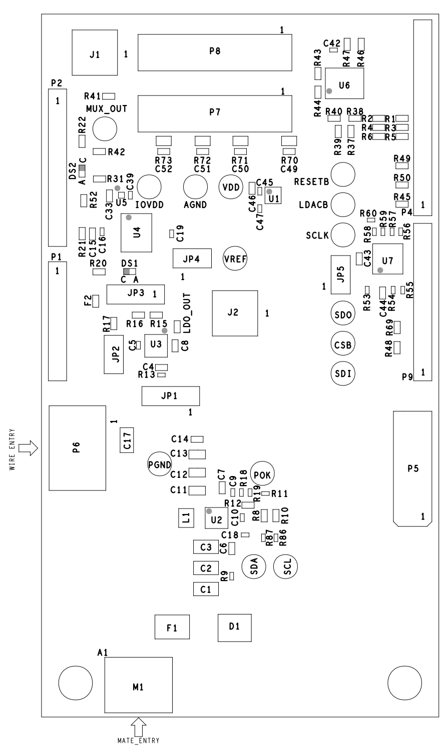
3. REPAIRS ARE NOT ALLOWED IN SOLDERMASK FREE AREAS ON EITHER SIDE OF THE BOARD.

4. RoHS COMPLIANCE: ASSEMBLY VENDOR SHOULD ASSURE COMPLIANCE WITH LEAD-FREE AND RoHS PCB ASSEMBLY STANDARDS (EU RoHS DIRECTIVE 2002/95/EC).

5. BOARDS TO BE SHIPPED SINGULATED AFTER ASSEMBLY PROCESS. SMOOTHEN EDGES AND FREE FROM BURRS AFTER DEPANELIZATION PROCESS.

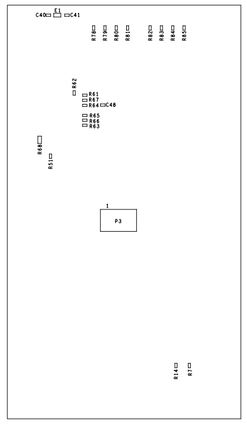
6. DO NOT TRIM EXCESS TAIL LENGTH:
P1, P2, P4 & P9

7. JUMPER SHUNT (MPN: SNT-100-BK-G-H) DEFAULT SETTINGS:
SHORT: JP1: (PIN1 & PIN2), JP3: (PIN1 & PIN2)
SHORT: JP2 , JP5
OPEN: JP4
NOTE: INCLUDE THE UNINSTALLED SHUNTS DURING SHIPMENT



PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN MM	APPROVAL	DATE		
TOLERANCES: DECIMALS .XX +-.010, .XXX +-.005, .XXXX +-.0050; FRACTIONS +-1/32; ANGLES +- 2	TEMPLATE ENGINEER		ANALOG DEVICES	WWM DIVISION, 804 WOBURN STREET, WILMINGTON, MA 01887
	HARDWARE SERVICES M.VALE	09OCT24		
	HARDWARE SYSTEMS		TITLE	ASSEMBLY EVAL-AD3531RARDZ CUSTOMER EVAL Z
MATERIAL	TEST ENGINEER			
	COMPONENT ENGINEER K.EMPENSANDO	13SEP24		
	TEST PROCESS			
	HARDWARE RELEASE K.JABATAN	09OCT24		
FINISH	DESIGNER J.PANGANIBAN	09OCT24	SIZE C / FSCM NO 24355	DRAWING NUMBER 01-084207 / REV A
	PTD ENGINEER I.ANDAL	09OCT24		
	CHECKER			
DO NOT SCALE DWG			SCALE 1/1	SHEET 1 OF 2



SECONDARY SIDE

ANALOG DEVICES
WWM
DIVISION
804 WOBURN STREET
WILMINGTON, MA 01887

SIZE	FSCM NO	DRAWING NUMBER	REV
C	24355	01-084207	A
SCALE 1/1			SHEET 2 OF 2