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REVISIONS

| REV | DESCRIPTION | DATE | APPROVED |
|---|---|---|---|
| A | INITIAL RELEASE | 03APR23 | I.ANDAL |
| B | AS PER ECR-117976 | 11MAR24 | I.SANTOS |
| C | AS PER ECR-121828 | 12AUG24 | I.SANTOS |

# EVAL-AD3530R-ARDZ SCHEMATIC REV (A)

PAGE 2 - POWER CIRCUITRY
PAGE 3 - MAIN DEVICE CIRCUITRY
PAGE 4 - ARDUINO HEADER AND IO CIRCUITRY

| P.O SPEC. | BK/BD SPEC. | SOCKET OEM | OEM PART# | HANDLER |
|---|---|---|---|---|
| | | | | |
| | | | | |
| | | | | |
| | | | | |
| | | | | |
| | | | | |
| | | | | |

| | | |
|---|---|---|
| TEMPLATE ENGINEER - | DATE | SCHEMATIC |
| HARDWARE SERVICES M.VALE | 03APR23 | |
| HARDWARE SYSTEMS - | | |
| TEST ENGINEER - | | |
| COMPONENT ENGINEER N.CATALAN | 29JAN24 | |
| TEST PROCESS - | | |
| HARDWARE RELEASE K.JABATAN | 03APR23 | |
| DESIGNER N.CORDOVA | 03APR23 | |
| PTD ENGINEER I.ANDAL | 03APR23 | |
| CHECKER - | | |

HW TYPE : Customer Evaluation Z
Product(s): AD3530R
: EVAL-AD3530R-ARDZ
PACKAGE : LeadCount-lead BodySize PackageType-family
: Pitch-pitch StyleVendor Style

<User Define>
<User Define>
<User Define>

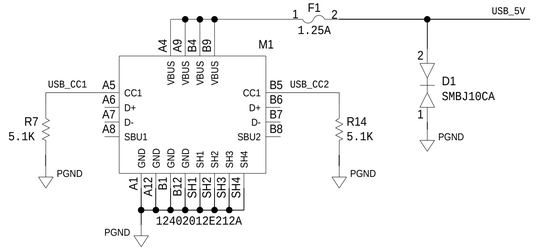
MASTER PROJECT TEMPLATE: TBD
TESTER TEMPLATE: 064489_C

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES
DECIMALS X.XX +-0.010 X.XXX +-0.005
FRACTIONS +-1/32
ANGLES +-2

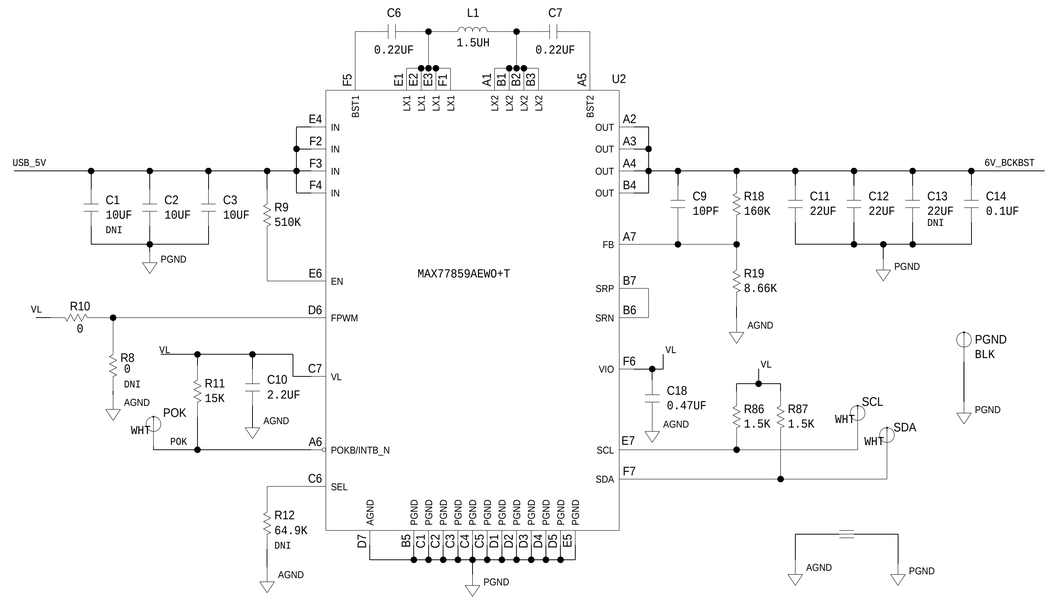
DRAWING NO. 02_071775
REV. C
SIZE D | SCALE 1:1 | CODE ID NO. CodeID | SHEET 1 OF 4

| REVISIONS | | | |
|---|---|---|---|
| REV | DESCRIPTION | DATE | APPROVED |
| | | | |

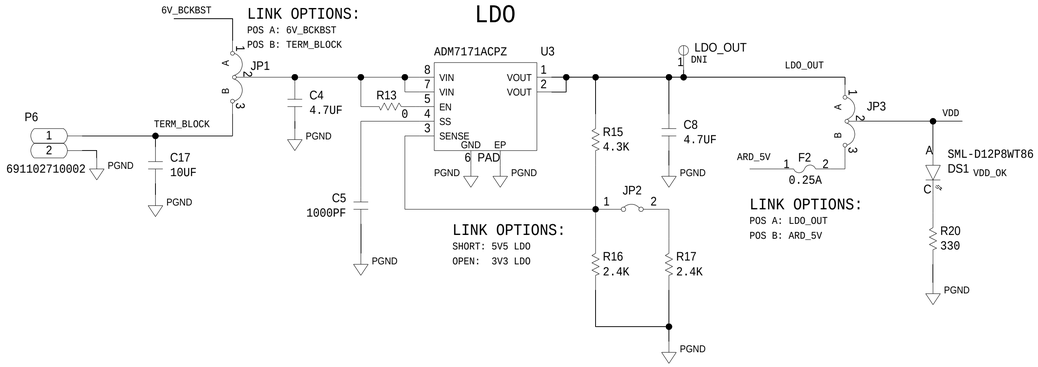
## USB-C CONNECTOR



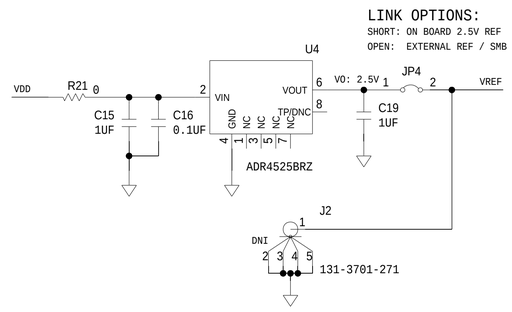

## BUCKBOOST



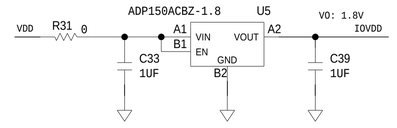

## LDO




## PRECISION REFERENCE




## IOVDD - LDO




DESIGN NOTE:
1.8V OUTPUT

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# SCHEMATIC

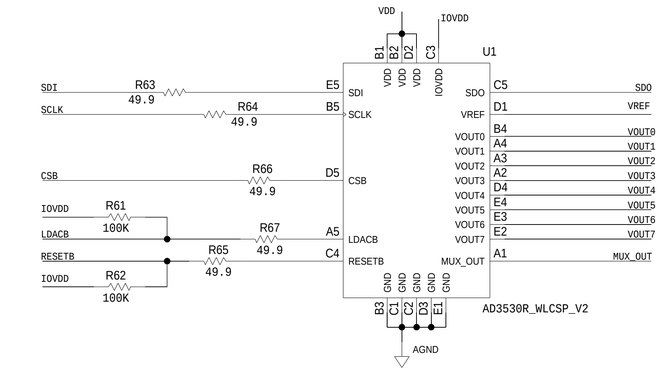
HW TYPE : Customer Evaluation Z
Product(s): AD3530R
: EVAL-AD3530R-ARDZ

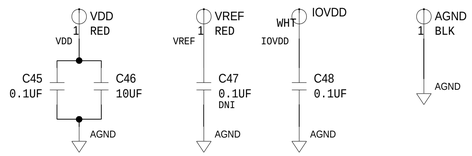
| DESIGN VIEW | DRAWING NO. | REV |
|---|---|---|
| <DESIGN_VIEW> | 02_071775 | C |

| PTD ENGINEER | SIZE | SCALE | SHEET |
|---|---|---|---|
| I.ANDAL | D | 1:1 | 2 OF 4 |

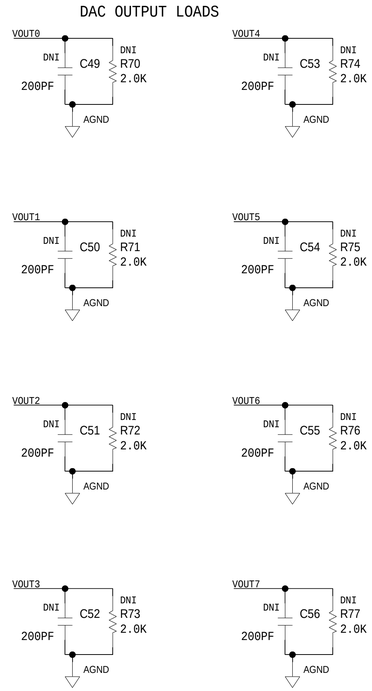
| REVISIONS | | | |
|---|---|---|---|
| REV | DESCRIPTION | DATE | APPROVED |
| | | | |

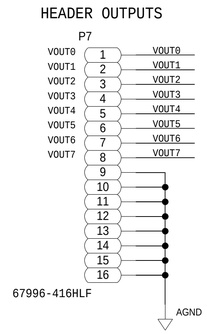
# MAIN CIRCUIT

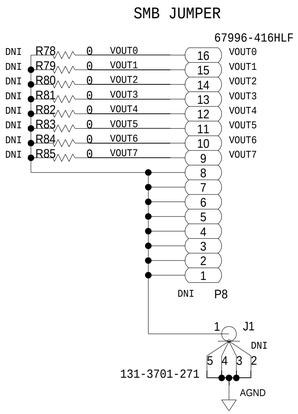



# DAC OUTPUT INTERFACE



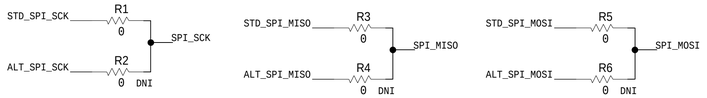




| | | | |
|---|---|---|---|
| ANALOG DEVICES | SCHEMATIC | | |
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| | DESIGN VIEW<br><DESIGN_VIEW> | DRAWING NO.<br>02_071775 | REV<br>C |
| | PTD ENGINEER<br>I.ANDAL | SIZE<br>D | SCALE<br>1:1 |
| | | SHEET 3 OF 4 | |

| REVISIONS | | | |
|---|---|---|---|
| REV | DESCRIPTION | DATE | APPROVED |
| | | | |

## ARDUINO HEADERS

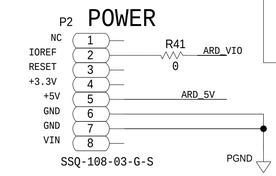


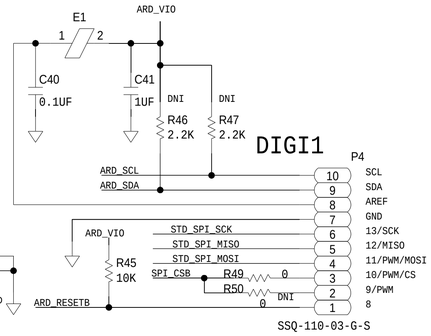




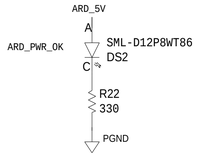


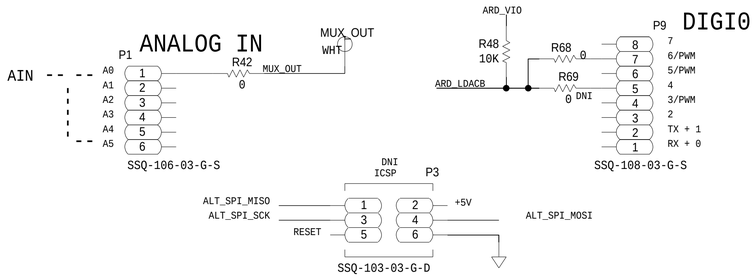

DESIGN NOTE:
SDPK1 IOREF SHOULD BE SET TO 3.3V



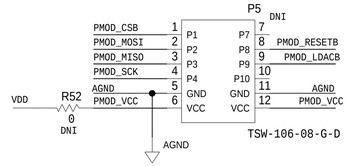




DESIGN NOTE:
AREF IS THE REFERENCE VOLTAGE FOR THE ANALOG LINES A0 TO A5.
AREF VOLTAGE LIMITS (SDP-K1) 1.7 <= AREF <= IOREF.



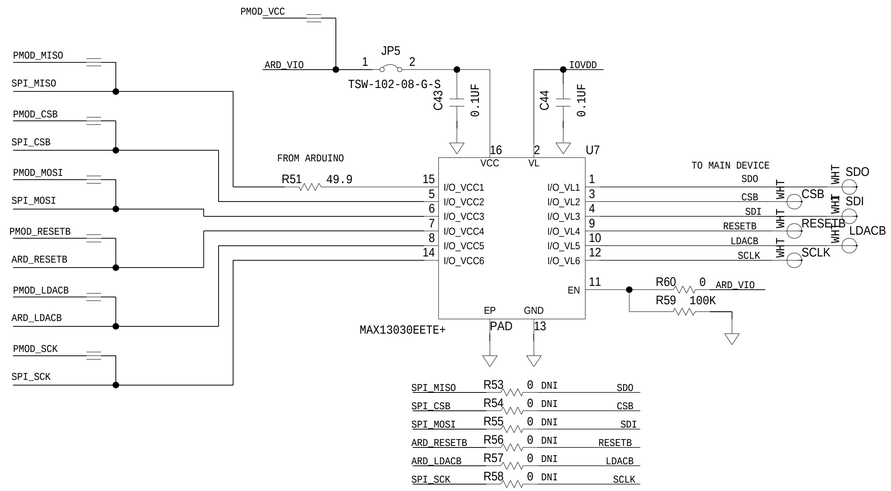




## DIGITAL I/F HEADERS

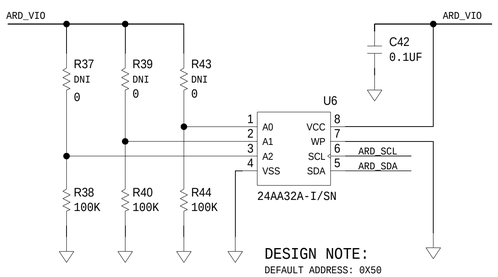



12-PIN SPI PMOD CONNECTOR

## LEVEL SHIFTER




## EEPROM - BOARD ID




DESIGN NOTE:
DEFAULT ADDRESS: 0X50

| | | | |
|---|---|---|---|
| ANALOG DEVICES | SCHEMATIC | | |
| | HW TYPE : Customer Evaluation Z<br>Product(s): AD3530R<br>: EVAL-AD3530R-ARDZ | | |
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| | PTD ENGINEER<br>I.ANDAL | SIZE<br>D | SCALE<br>1:1 |
| | | SHEET 4 OF 4 | |