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REVISIONS

REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	07APR22	R. WHITNEY
B	CHANGES AS PER ECR-109621	04OCT22	R. WHITNEY
C	CHANGES AS PER ECR-113566	11APR23	R. WHITNEY
D	CHANGES AS PER ECR-115079	08AUG23	R. WHITNEY

D

C

B

A

EVAL-AD2437A1MZ

RJ45 MAIN/LOCAL-POWERED SUB-NODE

P.O SPEC.	BK/BD SPEC.	SOCKET OEM	OEM PART#	HANDLER

TEMPLATE ENGINEER	DATE	SCHEMATIC								
HARDWARE SERVICES	07APR22	HW TYPE : Customer Evaluation Z Product(s): AD2437 : N/A PACKAGE : LeadCount-lead BodySize PackageType-family : Pitch-pitch StyleVendor Style <User Define> <User Define> <User Define>								
HARDWARE SERVICES										
HARDWARE SYSTEMS										
TEST ENGINEER										
TEST ENGINEER										
COMPONENT ENGINEER	07APR22	MASTER PROJECT TEMPLATE TBD								
COMPONENT ENGINEER										
TEST PROCESS										
HARDWARE RELEASE	07APR22	TESTER TEMPLATE no_template		DRAWING NO. 02_067221						
DESIGNER	07APR22	TOLERANCES			REV. D					
PTD ENGINEER	07APR22	DECIMALS X.XX +0.010 X.XXX +0.005		FRACTIONS +/-1/32	ANGLES +2	SIZE D	SCALE 1:1	CODE ID-NO CodeID	SHEET 1	OF 13
CHECKER										

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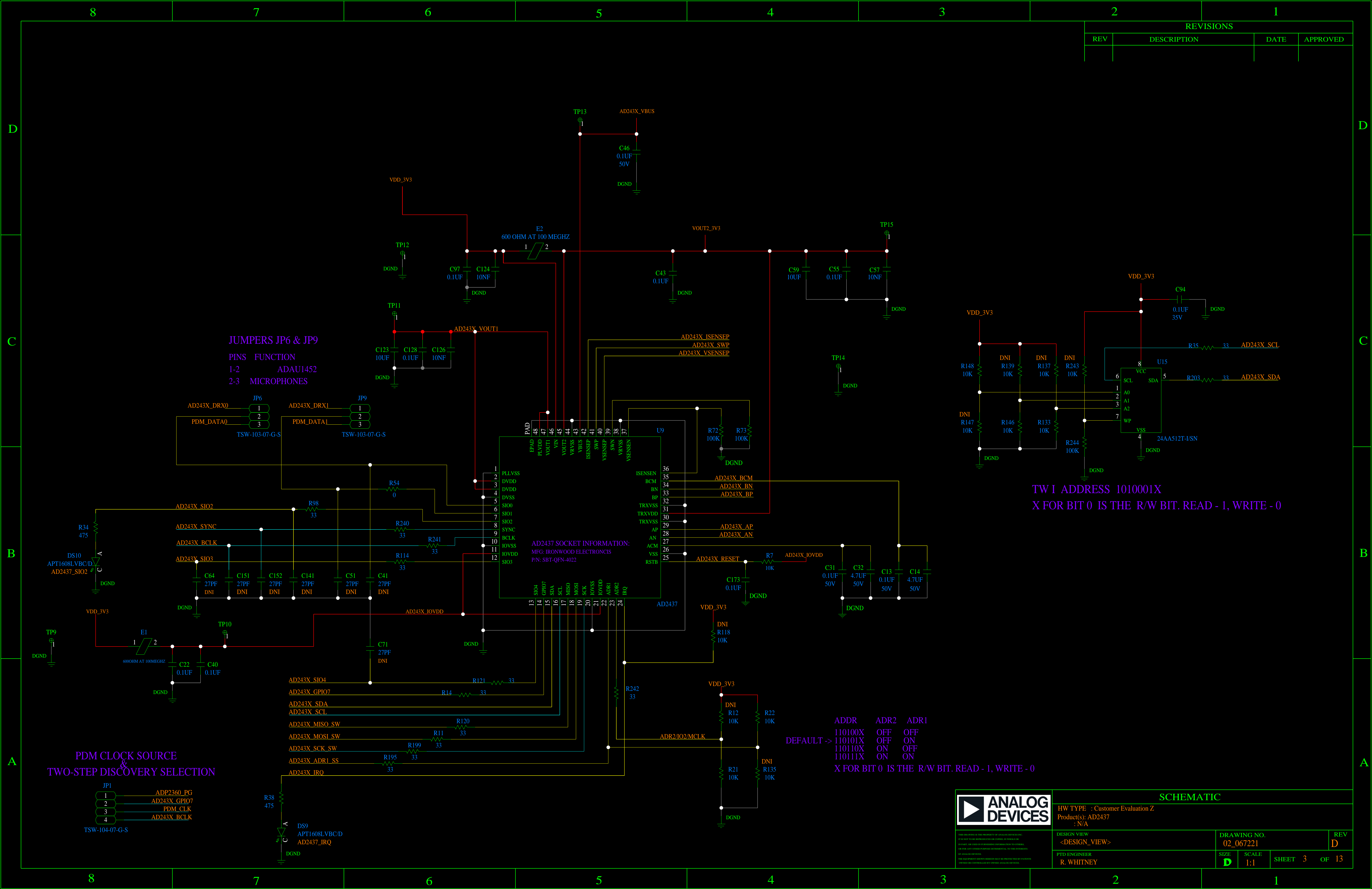
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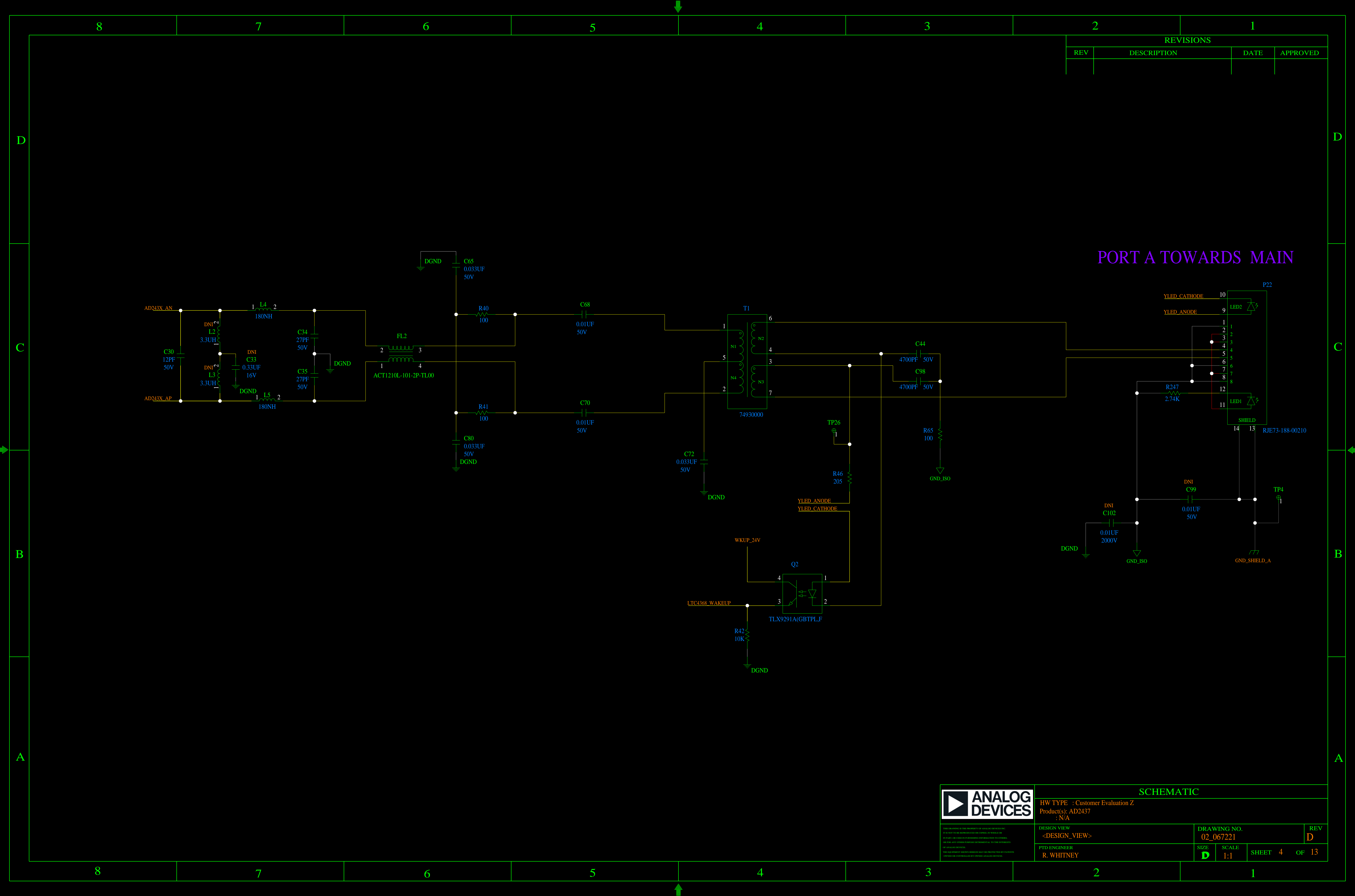
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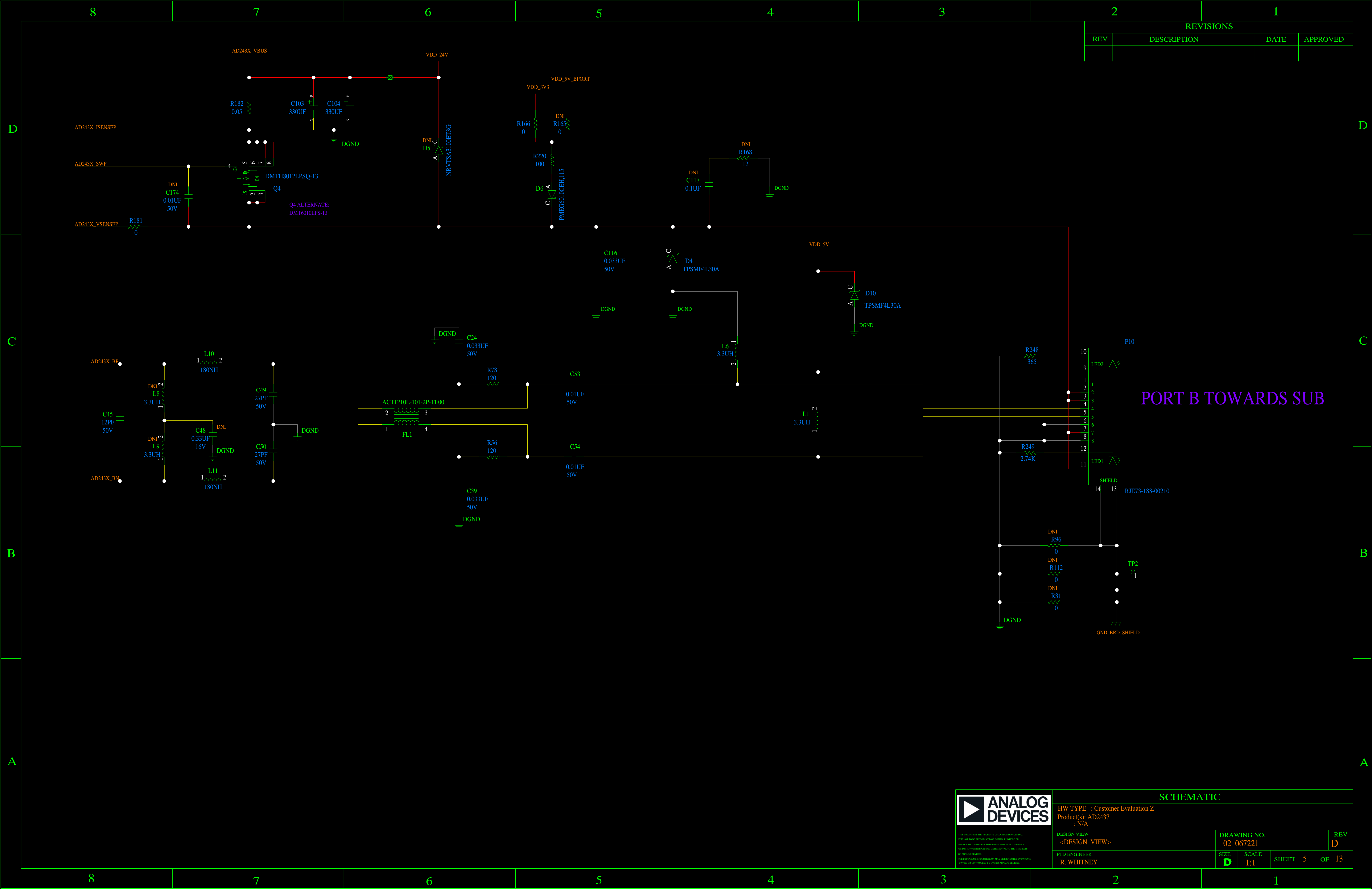
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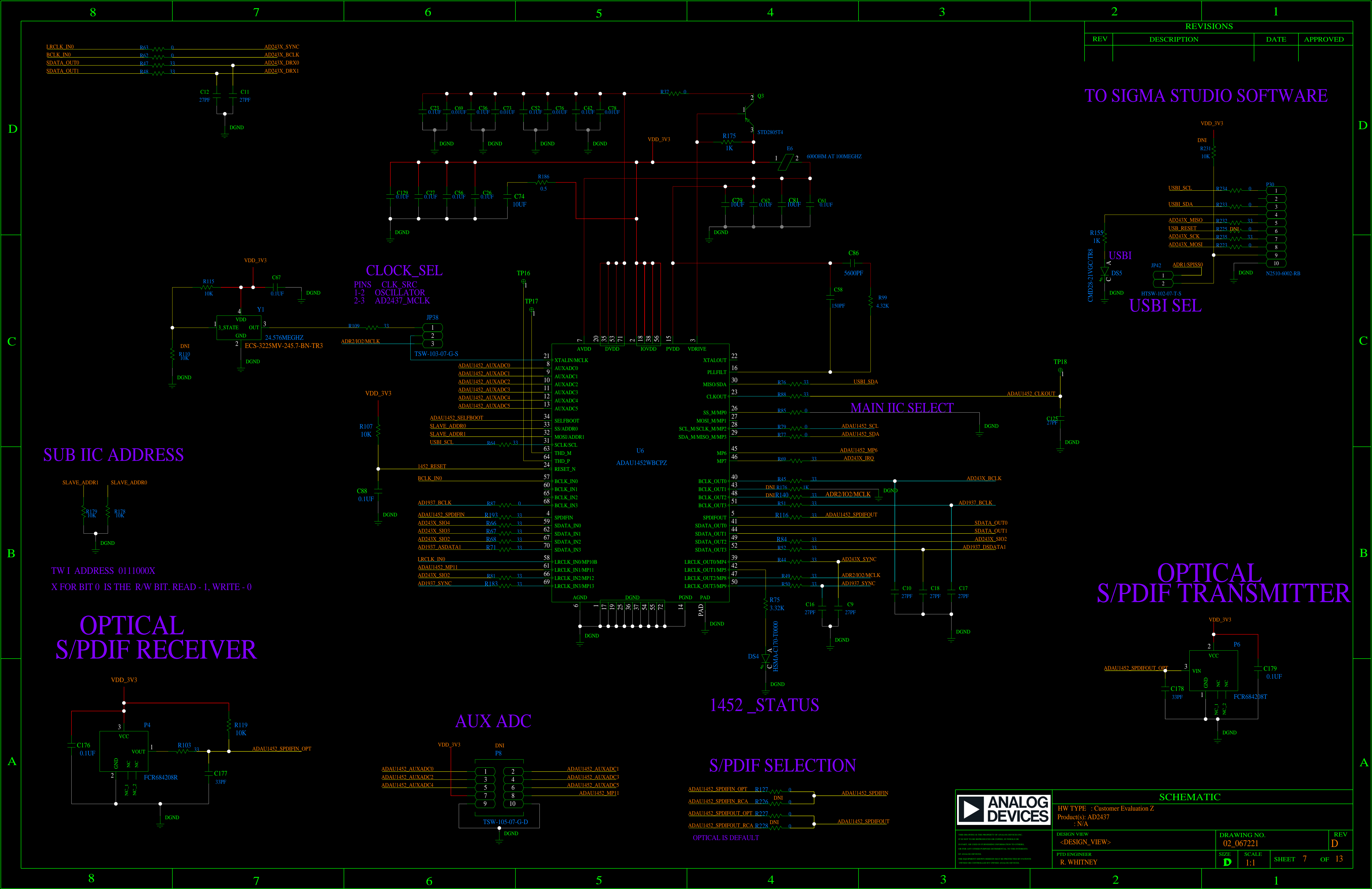




PORT A TOWARDS MAIN

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	HW TYPE : Customer Evaluation Z Product(s): AD2437 : N/A			
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_067221		REV D
	PTD ENGINEER R. WHITNEY	SIZE D	SCALE 1:1	SHEET 4 OF 13





REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



WP	Selfboot		I2C Bus Switch	Use Case and Action
JP2	JP3	ADAU1452_MP6	U26, U27	
OPEN	OPEN	X	CLOSED	Program/Parameter write from AD2437 or USBi to ADAU1452 or Boot EEPROM
CLOSED	OPEN	X	CLOSED	Program/Parameter write from AD2437 or USBi to ADAU1452
X	CLOSED	1 -> 0	OPEN -> CLOSED	Self Boot of ADAU1452 from Boot EEPROM
X	X	1	OPEN	Parameter Write from ADAU1452 to Boot EEPROM



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SCHEMATIC

HW TYPE : Customer Evaluation Z
Product(s): AD2437
: N/A

DESIGN VIEW	<DESIGN_VIEW>
PTD ENGINEER	R. WHITNEY

DRAWING NO.
02 067221

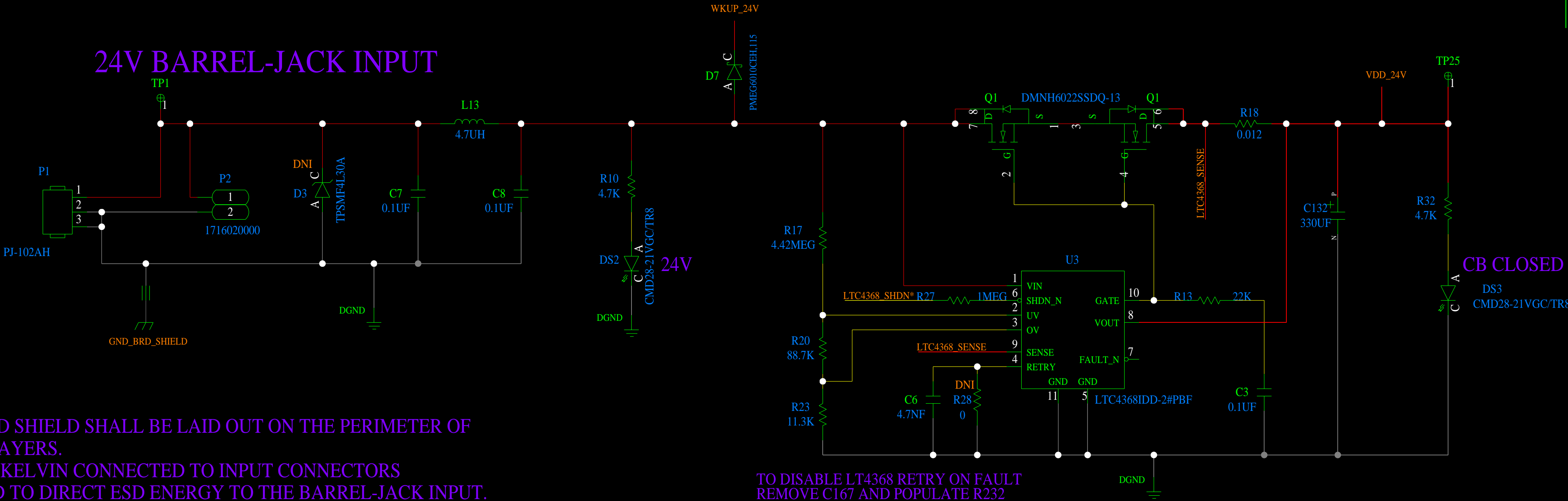
REV
D

SIZE	SCALE	SHEET 8 OF 13
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REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

24V BARREL-JACK INPUT

INPUT POWER SUPPLY:
MFG: KAGA ELECTRONICS USA
P/N: KTPS65-2430DT-3P-VI-C-P1
VNOM = 24V AT 72W



LTC4368 CIRCUIT BREAKER SETTINGS:

OV_LIM = 25.2V
UV_LIM = 22.8V
OC_LIM = 4.17A
OC_REV_LIM = -0.25A
I_INRUSH = 0.35A
RETRY_DELAY = 0.025 SEC

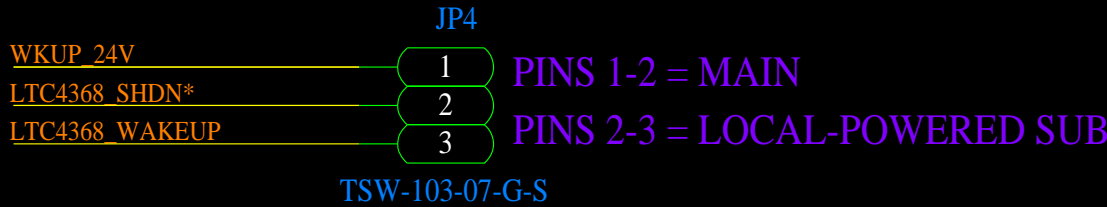
CB CLOSED

DS3
CMD28-21VGC/TR8

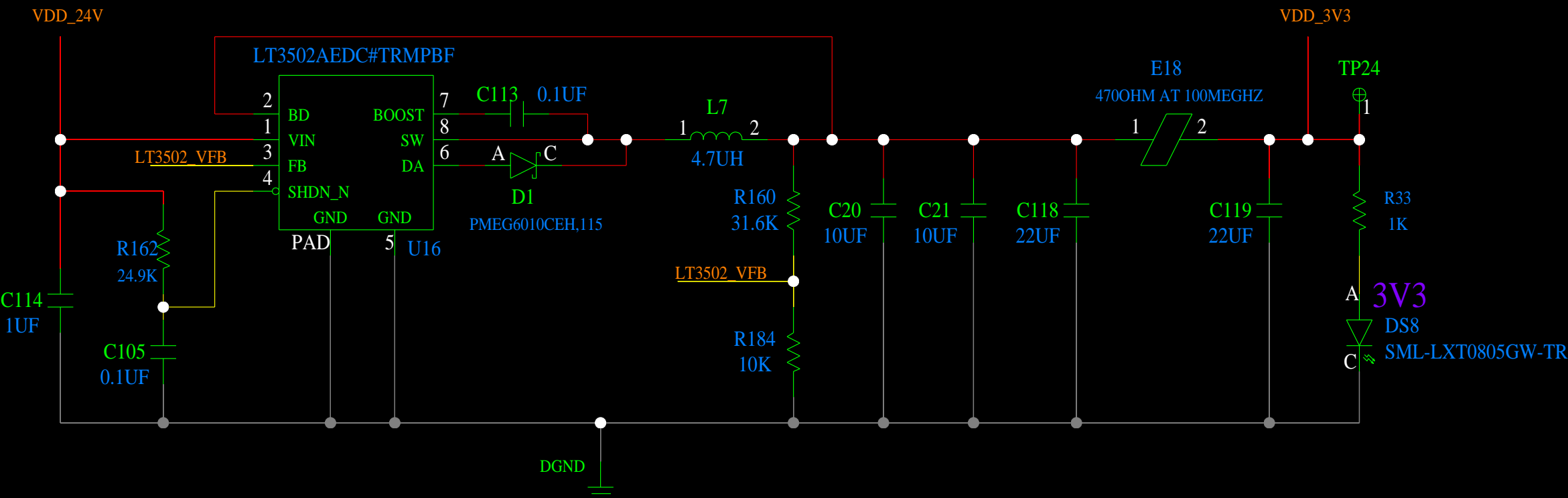
GND_BRD_SHIELD: A GROUND SHIELD SHALL BE LAID OUT ON THE PERIMETER OF THE BOARD THROUGH ALL LAYERS.
GND_BRD_SHIELD SHALL BE KELVIN CONNECTED TO INPUT CONNECTORS
NEEDING A CHASSIS GROUND TO DIRECT ESD ENERGY TO THE BARREL-JACK INPUT.

TO DISABLE LT4368 RETRY ON FAULT
REMOVE C167 AND POPULATE R232

LTC4368 WAKEUP

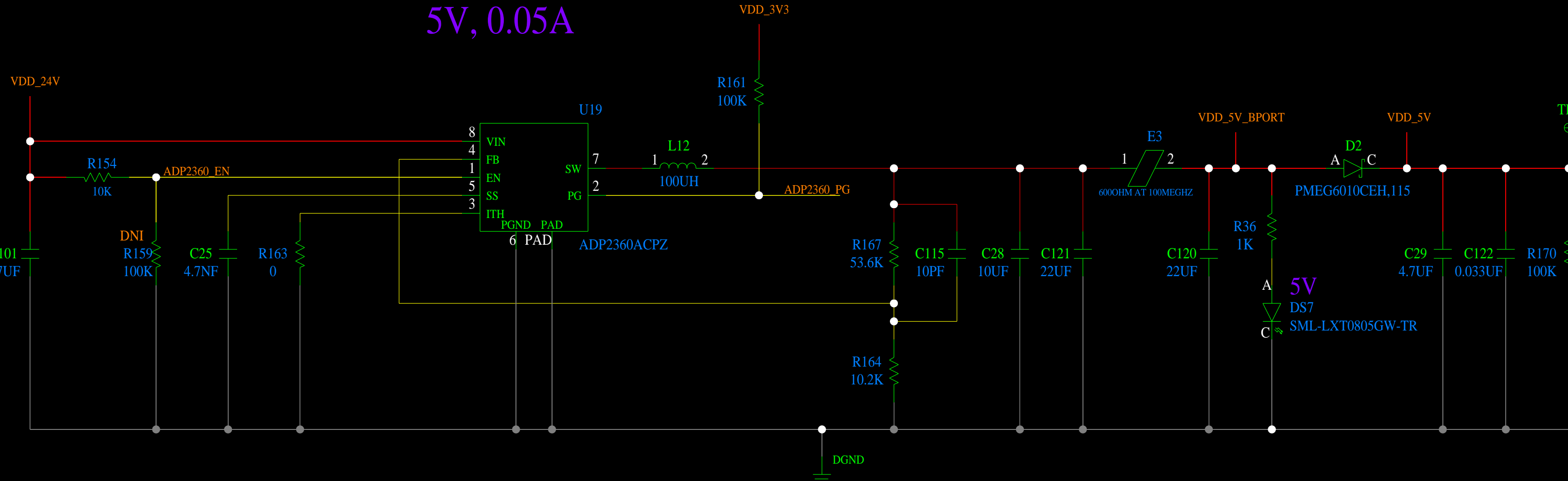


3.3V, 0.5A



REPLACE R182 (24.9K OHM) WITH A 0 OHM RESISTOR
TO DISABLE SOFT-START OF LT3502

5V, 0.05A



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SCHEMATIC			
HW TYPE : Customer Evaluation Z Product(s): AD2437 : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_067221	REV D	
PTD ENGINEER R. WHITNEY	SIZE D	SCALE 1:1	SHEET 13 OF 13