

DRILL CHART: TOP to BOTTOM				
ALL UNITS ARE IN MILS				
FIGURE	FINISHED SIZE	TOLERANCE DRILL	PLATED	QTY
X	9.8	+3.0/-3.0	PLATED	917
⊙	10.0	+3.0/-3.0	PLATED	414
⊗	40.0	+3.0/-3.0	PLATED	7
⊕	51.0	+3.0/-3.0	PLATED	2
△	59.0	+3.0/-3.0	PLATED	8
⊖	65.0	+3.0/-3.0	PLATED	12
⊕	195.0	+3.0/-3.0	PLATED	4
⊗	215.0	+3.0/-3.0	PLATED	2
⊖	70.0	+3.0/-3.0	NON-PLATED	2
⊕	187.0	+3.0/-3.0	NON-PLATED	4

NOTE: IC LTP88XX and DC3190A- *
WILL HAVE BOXES TO MARK WHICH VER.
REFER TO THE CORRESPONDING BOM

ADDITIONAL REQUIREMENT FOR PROTOTYPE FAB ONLY:

1. OUTGOING INSPECTION REPORT (BASED ON ACTUAL MEASUREMENTS AND CROSS SECTION).

ADDITIONAL REQUIREMENTS FOR PRODUCTION FAB ONLY:

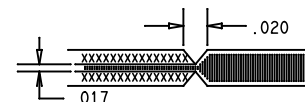
1. PROVIDE COMPLIANCE CERTIFICATES FOR RoHS, REACH AND CONFLICT-FREE MINERALS.
2. SOLDERABILITY BOARD WITH TEST RESULTS.
3. OUTGOING INSPECTION REPORT (BASED ON ACTUAL MEASUREMENTS AND CROSS SECTION).
4. VACUUM PACKED WITH DESICCANT.
5. FULL PANEL WITH NO REJECT

LAYER STRUCTURE

0.028"		LAYER 1- PRIMARY SIDE
0.005"		FR4
0.028"		LAYER 2 (GND)
0.005"		FR4
0.028"		LAYER 3 (ROUTE)
0.005"		FR4
0.028"		LAYER 4 (GND)
THICKNESS AS REQ'D		FR4
0.028"		LAYER 5 (GND)
0.005"		FR4
0.028"		LAYER 6 (ROUTE)
0.005"		FR4
0.028"		LAYER 7 (GND)
0.005"		FR4
0.028"		LAYER 8- SECONDARY SIDE

NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -EPOXY FIBERGLASS, FR-4, HIGH Tg180
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 8 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS
AND 2 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-O MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003"
IN RELATION TO CENTER
-FOR VIAS SIZE LESS THAN 0.012", VIAS HOLES NEED
TO BE PLUGGED AND COVERED WITH SOLDERMASK.
5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GLOSS GREEN
-GOLD IMMERSION BOTH SIDES.
(LEAD FREE SOLDER CAN BE USED FOR PROTOTYPE)
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB:



UNLESS OTHERWISE SPECIFIED			
DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		www.analog.com FOR ADI CUSTOMER USE ONLY	
TITLE: FABRICATION DRAWING 54V INPUT, HIGH CURRENT DC/DC POWER MODULE WITH PMBUS INTERFACE			
SIZE	IC NO.	LTP88XX DC3190A-*	REV 5
N/A			
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