

SIZE	QTY	SYM	PLATED	TOL
0.008	942	□	YES	+/-0.003
0.035	18	⊕ ^A	YES	+/-0.003
0.039	6	◇	YES	+/-0.003
0.067	8	⊕ ^B	YES	+/-0.003
0.07	2	⊗	NO	+/-0.003
0.095	8	⊗	YES	+/-0.003
0.187	4	⊕	YES	+/-0.003
0.205	8	⊗	YES	+/-0.003

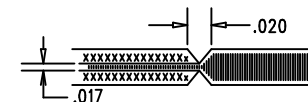
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	1	PROTOTYPE	DB	08-11-20



LAYER STRUCTURE

.0028		LAYER 1- TOP
.004		PREPREG
.0028		LAYER 2- GND PLANE
.006		CORE MATERIAL
.0028		LAYER 3- SIGNALS
.025		CORE MATERIAL
.0028		LAYER 4- GND PLANE
.006		CORE MATERIAL
.0028		LAYER 5- PWR PLANE
.004		PREPREG
.0028		LAYER 6- BOTTOM

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
-FINISHED THICKNESS TO BE 0.062 +/- .005
-TOTAL OF 6 LAYERS WITH 2 OZ. CU ALL LAYERS
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0/0 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



UNLESS OTHERWISE SPECIFIED		APPROVALS		  <small>www.analog.com FOR ADI CUSTOMER USE ONLY</small>	
DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX = ± 0.01 0.XXX = ± 0.005 INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		PCB DES.	LT		
		APP ENG.	DB	TITLE: FABRICATION DRAWING	
				LOW IQ, DUAL, 100% DUTY CYCLE STEP-DOWN SUPPLY	
				SIZE N/A	IC NO. LTC7805UFDM DC3138A
				SCALE = NONE	REV 1
				FILENAME: DC3138A-1.PCB	SHT 1 OF 1