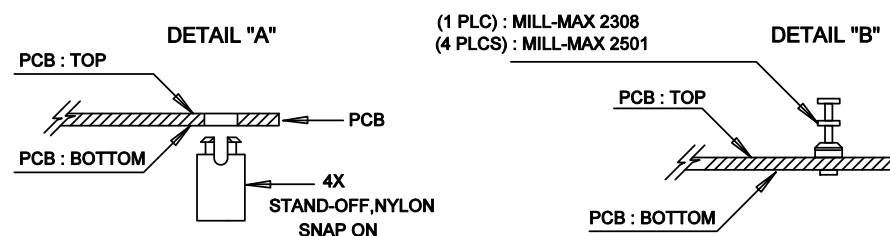
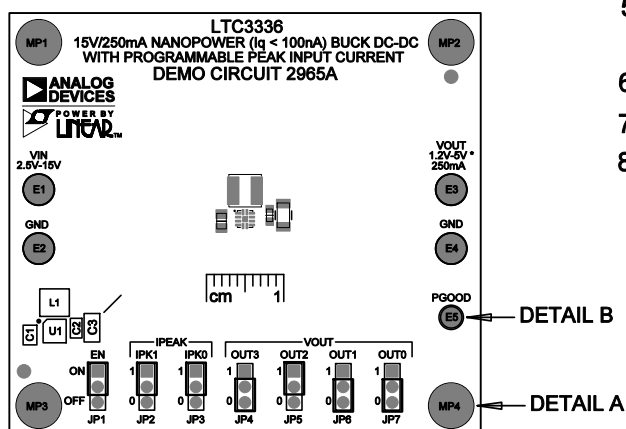


REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	01	PRODUCTION	ZP	10-22-20

## NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY REFLOW PROFILE SHALL BE IN ACCORDANCE WITH J-STD-020 WITH MAXIMUM SOLDER TEMPERATURE OF 250 DEGREES CELSIUS.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS  
LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER.  
MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. INSTALL SHUNTS AS SHOWN ON ASSY DRAWING.
5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
6. APPLY ASSEMBLY STAMP OR QA STAMP TO BOTTOM OF BOARD (UNSHOWY AREA).
7. INSTALL TURRETS, STAND-OFFS AS SHOWN BELOW:
8. APPLY DEMO S/N AT AREA ON BOTTOM SIDE AS SHOWN ON SHEET 2.



APPROVALS		ANALOG DEVICES   POWER BY LINEAR™ FOR ADI CUSTOMER USE ONLY		
PCB DES	NC	TITLE: TOP ASSEMBLY DRAWING 15V/250mA NANOPOWER (Iq < 100nA) BUCK DC-DC WITH PROGRAMMABLE PEAK INPUT CURRENT		
APP ENG	ZP			
		SIZE	IC NO. LTC3336	REV.
		N/A	DEMO CIRCUIT 2965A	01
SCALE = NONE		SHT 1 OF 1		

FOR ADI CUSTOMER USE ONLY



\*

OUT3	OUT2	OUT1	OUT0	VOUT
0	0	0	0	1.8V
0	0	0	1	1.2V
0	0	1	0	1.8V
0	0	1	1	1.5V
0	1	0	0	2.5V
0	1	0	1	2.4V
0	1	1	0	2.0V
0	1	1	1	2.8V
1	0	0	0	3.0V
1	0	0	1	3.3V
1	0	1	0	3.6V
1	0	1	1	3.7V
1	1	0	0	3.2V
1	1	0	1	4.1V
1	1	1	0	4.2V
1	1	1	1	5.0V

IPK1	IPK0	IPEAK
0	0	10mA
0	1	30mA
1	0	100mA
1	1	300mA

REV 01      WWW.ANALOG.COM

8 DEMO S/N LABEL  
APPLY IN THIS AREA

APPROVALS		  FOR ADI CUSTOMER USE ONLY		
PCB DES.	NC	TITLE: BOTTOM ASSEMBLY DRAWING 15V/250mA NANOPower (I <sub>q</sub> < 100nA) BUCK DC-DC WITH PROGRAMMABLE PEAK INPUT CURRENT		
APP ENG.	ZP			
		SIZE N/A	IC NO. LTC3336 DEMO CIRCUIT 2965A	REV 01
SCALE = NONE		SHT 2 of 2		