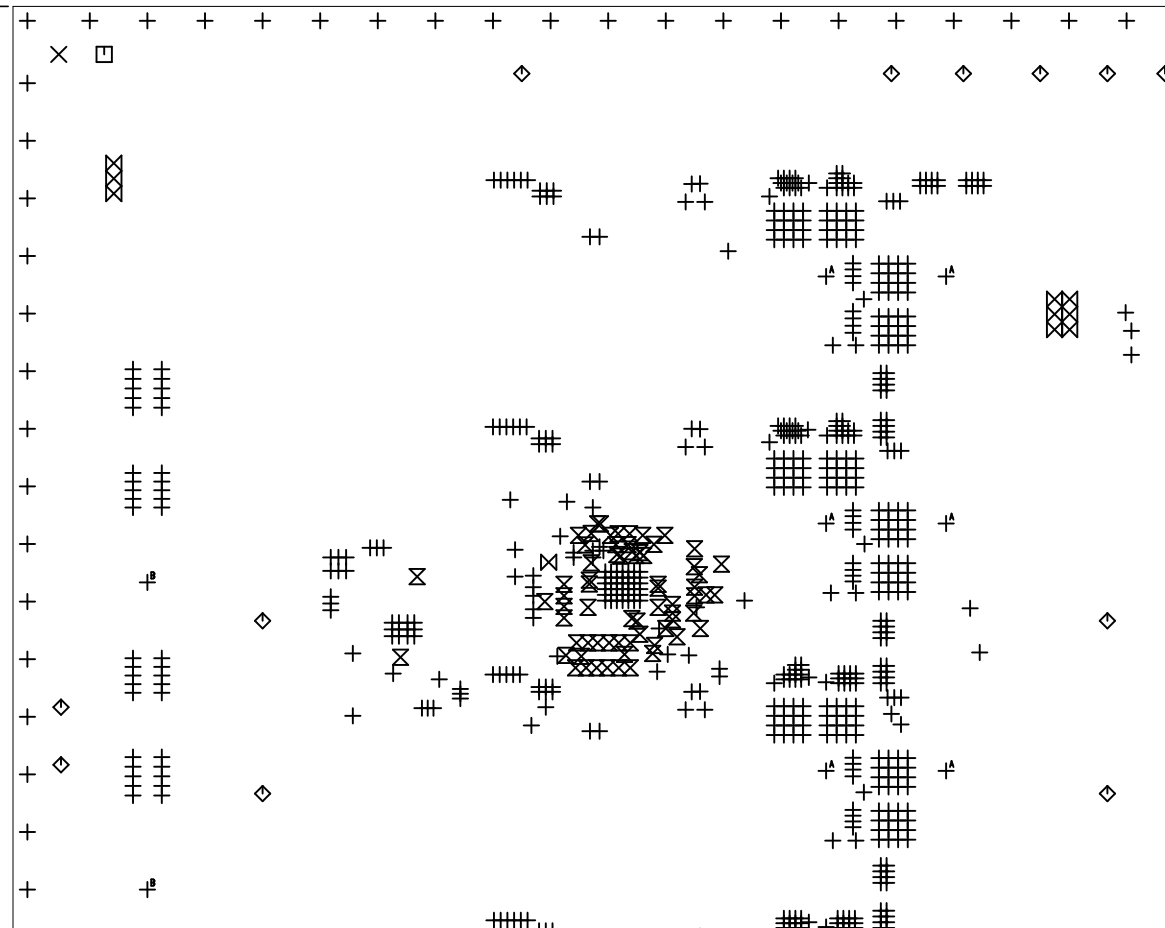
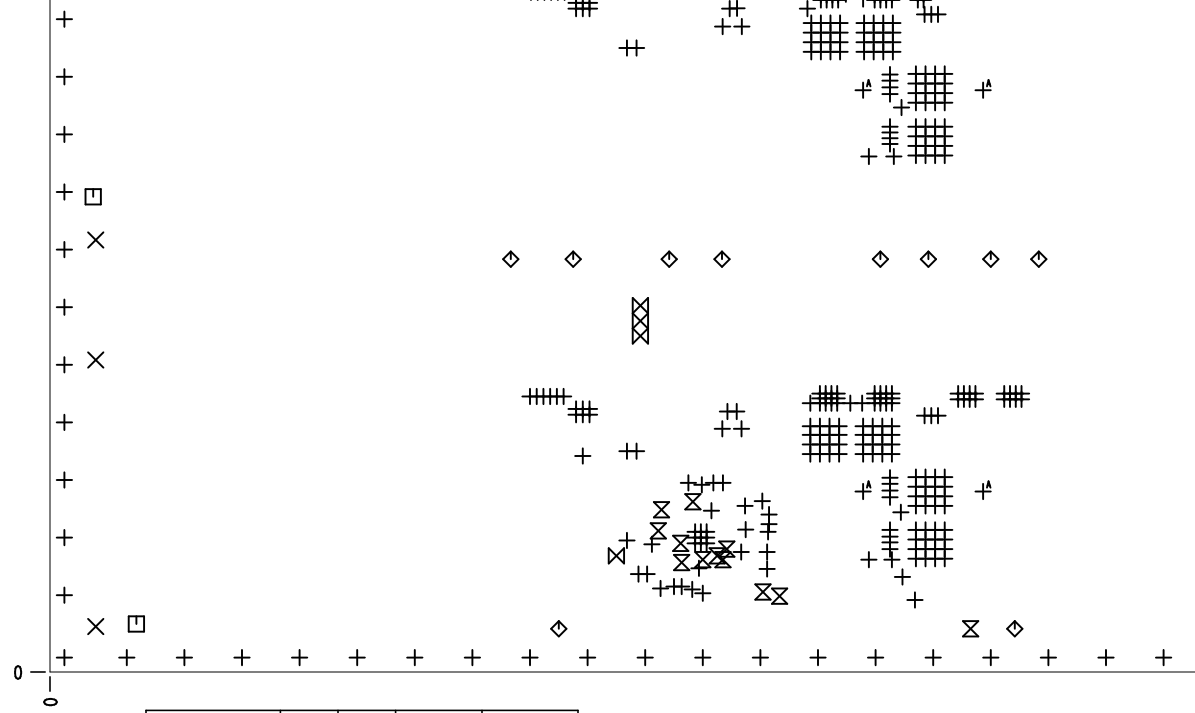
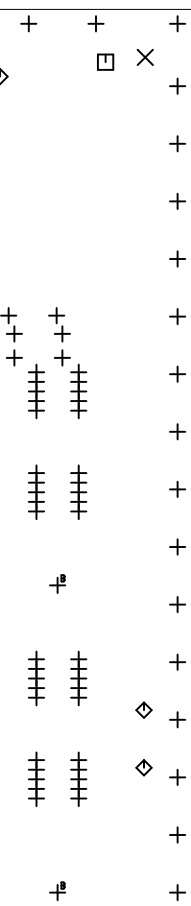


8.30



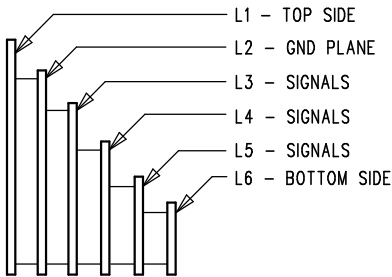


SIZE	QTY	SYM	PLATED	TOL
0.006	80	⊗	YES	+/-0.003
0.01	1026	+	YES	+/-0.003
0.035	14	⊗	YES	+/-0.003
0.065	24	◇	YES	+/-0.003
0.07	4	□	NO	+/-0.003
0.15	10	+ <sup>A</sup>	YES	+/-0.003
0.187	8	⊗ <sup>B</sup>	NO	+/-0.003
0.195	4	+ <sup>B</sup>	YES	+/-0.003



REVISION HISTORY				
ECO	REV	DESCRIPTION	APPR	DATE
-	3	PRODUCTION	VICTOR	01-15-20

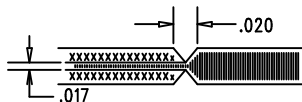
LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

- 1. FAB PER IPC-A-600.
- 2. MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4  
-FINISHED THICKNESS TO BE 0.062 +/- .005  
-TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS  
AND 1 OZ. CU ON THE INNER LAYERS.  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- 3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0.00" ARE PRIMARY DATUMS.
- 4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH  
HOLES WITH COPPER, 0.001 THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.003  
IN RELATION TO CENTER  
-FOR VIAS SIZE > 0.01, VIAS HOLES NEED TO BE PLUGGED

- AND COVERED WITH SOLDERMASK.
5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GLOSS GREEN.  
-GOLD IMMERSION BOTH SIDES.  
(LEAD FREE SOLDER CAN BE USED FOR PROTOTYPE)  
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
  6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
  7. PCBS ARE TO BE RoHS COMPLIANT.
  8. SCORING FOR PANELIZED PCB:





ADDITIONAL REQUIREMENT FOR PROTOTYPE FAB ONLY:

1. OUTGOING INSPECTION REPORT (BASED ON ACTUAL MEASUREMENTS AND CROSS SECTION).

ADDITIONAL REQUIREMENTS FOR PRODUCTION FAB ONLY:

1. PROVIDE COMPLIANCE CERTIFICATES FOR RoHS, REACH AND CONFLICT-FREE MINERALS.
2. SOLDERABILITY BOARD WITH TEST RESULTS.
3. OUTGOING INSPECTION REPORT (BASED ON ACTUAL MEASUREMENTS AND CROSS SECTION).
4. VACUUM PACKED WITH DESICCANT.
5. FULL PANEL WITH NO REJECT.

UNLESS OTHERWISE SPECIFIED		
DIMENSIONS ARE IN INCHES		
TOLERANCES:		
0.XX = $\pm 0.01$		
0.XXX = $\pm 0.005$		
INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		
  <a href="http://www.analog.com">www.analog.com</a>		
FOR ADI CUSTOMER USE ONLY		
TITLE: FABRICATION DRAWING		
MULTIPHASE BOOST CONVERTER EXPANDER WITH INTERNAL GATE DRIVERS		
SIZE N/A	IC NO. LT8561EUKG + LT8551EUKG DC2896A(A,B)	REV 3
FILENAME: DC2896A-3.PCB		SHT 1 OF 1