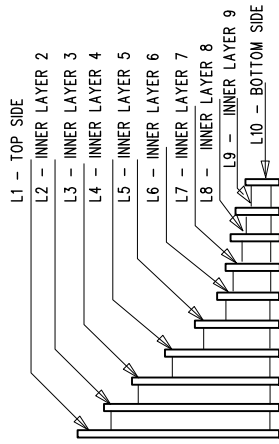
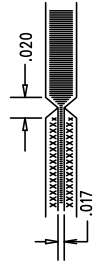


LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

- 1. FAB PER IPC-A-600.
- 2. MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062 +/- .005
-TOTAL OF 10 LAYERS WITH 2 OZ. CU ON TOP AND BOTTOM LAYERS
1 OZ. CU ON INNER LAYERS
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- 3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
- 4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- 5. FINISH: -SMOBC USING LPI BOTH SIDES.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKScreens: USE WHITE NON-CONDUCTIVE INK.
- 6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- 7. DESIGN HAS SOLDER MASK DEFINED PADS ON U1.
U1 SOLDER MASK SIZE IS 0.016 INCH AND PAD SIZE IS 0.020 INCH.
DON'T CHANGE PAD SIZE OR SOLDER MASK SIZE.
- 8. PCBs ARE TO BE RoHS COMPLIANT.
- 9. PLUG ALL VIAS WITH EPOXY.
AND PLATE OVER. PLANARIZE FINAL SURFACE.
- 10. SCORING FOR PANELIZED PCB:



UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES:
0.XX = ± 0.01
0.XXX = ± 0.005
INTERPRET DIM AND TOL
PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION

APPROVALS

PCB DES.	HZ
APP ENG.	GL

ANALOG DEVICES

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TITLE: FABRICATION DRAWING

QUAD OUTPUT DC/DC PSN UMODULE REGULATOR

SIZE	IC NO. LTM4673Y	REV
N/A	DEMO CIRCUIT 2810A	2

SCALE = NONE

FILENAME: DC2810A-2.PCB

SHT 1 OF 1

SIZE	QTY	SYM	PLATED	TOL
0.187	4	+	YES	+/-0.07
0.065	54	X	YES	+/-0.07
0.03543	58	□	YES	+/-0.07
0.01	809	◇	YES	+/-0.07
0.05512	30	⊗	YES	+/-0.07
0.035	34	⊗	YES	+/-0.003
0.205	6	† ^A	YES	+/-0.07
0.006	357	† ^B	YES	+/-0.07
0.09528	4	† ^C	YES	+/-0.07
0.037	20	† ^D	YES	+/-0.07